Journal of
Low Power Electronics and Applications

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Circuits and Systems Advances in Near Threshold Computing

Guest Editor:

Prof. Dr. Sanghamitra Roy
Electrical and Computer Engineering, Utah State University, Logan, UT 84322-4120, USA

Deadline for manuscript submissions:
closed (30 September 2020)

Message from the Guest Editor

As a broad spectrum of applications shift to the edge of the network, near-threshold computing (NTC) is emerging as one of the promising low-power computing platforms. Despite showing a substantial promise of energy efficiency, NTC (gray silicon) operation is yet to see a wide-scale commercial adoption.

The readers of this Special Issue will be able to familiarize themselves with the recent advances in electronics systems focusing on near-threshold computing.

- Design automation/algorithms for NTC circuits and systems;
- Architectural techniques for NTC;
- Reliability or variation aware design of NTC circuits and systems;
- Design of deep learning hardware for the edge at NTC;
- Security vulnerabilities at NTC and countermeasures;
- Recovering performance at NTC operation;
- Design techniques for hybrid STC and NTC architectures.

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Editor-in-Chief

Prof. Dr. Andrea Acquaviva
Department of Electrical, Electronic, and Information Engineering "Guglielmo Marconi", University of Bologna, 33 - 40126 Bologna, Italy

Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications (ISSN 2079-9268) is an open access journal which provides an advanced forum for the studies of electronics for low power applications. A special emphasize is made on ultralow power bio-medical applications. It publishes reviews, regular research papers and short communications.

Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. There is no restriction on the length of the papers. Full experimental and/or methodical details must be provided.

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Journal of Low Power Electronics and Applications Editorial Office
MDPI, St. Alban-Anlage 66
4052 Basel, Switzerland

Tel: +41 61 683 77 34
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mdpi.com/journal/jlpea
jlpea@mdpi.com
𝕏@JLPEA_MDPI