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# Validation & Verification of Intelligent Systems: The Case of Digital Twins

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## **Message from the Guest Editors**

In intelligent systems, the code written is traditionally responsible for developing the program that solves the problem. Consequently, testing the resulting systems becomes intractable due to lacking requirements, associated specifications, and eventually insight into how the solution was established and thus exposes specific and in the worst case erroneous—behavior. Digital twins offer the potential to replicate internal system processes and behaviors by recreating the conditions as they happen and thus provide tractability. Near real-time data can be sequenced, and the system under test can be tested with production data and production triggers.

Any failure that happens in production can be simulated in the digital twin, thus significantly improving application testing accuracy. The goal of this Special Issue is to revise the verification and validation of intelligent systems with a special focus on the benefits delivered by the digital twin.









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## Message from the Editor-in-Chief

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