Low-Power Techniques for Embedded Systems and Network-on-Chip Architectures

Guest Editor:

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Deadline for manuscript submissions:
closed (31 July 2020)

Message from the Guest Editor

This Special Issue will explore emerging approaches, ideas, and contributions to address the challenges in the design of energy efficient computational-centric embedded systems and on-chip networks.

Potential topics include, but are not limited to:

- Approximate computing for energy-efficient applications
- Novel architectures for embedded low power computing
- Communication infrastructures for energy efficient IoT environments
- Energy efficient neural networks
- Energy-aware parallel architectures for high-performance computing
- Design Platforms and Tools for optimizing energy/performance trade-offs

Click here to find more information!

Welcome to contribute!

Special Issue

Editor-in-Chief

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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