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Computer Architecture and Parallel Computing

Guest Editor:

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Deadline for manuscript submissions:

closed (15 May 2024)

Message from the Guest Editor

Dear Colleagues,

In the face of escalating computing demands, particularly in the era of large language model (LLM)-based applications, the necessity for efficient and scalable solutions has grown. These solutions must effectively leverage parallelism and optimized architectures. To ensure dependable and sustainable computational support, it is imperative to encompass key innovations in new hardware designs for high-performance computing architectures. Equally vital are the software techniques that unlock their complete potential, such as parallel applications and algorithms, performance assessment and modeling, as well as power and thermal management.

We encourage submissions covering a wide range of topics related to Computer Architecture and Parallel Computing, including but not limited to:

- 1. Novel Processor Architectures;
- 2. Memory Hierarchy and Storage Systems;
- 3. Parallel Programming Models and Languages;
- 4. Multi-core and Many-core Systems;
- 5. Parallel Algorithms and Applications;
- 6. Performance Evaluation and Benchmarking;
- 7. Power and Thermal Management;
- 8. Emerging Technologies.











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Editor-in-Chief

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Message from the Editor-in-Chief

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