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Design of Low-Voltage and Low-Power Integrated Circuits

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Message from the Guest Editors

Dear Colleagues,

This Special Issue focuses on advancing the research of low-voltage and low-power integrated circuits. This area of research is becoming increasingly important in several industries: from the medical/biomedical industries, where we rely on battery-powered devices and their respective management systems.

The scope of this Special Issue includes analogue and digital circuits focusing on improving accuracy, reliability and the signal-to-noise ratio, while operating at low power. The purpose of this Special Issue is to consolidate the state-of-the-art research in low-voltage and low-power integrated designs. Below are the following topics to be covered in this Special Issue:

- Novel designs of low-voltage and low-power integrated analogue and digital circuit designs;
- Analogue-to-digital converters;
- Digital-to-analogue converters;
- Low-voltage and low-power circuits for IoT applications;
- Low-voltage circuit designs for battery management systems (BMS);
- Low-voltage and low-power circuit designs for biomedical and medical applications;
- Low-power operational amplifier circuits;
- Low-power bandgap reference circuits.

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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