Compiler and Hardware Design Systems for High-Performance Computing

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Message from the Guest Editors

Compilers and computer architecture are both critical components of modern computing systems because their significance lies in their essential roles in making software and hardware work together efficiently and effectively. Compilers enable developers to write code in higher-level languages and convert it into machine code, making programming more accessible and efficient.Therefore, compilers and computer architecture both play a critical role in enhancing modern computing technology given that high-performance parallel architectures are exploited to solve many critical challenges in a variety of areas.

In this Special Issue, original research articles and reviews are welcome. Research areas may include (but are not limited to) the following:

- High-performance computer architectures;
- IoT, mobile, Edge, and embedded architectures;
- Compilers, runtimes, and programming languages for parallel computer systems;
- Compilers and programming languages for novel architectures;
- Heterogeneous computing accelerators;
- Machine learning compilers and runtime;
- Programming languages for machine learning;
- Specialized hardware for machine learning.

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Message from the Editor-in-Chief

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