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# 5G/6G IoT: Smart RF Link Integrated Circuit System Design

Guest Editor:

### Message from the Guest Editor

**Prof. Dr. Gyung-Su Byun** College of Engineering, Inha University, Incheon 22212, Korea

Deadline for manuscript submissions: closed (30 June 2021) Dear Colleagues,

5G/6G heterogeneous computing and communication systems will be one of the most important technologies for autonomous vehicles and smart mobility systems. To enable those systems, innovative 5G/6G RF IC and systemon-chip (SoC), and application-specific integrated circuits (ASIC) designs will be needed to improve far beyond mobile communication links by high-performance RF integrated transceivers, transmitters, and receivers design.

This Special Issue focuses on the analysis, design, and implementation of RF/analog integrated circuit design for energy-efficient wireless/wireline communication, RF/THz electronics for sensing/imaging, biomedical applications, etc.

- 5G/6G heterogeneous computing and communication system
- High-performance RF integrated transceivers
- High-performance transmitters and receivers
- Analog integrated circuit design for wireless/wireline communication
- RF/THz electronics for sensing/imaging
- biomedical applications.

Prof. Dr. Gyung-Su Byun *Guest Editor* 









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### Message from the Editor-in-Chief

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