

Article



# Optimized Design and Simulation of Optical Section in Electro-Reflective Modulators Based on Photonic Crystals Integrated with Multi-Quantum-Well Structures

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**Abstract:** In the design of photonic integrated circuits (PICs), the optical connections of the PIC surface, along with the electronic components of the chips, are significant issues. One of the optoelectronics components that utilizes these surface connections are electro-reflective modulators, consisting of an optical section and an electronic section. In this paper, a novel scheme of two-dimensional photonic crystals (PhCs) is presented for the optical and reflective sections of this device. This design is two-dimensional; thus, it has less volume than the current bulky structures. The finite element method is utilized to simulate and optimize the scheme of PhCs and gold layer parameters. Furthermore, optimization of design parameters is accomplished through the Nelder–Mead method. Moreover, the modeling and simulation of the proposed hybrid PhCs has been investigated according to the structural parameters with tolerance. These tolerances, related to the nanorods' radius and lattice constants, are considered to justify and vindicate the fabrication technology limitations and conditions. In the "on" state of the modulator, the light transmission ratio is 98% for a 903 nm wavelength with a 45° angle of deflection and incident light, nd the bandwidth is 20 nm. For an 897 nm wavelength with a 41° angle, the transmission ratio is 95%, and the bandwidth is 7 nm.

**Keywords:** electro-reflective modulators (ERMs); optical integrated circuits (OICs); optical surface waves; optical modulation; optoelectronics; photonic crystals (PhCs); photonic integrated circuits (PICs); two-dimensional design

## 1. Introduction

Several schematic designs and applications are shown for the surface optical connections to electronic circuits of integrated chips in Figure 1a, including the coupling of two internal parts of an optical integrated circuit (OIC) and the linking of two segregated OICs. One of the candidate technologies for these connections is silicon photonics (SiP), which uses CMOS processes to connect electronic chips through multi-level integration [1,2]. Recently, a seamless hybrid-integrated interconnected network architecture has been proposed that employs an "optical bridge" platform comprising flexible polymer ribbon waveguide arrays integrated with three to five active devices for the optical interconnections between Si CMOS chips co-packaged with the optical components.

As an alternative to planar waveguide integration of photonic components in SiP, the optical bridge integrates compact and efficient surface-incident active photonic components with single-mode flexible polymer waveguides through freeform optical couplers [3]. Many optoelectronic devices use a surface-incident configuration to facilitate coupling with light emitted to or from free space [4]. Examples of such devices include vertical cavity surface-emitting lasers, multi-quantum-well (MQW) modulators, and free-space photodetectors [3].



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**Copyright:** © 2023 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). In the multimode regime, flat and curved mirrors, prism couplers, and facet couplers have been implemented to couple light from or to these surface-incident devices [5].

These connections are made through optical reflectors from the chip surface to electronic components such as optical detectors or laser active sources of chips. Direct coupling of light from an optical waveguide on a chip to a second waveguide on another chip is a useful and effective function for interfacing photonic chips or coupling light from chips to optical printed circuit boards (PCBs) [6–11].

Such interconnection has been traditionally performed with 45° mirrors [12–15] or grating couplers [16]. The surface reflectors act to bend light based on the difference between the refractive index of waveguide and air; therefore, they can direct light to another surface at any angle. These reflectors are initially considered flat; however, after shape optimization, they are parabola shaped . The three-dimensional parabola that focuses light at its focal point is created by the process of making two-photon polymers (TPP); however, recent advances in parallel TPP printing promise significant advances in the production of 3D free-form reflectors [17,18]. Instances of photonic structures utilizing TPP include the fabrication of microneedle arrays [19], three-dimensional photonic crystals [20–23], photonic wire bonding [24–26], multi-lens objectives [27,28] free-form optics and coupling [29,30], and fiber to waveguide connector [31].

In this article, the electronic section of chip is a p-i-n diode with an intrinsic region comprising MQWs GaAs/AlGaAs layers. On–off modulators can be made by combining the optical reflector part at the optical surface and this diode. This design presents more effective specifications than the ones in the conventional designs, i.e., the electro-optical modulators and electro-absorption modulators [32].

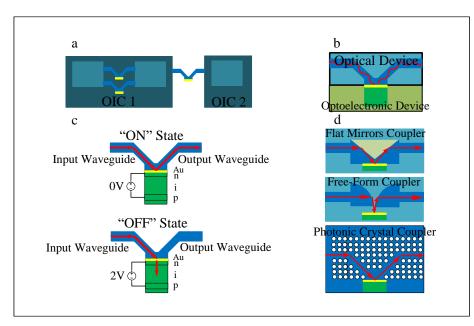
The modulators consist of an optical section and an electronic section (Figure 1b). A gold layer is used to reflect light in the "on" state of the modulator. This layer is located at the common surface of photonic crystals (PhCs) and the diode or terminal layer at the other side of the diode. When the diode bias is zero, light hits the surface of the gold layer at an angle of incidence and is reflected by the gold layer onto the output waveguide route. However, in the "off" state, when the diode is at a reverse bias of 1 V or greater, the light creates plasmonic modes on the gold layer surface after colliding with the gold layer surface. This light is then confined to the stack structure due to the hybrid plasmonic modes and quantum-confined Stark effect (QCSE) in the MQWs (Figure 1c), greatly attenuating the wave at the modulator output [32,33].

A PhC is an optical structure with periodically and regularly changing refractive indices along a specific direction. In two-dimensional PhCs, the refractive indices vary in two directions. A photonic waveguide is created by removing some pillars from a PhC structure. In other words, PhC waveguides are typically formed by a linear defect that consists of a row of modified unit cells of the lattice. The distance between the pillars determines the photonic bandgap obtained. This bandgap results in light confinement. Only waves within a specific frequency range can propagate through the waveguide geometry within the photonic bandgap. PhC waveguides can support waveguide bends with arbitrary bending radius and almost total transmission. Due to their many applications, PhCs are very popular among engineers and scientists. In recent years, various investigations of PhCs have been presented. In these studies, multifarious applications of PhCs such as logic gates, power splitters, and filters have been explored via experimental and numerical works [34–42]. Thus far, PhCs have been designed for surface waveguides, surface bends, and optical components such as surface emitting lasers [43,44], diode laser arrays integrated with a phase shifter [45], Mach–Zehnder optical filters [46], PhC switches [47], on-chip polarization division multiplexing [48], PhC electro-optic modulators [49], and they have even been used in medical applications such as PhC biosensors [50]. Light transmission in PhCs is possible with high efficiency and low scattering. Light bending in the transmission path can be carried out via creating defects and optimizing the plan parameters of PhCs. This operation is utilized for the fabrication of optimized optoelectronic components [51–56]. For example, waveguides and their bending have been recently investigated in diamond PhCs [57].

The reflective part of this optical connection is designed in accordance with the waveguide properties and various designs of two-dimensional PhCs by hybrid two-dimensional PhCs (Figure 1d). This design is straightforward; it has a less perplex computational process and smaller dimensions. Additionally, it is less arduous than 3D fabrication technology [32].

We can execute and exploit the processes of nano-structure fabrication with precise arrays. This is regularly performed in the fabrication of PhCs consisting of GaAs and gold nanorods, and it is more cost effective than 3D fabrication. Up to now, these processes have been used for the fabrication of multi-color InGaN/GaN nanorod light-emitting diodes [58] and the fabrication process of deterministic aperiodic PhCs with a 2D array of metallic nanorods [59]. Furthermore, novel investigations of nano-photonics using nanorods fabrication have been very recently presented, and they show that the nanofabrication of nanorod PhCs is possible [60,61].

These connections can be used on the optical surface of integrated chips to connect two parts or components and even to design new hybrid components such as optical modulators, optical filters, optical switches, and optical logic gates.



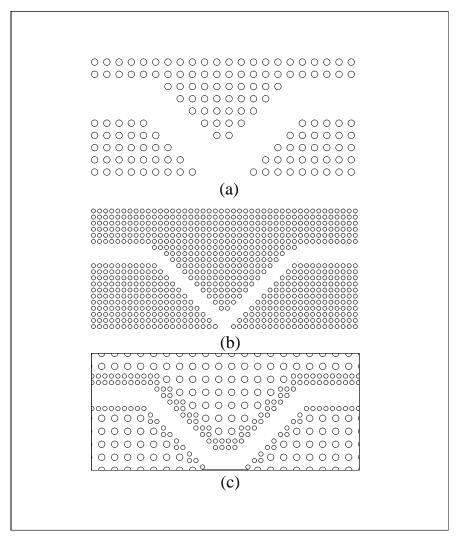
**Figure 1.** The illustrations are schematics. (**a**) Applications of the electro-reflective modulators in the interconnections of an OIC and the optical connection between two OICs. (**b**) The top section shows the optical component, and the bottom section shows the electronic component of this modulator. (**c**) The operation of the "on" and "off" states of the device is illustrated. (**d**) The optical section of the modulator shows the flat reflector, parabolic reflector, and PhCs.

### 2. Conceptual Design

A 2D PhC waveguide structure is proposed for the surface region where light is directed and strikes the electronic component at a certain angle. Polymer material with a refractive index of 1.51 was utilized in the waveguide layer based on the characteristics of the electronic element at the common surface of the PhC structure with the electronic element [32,33]. Recently, similar refractive indices have been investigated in the numerical analysis of parabolic 2D couplers [62]. Likewise, a range of refractive indices can be used for optimal design.

As illustrated by the structure shown in Figure 2a,b, in this structure, two separate PhC networks with GaAs nanorods in Figure 2a are utilized to fill the space, resulting in less light scattering and better light transmission. Likewise, it has a simpler fabrication process due to its dimensions. Furthermore, the gold (Au) nanorods in Figure 2b are exploited for an

improved waveguiding mechanism, better transmission, and adjustment of the incoming light to the gold layer in both "on" and "off" states. The structure plan in Figure 2c is designed to be exploited in the optical part of the electro-reflective modulators (ERMs) by combining these two separate structures in the waveguide region. At the junction of the optical and electronic sections, an Au layer with optimized thickness is deposited for both "on" and "off" states.

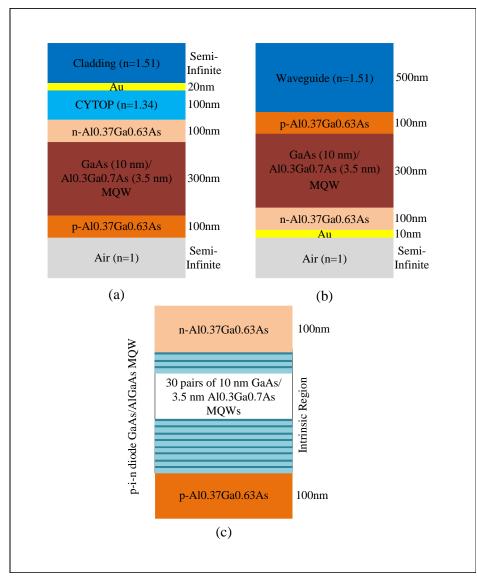


**Figure 2.** (a) The PhC structure with r and d specifications larger than that of structure (b) is used to fill space, achieve better confinement, and due to its dimensions, has a more simple manufacturing process. (b) The PhC structure with r and d specifications smaller than that of structure (a) is used for better wave propagation and for adjusting the incident angle of the wave with the electronic section. (c) The hybrid structure of (a,b) is used in the optical section of ERMs.

The electronic section of the ERMs is structured as a p-i-n diode with an intrinsic region of MQWs. There are two methods for using the gold layer in this structure, as shown in Figure 3. In the first design, the gold layer is located at the interface between the optical and electronic sections of the device. In this case, the wave hitting the gold surface is considered for using hybrid plasmonic MQWs modes in the "off" state of the modulator (Figure 3a), and TM polarization is used for the incident light [32]. In the second design, the gold layer is located at the bottom of the p-i-n diode for better reflectance, and the optimized thickness of the gold layer is 50 nm. In this case, TE polarization is used and investigated here.

Previously, the electronic component has been investigated with the noted first structure p-i-n diode via performance under TM polarization [32]. Furthermore, the second structure has been investigated in the wavelength range of 830–870 nm [32].

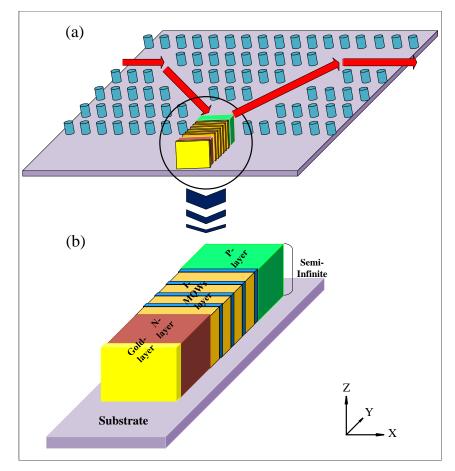
When 0 volts are applied to this diode, the modulator is on. Thus, the diode is off and has no special function to absorb the incident light in the PhCs layer. When a 1-volt reverse bias (or reverse bias larger than 1 volt) is applied to this diode, the modulator is off. Thus, the light is absorbed by the electronic section of the device and not transmitted to the PhCs output waveguide. The light waves are routed through the stack waveguide in the p-i-n, which causes increased damping in the "off" state of this ERMs. This is in accordance with the QCSE in MQWs of the intrinsic region. The structure of these MQWs consists of 30 pairs of 10 nm coupled layers of GaAs and 3.5 nm AlGaAs (Figure 3a).



**Figure 3.** (a) The gold layer is at the interface between the optical and electronic sections. TM polarization has been exploited for MQW plasmonic hybrid modes. (b) The gold layer is used at the bottom of p-i-n diode for better reflectance. In this case, TE polarization has been applied. (c) The structure of p-i-n diode is suitable and acceptable in both cases.

Figure 4a shows the three-dimensional structure of the modulator, including the PhC waveguide and the p-i-n modulator. The configuration of the hybrid nanorods is shown in Figure 2c, where the light wave is confined along the rod direction. The 2D PhC rods/slab waveguides are placed perpendicular to the modulator, which is generally formed vertically on the substrate, and the input port is located on the modulator surface.

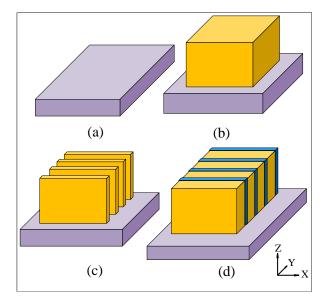
To address this issue, we propose a novel design in this paper which involves horizontally locating and fabricating the electronic section on the substrate surface. The p-i-n modulator is horizontally formed on the substrate for this purpose, and the input port is placed on its surface. The structure and arrangement of the electronic part of the modulator are shown in Figure 4b. This design can be fabricated using nano-fabrication processes such as patterning, deposition, developing, exposing, etching, and deletion. The design is used for the p-i-n modulator via the f-b cavity mechanism.



**Figure 4.** (a) Three-dimensional structure of the modulator including PhC waveguide and p-i-n modulator. (b) The structure and arrangement of the electronic part of the modulator horizontally.

The proposed schematic of the coupled MQWs fabrication process is shown in Figure 5. In the first step, a substrate is provided for the growth of the designed layers. The substrate is common to the electronic and optical sections of the modulator, on which the PhCs are also implemented (Figure 5a). Next, a GaAs layer is grown to size in the intrinsic region of the p-i-n diode for deposition and patterning of the coupled MQWs (Figure 5b). The GaAs MQWs are then formed and developed through a mask and pattern process in the previous step to maintain the GaAs MQWs and vacant MQWs. The vacant MQWs are created through an etching process (Figure 5c). Finally, AlGaAs layers are deposited in the vacant MQWs (Figure 5d). Thus, an intrinsic region is created for the p-i-n diode via the coupled MQWs.

In comparison with the usual vertically formed p-i-n modulator on the substrate, this proposed process has fewer steps of deposition. The intrinsic region of the p-i-n modulator is fabricated through masking once, etching once, and deposition once, unlike the deposition process for the usual p-i-n modulator, which repeats approximately 60 times. However, the proposed process has a big limitation, that is, the size of the masking pattern, which os 5–10 nm for the noted model. Therefore, there is a trade-off.



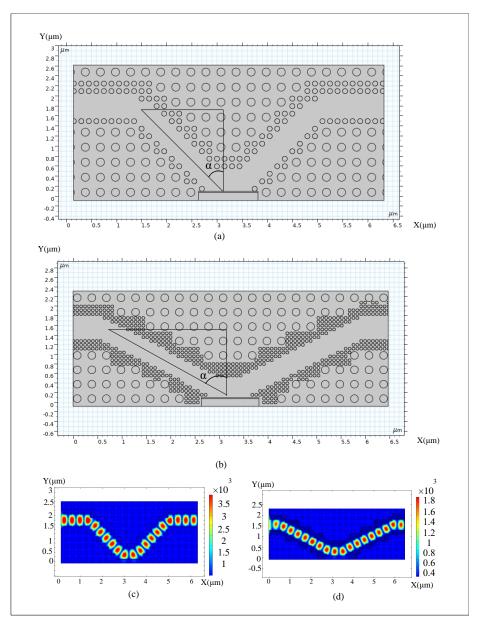
**Figure 5.** Proposed schematic of coupled MQW fabrication process. (a) The substrate is provided for the growth of the designed layers (substrate is common to electronic section and the optical section of the modulator on which the PhCs is also implemented). (b) GaAs layer is grown to size in the intrinsic region of the p-i-n diode for deposition and patterning of coupled MQWs. (c) GaAs MQWs are formed and developed by mask and pattern process of the previous step to maintain the GaAs MQWs and vacant MQWs. Vacant MQWs are created by etching process. (d) AlGaAs layers are deposited in vacant MQWs. Therefore, an intrinsic is region created for the p-i-n diode via coupled MQWs.

#### 3. The Design of Electro-Reflective Photonic Crystals Modulators

The PhC structure with optimized materials and dimensions is utilized to design the waveguide structure and determine the angle for the incident light of the p-i-n diode. Its optimized measures are GaAs with R = 80 nm and D = 300 nm (R is the radius of the nanorods and D is the lattice constant of the PhC structure), in this state, the removed areas of the nanorods are clear. A smaller and denser gold PhC structure was utilized to fill the margins of the removed areas. Its optimized measures are R = 50 nm and D = 150 nm. Then, the removed waveguide areas and the angle of incident light is  $\alpha = 45^{\circ}$  for the "on" and "off" states of the modulator. Furthermore, the dimensions of this device are 5 µm by 2.7 µm in the deflection area and the thickness of waveguide area is about 450 nm (Figure 6a).

Different angles can be created to prove that in this PhC structure, the waveguide structure and diverse angles are designed and shaped for the incident light on the p-i-n diode. For example, when investigating the incident light angle  $\alpha = 60^{\circ}$ , GaAs PhC Structural specifications are optimized and calculated for R = 80 nm and D = 300 nm. In this state, the removed areas of the nanorods for the waveguiding area are visible. Smaller and denser gold PhC structures were utilized to fill the margins of the removed areas. Its optimized measures are R = 30 nm and D = 80 nm; then, the removed waveguide areas and the shape of incident light angle  $\alpha = 60^{\circ}$  for the "on" and "off" states of the modulator. Furthermore, the dimensions of this device are 5.5 µm by 2.4 µm in deflection area. The thickness of the waveguide area is about 500 nm (Figure 6b). The polymer waveguide region with a refractive index of n = 1.51 was determined according to the p-i-n diode features in both structures.

The electric field propagation plot of TE polarization is shown in Figure 6c for a PhC waveguide layer with a deflection angle of 45° at 903 nm. It is analytically simulated using the finite element method. Furthermore, the electric field propagation plot of TE polarization is shown in Figure 6d for a photonic crystal waveguide layer with a deflection angle of 60° at 903 nm. At this wavelength, when the modulator is on, the transmittance is at its highest value, and the losses (or scattering) are at their lowest value.

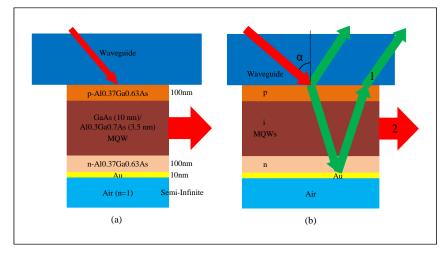


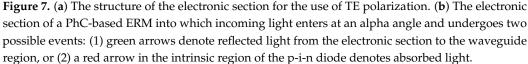
**Figure 6.** (**a**,**b**) The optical sections of ERMs based on hybrid PhC structures are shown with deflection angles of 45 and 60 degrees. (**c**) The electric field propagation plot for TE polarization in the PhC waveguide section corresponding to part (**a**). (**d**) The electric field propagation plot for TE polarization in the PhC waveguide section corresponding to part (**b**).

The second structure of the p-i-n diode mentioned above is designed for the electronic section of the device, as shown in Figure 7. In this configuration, the 'p' region is located at the interface with the polymer waveguide, which has a refractive index of n = 1.51, and the 'n' region is located at the interface with the gold layer, which is located at the bottom of the structure.

The polymer coating (i.e., the waveguide region in the PhCs) and the lower gold reflector form a Fabry–Pérot cavity to increase the insertion loss (*IL*) in the coupled MQWs for the modulator's "off" state, thereby increasing the extinction ratio (*ER*). This function is greatly improved at low voltage [33]. The input light is TE polarized, and it enters the electronic section of the device at an alpha angle from the polymer waveguide. It then enters the 'p' region and, after passing through the 'i' and 'n' regions, strikes the surface of the gold layer.

Depending on the alpha angle and the voltage applied to the diode (either zero or two volts), which is indicated by a green arrow, the light is reflected and exits the electronic section and is then coupled into the output path of the PhC waveguide (Figure 7b). Otherwise, as shown by a red arrow in the intrinsic region, the wave is propagated through the intrinsic region of the p-i-n diode, which consists of coupled MQWs and does not reach the output of the PhCs waveguide (Figure 7b).





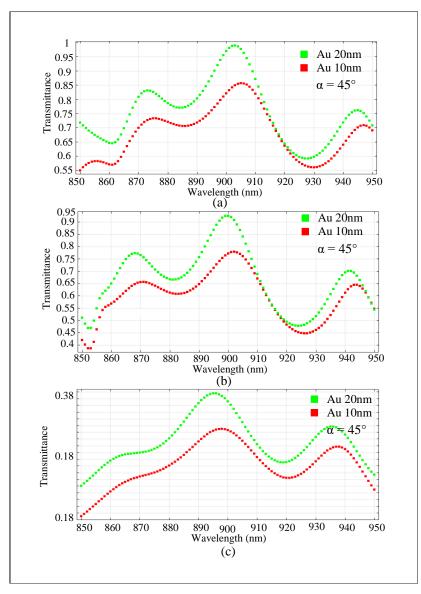
#### 4. Results and Discussion

A figure of merit (FOM) can be introduced when multiple parameters are used in optimization. Here, optimization is performed using parameters such as *r1* (radius of large nanorods), *d1* (distance between two large nanorods), *r2* (radius of small nanorods), *d2* (distance between two small nanorods), number of nanorods, thickness of the gold layer, and spectral data via the Nelder–Mead method simultaneously.

The utilized gold nanorods were selected from silver(Ag), aluminum (Al), and gold (Au) nanorods for small nanorods with PhCs structure, that have better characteristics for waveguide and transmission. Moreover, GaAs nanorods have been opted among GaAs, lithium niobate (LiNbO3), gallium nitrate (GaN) and gallium phosphate (GaP) for the large nanorods since GaAs nanorods have better performance.

The transmission diagrams in terms of the wavelength of the light propagated are given for the PhCs structure in the optical section of ERMs in the "on" state of the device. Transmittance curves in the PhCs are shown according to the material in the PhCs structure of large and small nanorods. The thickness of the gold layer is also considered for the reflection situation of the device in the "on" state.

In all plots, green curves correspond to a transmittance rate with a reflective gold thickness of 20 nm, and red curves correspond to the transmittance rate through a reflective gold layer with a thickness of 10 nm (Figures 8 and 9).



**Figure 8.** (a) The transmittance diagram for the PhC structure consisting of large GaAs nanorods and small Au nanorods with a deflection angle of 45°. (b) The transmittance diagram for the PhC structure consisting of large GaAs nanorods and small Ag nanorods with a deflection angle of 45°. (c) The transmittance diagram for the PhC structure consisting of large GaAs nanorods and small Al nanorods with a deflection angle of 45°.

As can be seen, the 20 nm thickness of gold has a better reflectance than the 10 nm thickness of gold (Figures 8 and 9). In fact, the 20 nm gold layer has better reflectance, and thus less light wave is leaked in the electronic section for the modulator "on" state. Therefore, it causes better transmittance for the optical section. Light is applied to the gold layer at an angle of 45°, as shown in Figure 6a.

In this analysis, we used large GaAs nanorods and small Au nanorods. The transmittance efficiency at 903 nm is 98%, and the bandwidth is in the range of 890–913 nm (23 nm) with over 80% output (Figure 8a). This special design has optimized R and D parameters, a high-contrast refractive index between the nanorods and gap, and the width of the waveguiding region is intended for these accurate and explicit wavelengths.

Additionally, we used large GaAs nanorods and small Ag nanorods. In this case, the transmittance efficiency at 898 nm is 97%, and the bandwidth is in the range of 890–907 nm (17 nm) with over 75% of the input light wave of the waveguide passed from the output waveguide by the PhC structure (Figure 8b). The transmittance values are approximately

equal when the small nanorods are Au or Ag. This is because both metals have similar and nearly identical features. However, Au nanorods were chosen for this role in the device because it has a broader pass bandwidth compared to Ag.

For the next analysis, we used large GaAs nanorods and small Al nanorods. However, the transmittance efficiency at 895 nm is only 37%, which is not acceptable (Figure 8c).

In further analysis, large nanorods of GaP and GaN and small nanorods of Au are utilized in the PhCs. However, the light wave transmittance in the range of 850–950 nm is less than 77%, which is not appropriate (Figure 9a,b). In the next case, large nanorods of LiNbO3 and small nanorods of Au are consiered, but the light wave transmittance in the range of 850–950 nm is also less than 77%, which is not admissible in comparison to the GaAs and Au hybrid (Figure 9c).

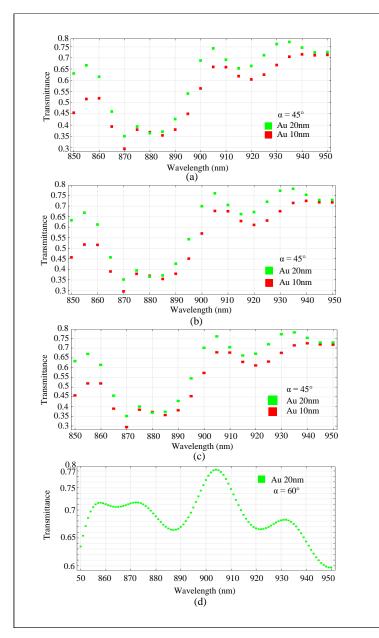
In the last case, light is applied on the gold layer at an angle of 60°, as shown in Figure 4a. Large nanorods of GaAs and small nanorods of Au are devised. The transmittance efficiency in the range of 895–915 nm is between 70% and 77%, which demonstrates that changing the deflection angle from 45° to 60° has reduced the light wave transmittance (Figure 9d). This indicates that a greater deflection angle results in reduced transmittance and increased losses, which occur on the surface of the reflector layer and depend on the critical angle and angle of refraction. These parameters depend on the boundary between the gold and polymer layer, where the refractive index of the polymer layer is assumed to be 1.51.

Figure 10a–c illustrate the 3D design of a single-mode waveguide coupling with surface-incident devices via the depicted free-form reflectors. Figure 10d demonstrates the FEM simulation results for this kind of design. A pair of parabolic reflectors are used to collimate the output from a single-mode waveguide with a 40° incident angle with respect to the device and refocus the beam reflected from the surface-incident an MQW modulator back to the waveguide. The simulation reveals high coupling efficiency values of 94% ( $IL \sim 0.27$  dB) at 850 nm wavelength for TE polarized inputs [4]. The dimensions of this 3D and bulky structure for the free-form region are about 60 µm × 20 µm × 10 µm [32,62].

The alternative design described in this paper for the optical part of MQW ERMs (as shown in Figures 11 and 12) is significantly reduced in size compared to the previous design (as shown in Figure 10), whereas this 2D PhC structure is composed of large and small hybrid PhCs with a plate size of about  $3 \mu m \times 3 \mu m$  in the area of light deflection. It has the same function while reducing the dimensions, and the coupling efficiency is still more than 90% at the noted wavelengths for MQW ERMs.

The p-i-n diode is designed to create the ERMs in the electronic part of this device, with the intrinsic regions of this diode being coupled MQW layers of AlGaAs/GaAs. The upper polymer coating (i.e., the waveguide region in PhCs, in this context) and the lower gold reflector form a Fabry–Pérot cavity to increase the *IL* in the coupled MQWs, thereby greatly improving the *ER* at low voltage. The specifications of this electronic section of the device have been obtained as *ER* ~ 6 dB and *IL* < 3 dB at 2 volts for the incident light angles of 38° and 41°. A more optimized modulator design can reach a peak ER of about 9–12 dB at 2 volts with a low *IL* of about 2–3 dB and a relatively wide spectral bandwidth of about 10 nm. It has also achieved low *IL* and high *ER* values for wavelengths of 856 nm. This device has better characteristics than similar devices [33].

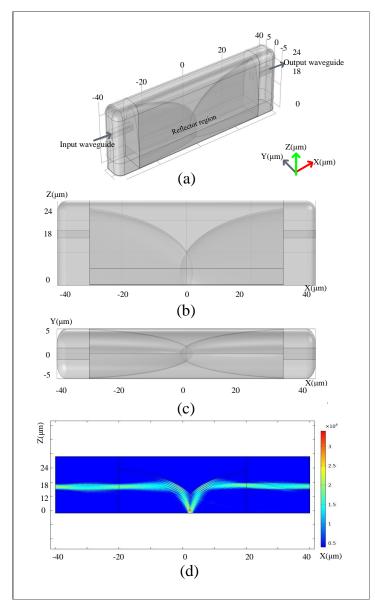
Here, the same specifications are intended for the electronic part of the modulator, and thus the angle of incident light in the optical part can be adjusted to 38° and 41° accordingly. This novel scheme is designed for the 41° angle of the optical section using integrated PhCs (Figure 11a). The electric field propagation in the waveguide path of this PhCs for a wavelength of 896 nm is shown in Figure 11b.



**Figure 9.** (**a**,**b**) The transmittance diagram for the PhC structure consisting of large nanorods of GaP and GaN and small nanorods of Au with a deflection angle of 45°. (**c**) The transmittance diagram for PhC structure consisting of large LiNbO3 nanorods and small Al nanorods with a deflection angle of 45°. (**d**) The transmittance diagram for the PhC structure consisting of large GaAs nanorods and small Al nanorods with a deflection angle of 60°.

As shown by the PhC design in Figure 11, significant light confinement and no light scattering occurs in specific regions. The electronic part of the modulator operates at a frequency of 856 nm, with light wave transmittances of 70–80% in the frequency range of 846–860 nm, and a maximum of 80% at 852 nm in the modulator's "on" state.

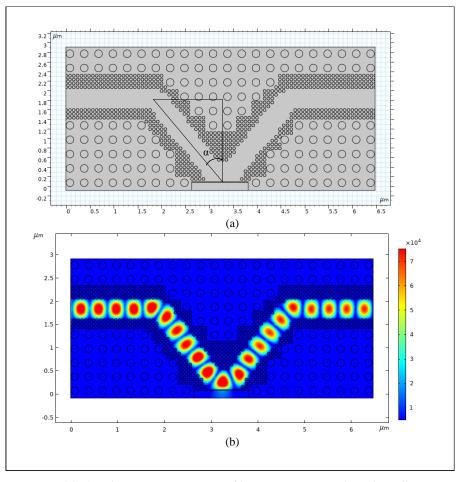
However, due to the optimized R, D, and waveguiding region width parameters of this special design, if the operating frequency is in the range close to 900 nm, the light wave transmittance is more than 80% in the range of 891–904 nm, with a peak of 95% at a wavelength of 897 nm, as shown in Figure 12. The transmittance in the "on" state is sufficiently desirable, and the incident light angle can be adjusted to the electronic section of the device.



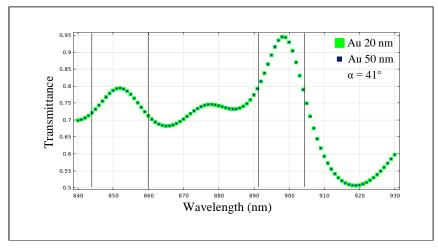
**Figure 10.** (a) 3D structure of Optical Free-Form Couplers for MQWs ERMs with a pair of parabolic reflectors. (b) Side view of this scheme. (c) Top view of this scheme. (d) Side view of FEM simulation of the coupling between the polymer waveguides and the MQW modulator region are demonstrated [62].

Furthermore, the optical part of the modulator has a significantly reduced size in comparison to three-dimensional models [32], and the two-dimensional PhC design enables a simpler fabrication process, making it advantageous for integration with optical integrated circuits (OICs) and connection with other components in photonic integrated circuits (PICs), both optical and optoelectronic.

The PhC structure is composed of large GaAs nanorods and small Au nanorods to achieve a deflection angle of 37° for the modulator "on" state at zero-volt bias, as shown in Figure 13a. In this case, the TE mode is propagated in the modulator, as depicted in Figure 13b. The electronic part of the device is also designed and optimized based on the release of TE mode for both "on" and "off" states of the modulator. For this simulation, the effective refractive index of the intrinsic region of the p-i-n diode consisting of coupled MQWs is considered to be  $n_{eff} = 3.43 + 0.004i$ , and for the "off" state of the modulator, which corresponds to a 2-volt bias applied to the diode,  $Alpha_{mqw-2v} = 9863(1/\text{cm})$ . Additionally, the thickness of the gold layer is 50 nm [33].



**Figure 11.** (a) The PhC structure consists of large GaAs nanorods and small Au nanorods to create a deflection angle of 41°. (b) The TE mode electric field propagated in the PhC waveguide section corresponding to part a.



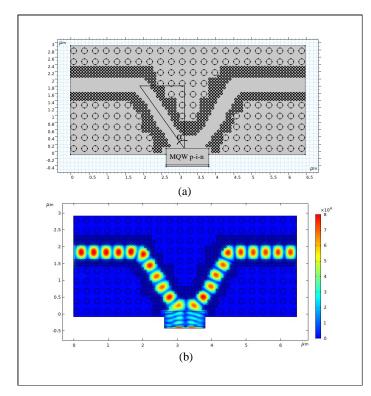
**Figure 12.** The transmittance diagram for the PhCs structure consisting of large GaAs nanorods and small Au nanorods with a deflection angle of 41°.

In Figure 13, it can be noted that when the modulator is turned on, the light wave transmittance is more than 80% at the 867–875 nm range for the PhC structure. This is due to the special design with optimized *R* and *D* parameters, a high-contrast refractive index between the nanorods and gap, and awaveguiding region width selected for these specific wavelengths. The parameters of the Fabry–Pérot cavity, located between the polymer coating and p-i-n diode, also have an effect on the transmittance value of the light wave in

this situation. Furthermore, designing the structure with a Fabry–Pérot cavity has reduced the range of the light wave transmittance bandwidth, as shown in Figure 14.

Taking into account the limitations and conditions of current fabrication technology, the proposed structure was designed and simulated with random tolerance of Au and GaAs nanorod radii. This approach makes the results of the simulation and proposed design closer to the reality of the experimental sample.

As shown in Figure 15a, a tolerance between 77.5 nm and 82.5 nm, with a step of 0.5 nm, was assumed for random changes in GaAs nanorod radius. Similarly, a range of 47.5 nm to 52.5 nm, with a step of 0.5 nm, was randomly used for the gold nanorod radius. These changes were applied to the structure in the worst-case scenario, where all GaAs and Au nanorods have some tolerances due to errors in the fabrication technology. The number of nanorods that undergo these changes was applied randomly within the range stated in the design. The average radius of GaAs and Au nanorods was then calculated. This process was repeated 100 times, and the transmission rate was calculated each time. The transmission rate for different states is shown in the diagram of Figure 15b. This diagram depicts the transmittance in terms of the average radius of GaAs and Au nanorods. According to this graph, at least half of the states considered with tolerance still have a transmittance of more than 80%. Therefore, the probabilistic fabrication limitations and faults are taken into account in the proposed novel structure via random and presumptive parameters in simulation.



**Figure 13.** (a) The PhC structure elements to create a deflection angle of 37° at zero-volt bias (the modulator is on). (b) The electric field of TE polarization emitted in the PhCs waveguide corresponding to section a.

Figure 15c shows a schematic of a part of the proposed structure, consisting of a polymer layer, GaAs, and Au nanorods. The wave propagation occurs in the polymer layer and the specific gap between the PhC structure. In Figure 15d, a section of the previous schematic is shown, which demonstrates how the polymer layer and GaAs and Au nanorods are vertically placed together. The process model and fabrication technology of this new structure have been put forward through these schematics.

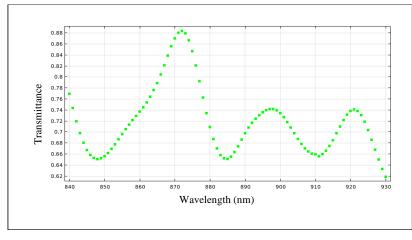
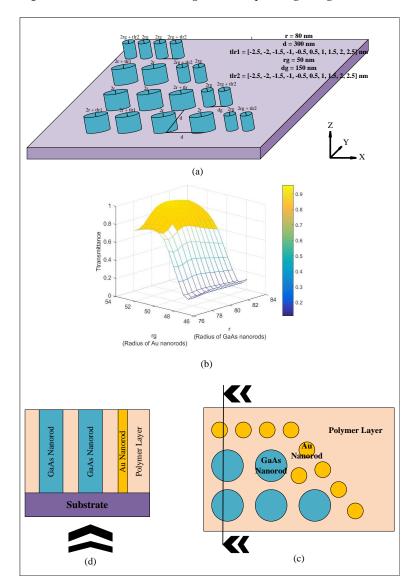


Figure 14. The transmittance diagram corresponding to Figure 13a,b.



**Figure 15.** (a) Design and schematic of the proposed structure with radius random tolerance of Au and GaAs nanorods performed simultaneously. (b) The transmittance in terms of the average radius of GaAs and Au nanorods is depicted simultaneously. (c) A part of the proposed structure consisting of polymer layer and GaAs and Au nanorods. (d) Schematic of the polymer layer and GaAs and Au nanorods placed together vertically.

#### 5. Conclusions

The adaptability and compatibility of coupling different elements to each other is very momentous for the realization of integrated connection in the PICs and OICs. Accordingly, lower dimensions and lower losses are always considered of high importance and are thus developed. In this paper, an electro-reflective modulator was investigated, which consists of an optical section (PhCs) and an electronic section (including a p-i-n diode with an intrinsic region of coupled MQWs).

A novel design of these modulators was executed in its optical region using the integrated PhC structure. This two-dimensional structure greatly reduces the dimensions of optical section and has more cost-effective fabrication processes than the bulky 3D components. The transmittance and bandwidth have increased, because this design is optimized for the lowest amount of losses, and the scattering of light in the waveguide and deflection route is low. It justifies the utilization of PhC structure in the optical part of the ERMs. Simultaneously, changing the deflection angle to create the desired angle of incident light to the electronic part of the device simplifies the design of the electronic part of the device. The PhC structure in the optical section of this device resulted in better linkage and excellent compatibility with other optical elements in the PICs.

OICs can utilize this structure in other optical elements such as functional modulators, optical switches, optical waveguides with bends, optical logic gates, optical computational circuits, and optical filters.

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