



# Application and Analysis of Modified Metal-Oxide Memristor Models in Electronic Devices <sup>†</sup>

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Article

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**Abstract**: The design of memristor-based electronic circuits and devices gives researchers opportunities for the engineering of CMOS-memristor-based electronic integrated chips with ultra-high density and various applications. Metal-oxide memristors have good compatibility with the present CMOS integrated circuits technologies. The analysis of new electronic circuits requires suitable software and fast-functioning models. The main purpose of this paper is to propose the application of several modified, simplified, and improved metal-oxide memristor models in electronic devices and provide a comparison of their behavior, basic characteristics, and properties. According to this, LTSPICE is utilized in this paper because it is a free software product with good convergence. Several memristor-based electronic circuits, such as non-volatile passive and hybrid memory crossbars, a neural network, and different reconfigurable devices–filters, an amplifier, and a generator are analyzed in the LTSPICE environment, applying several standards and modified metal-oxide memristor models. After a comparison of the operation of the considered schemes, the main advantages of the modified metal-oxide memristor models, according to their standard analogs, are expressed, including fast operation, good accuracy, respectable convergence, switching properties, and successful applicability in complex electronic circuits.

**Keywords:** metal-oxide memristors; modified memristor models; memory crossbars; reconfigurable electronic devices; window function; switching properties

# 1. Introduction

The memristor is a highly nonlinear, passive electronic one-port element with switching and memory properties [1]. It was predicted by Chua in 1971, according to the missing direct relation between the electric charge and magnetic flux linkage and symmetry of the relations between the basic electric quantities–current, voltage, flux, and charge [2]. Its memory and switching properties are related to the transformation of a defect-impurity composition under applied voltage signals, leading to a change in resistance. According to the materials they are based on, there are several basic types of memristors [3]. In the scientific literature, they are described as titanium dioxide [4,5], hafnium oxide [6], tantalum oxide [7], niobium oxide [8], polymeric [9], ferroelectric [10], spintronic [11], other types of different memristor elements [12]. In recent years, metal-oxide memristors based on transition metals, are under intensive investigation due to their stable characteristics and parameters, very small dimensions in the nanometer range, low power consumption, and good compatibility with the present CMOS (complementary metal oxide semiconductor) integrated circuits with ultra-high density [13]. Memristors are potentially applicable in different types of electronic circuits and devices, such as artificial neurons and neural networks [14,15], nonvolatile memory crossbars [16,17], reconfigurable digital and analog circuits, filters [18,19], amplifiers [20], generators [21,22], logic gates, chaotic systems,



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**Copyright:** © 2023 by the author. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). and many others [23–25]. In memory crossbars, the memristors are used for accumulating logical information, and their state is changed by the external voltage or current pulses [16,24,26]. In such electronic circuits, memristors operate in a hard-switching mode in writing and erasing information, and their switching properties are important for the fast operation of memory devices [24,27]. In artificial neural networks, memristors are mainly used in the synaptic circuits as storing elements for the synaptic weights [24,25,28]. During the change in weights, the memristors normally operate in a soft-switching mode. Memristors are applicable to in-memory computing electronic schemes, brain-inspired computing devices, and many other circuits with artificial intelligence [28]. In reactanceless relaxation oscillators, memristors operate in a soft-switching mode but with a change in the state variable in comparatively broad ranges [21,22]. Memristors are applied for the avoidance of inductors and capacitors in the generators' integrated circuits and for minimization of the chips' area. In some reconfigurable memristor-based linear circuits and devices, as filters [18,26] and amplifiers [20], the memristors operate at low-level signals as linear resistors. Then, the amplitude of the applied signals has to be lower than the memristor activation threshold. The characteristics of these memristor-based circuits are accurately tuned by external voltage pulses.

The design and analysis of complex memristor-based electronic circuits require an appropriate software environment such as SPICE (simulation program with integrated circuits emphasis), Verilog-A, and others [29], and respective fast-operating memristor models [30,31]. In the last several years, LTSPICE (linear technology SPICE), along with the other SPICE products, have been preferred by many researchers owing to their free license, simple and user-friendly interface, and improved convergence [32]. The number of electronic elements and connections between them in LTSPICE is not restricted, and this gives the engineers the possibility to analyze and simulate very large and complex electronic circuits [32,33]. This is the main reason for LTSPICE software to be applied and used in the present work. The latest versions of LTSPICE software for Microsoft Windows and Mac operating systems (OS) with detailed instructions can be freely downloaded and installed using the link: https://www.analog.com/en/design-center/design-tools-and-calculators/ltspice-simulator.html (accessed on 21 September 2022).

The existing research in the scientific literature on classical metal-oxide memristor models, such as those of Strukov-Williams [1], Joglekar [4], Biolek [34], Lehtonen-Laiho [35], and others [36,37], have some disadvantages, together with their positive properties. For example, Strukov-Williams, Joglekar, and Biolek's models are simple and fast-operating ones, but they have comparatively low accuracy, do not completely represent the nonlinear dopant drift, are dependent on the applied voltage, and do not include activation thresholds, which are very useful for the separation of the main operating modes of metal-oxide memristors [3,34]. Lehtonen-Laiho's [35] model and Williams' model for tantalum oxide memristors [7] have very high accuracy and good properties, but they contain a high amount of elementary mathematical operations, and due to this, they require serious computing resources and time and are not very appropriate for the simulation of complex memristor-based circuits with a large number of memristors. Physically based compact metal-oxide memristor models are used for the analysis of resistance-switching effects and the operation of memristor-CMOS-based resistive random-access memories, logic circuits, and many other electronic schemes and devices [29,30]. Variations in memristor parameters and characteristics play an important role for RRAM devices in both computing and memory applications. In many physics-based and compact memristor models, the variations of characteristics and parameters are represented [38,39].

To partially overcome the disadvantages of some standard memristor models, several modified metal-oxide memristor models were proposed [15,17,19]. These models are mainly based on the classical memristor models of Joglekar, Biolek, and Lehtonen-Laiho. These modified models are improved by the approximation and simplification of their mathematical expressions, by the inclusion of activation thresholds, by applying highly nonlinear voltage-dependent terms in state equations, and by utilizing a differentiable

step-like function, instead of the classical non-differentiable Heaviside function [33]. The activation thresholds in the modified memristor models allow them to operate at different signal levels in soft-switching and hard-switching modes and as linear resistors and to be applied in neural networks, nonvolatile memory matrices, and reconfigurable electronic circuits [17]. The modified models [15,17,19] contain a sufficient number of parameters, which are used for their tuning according to the experimental current-voltage characteristics of metal-oxide memristors. They correctly represent the nonlinear ionic dopant drift in metal-oxide memristor elements and are able to operate at high frequencies.

The aim of this paper is to propose a comparison of several modified models [15,17,19] and classical metal-oxide memristor models, including their behavior, properties, and application in different memristor-based electronic circuits and devices—passive and hybrid memory crossbars, low-pass, high-pass, band-pass and band-stop filters, a memristor-based generator, an amplifier, and a neural network.

For the achievement of the aim of this paper, the following tasks are solved. The modified LTSPICE memristor models [15,17,19] are applied and analyzed in passive and hybrid memristor matrices, in a neural network with memristor-based synapses, in reconfigurable circuits including filters, a generator, and an amplifier. The analysis of these memristor-based devices is realized in the LTSPICE environment. A comparison of the modified models together with the classical models of Strukov-Williams, Joglekar, Biolek, and Lehtonen-Laiho is made according to their behavior in electronic circuits and the use of several important criteria-complexity, accuracy, simulation time, operating frequency, and switching properties [3,33]. The main advantages of the modified metal-oxide memristor models are confirmed as high operating speed, good switching properties and accuracy, high operating frequencies, improved convergence, and applicability for the design of different complex electronic circuits with a large number of memristors in ultra-high-density integrated chips.

The used LTSPICE memristor library models are included in a unified and open library [33], together with many other standard and modified memristor models, memristorbased electronic circuits, and instructions for their use. The metal-oxide memristor models' library is freely available for use at the following website: https://github.com/mladenovvaleri/ Advanced-Memristor-Modeling-in-LTSpise (accessed on 23 September 2022).

The rest of this paper is organized as follows. In Section 2, a brief description of the applied classical and modified metal-oxide memristor models is presented. A discussion on the generation of the respective LTSPICE memristor library models is presented in Section 3. The next Section 4 represents the analysis of several memristor-based electronic devices, including neural networks, passive and hybrid memory crossbars, low-pass, high-pass, band-pass and band-stop filters, amplifiers, and oscillators, and applying classical and modified metal-oxide memristor models. A comparison of the considered memristor models, according to their behavior in electronic circuits and using several basic criteria, is expressed in Section 5. In Section 6, a discussion of the obtained results is included. The conclusion is presented in Section 7.

#### 2. A Description of the Applied Memristor Models

In this section, general information about memristors and a description of the considered classical and modified metal-oxide memristor models is presented for a better understanding of the operation of memristors in electronic circuits and devices. In the scientific literature, many physics-based memristor models are presented [27,29]. The efforts of researchers working in this field of science are related to the representation of the cycle-to-cycle variations of the memristor parameters, the enhancement of the reliability and stability of the memristor models, and the improvement of the endurance of memristor elements [38,39]. Each memristor model is expressed by a system containing two math equations. The first one represents the relationship between the memristor current *i* and voltage *v*. It includes the memristor ON-state and OFF-state resistances— $R_{ON}$  and  $R_{OFF}$ —and the state variable *x*. In most cases, the state variable *x* is expressed as a ratio between the length of the doped region w and the whole memristor length D. The second one is the so-called state differential equation. It relates the time derivative of state variable x and the memristor current i (or voltage u). To derive the current–voltage and state–flux relationships, the initial value of the state variable  $x_0$  and the excitation signal is used for the solution of the described system of equations.

## 2.1. Classical Memristor Models

In the scientific literature, different standard metal-oxide memristor models are described [1,4,5,27]. Here, the mainly used classical models of Strukov-Williams [1], Joglekar [4], Biolek [34], and Lehtonen-Laiho [35] are considered.

#### 2.1.1. Strukov-Williams Memristor Model [1]

This memristor model is especially applicable to titanium dioxide memristor nanostructures [1]. It has very simple math equations presented by the system (1). The applied parabolic window function x(1 - x) is used for the limitation of the memristor state variable x in the range (0, 1).

$$i = [R_{ON}x + R_{OFF}(1-x)]^{-1} \cdot v$$

$$\frac{dx}{dt} = k \cdot i \cdot x(1-x)$$
(1)

The physical constant *k* is dependent on memristor parameters [1]:

$$k = \mu \cdot R_{ON} \cdot D^{-2} \tag{2}$$

where  $\mu$  is the ionic dopant drift mobility. From a physical point of view, the mobility of the oxygen vacancies depends on the electric field intensity, and it increases with the applied voltage. Strukov-Williams' memristor model uses ionic mobility with a constant value.

#### 2.1.2. Joglekar Memristor Model [4]

The main difference between Joglekar's memristor model and Strukov-Williams' model is in the applied window functions. Joglekar's window function is a higher-order polynomial, where p is a positive integer exponent. Strukov-Williams' window function is a special case of the Joglekar function when p = 1. The Joglekar memristor model is more flexible, owing to the use of the parameter p. It is presented by system (3).

$$i = [R_{ON}x + R_{OFF}(1-x)]^{-1} \cdot v$$

$$\frac{dx}{dt} = k \cdot i \cdot \left[1 - (x-1)^{2p}\right]$$
(3)

Strukov-Williams' and Joglekar's window functions have a disadvantage, known as a *terminal state problem* [3,4]. Sometimes, when the state variable *x* reaches the limiting values of zero and the unity, the model is unable to represent the continuous change in the state variable *x* in accordance with the equivalent flux linkage  $\Psi$ .

## 2.1.3. Biolek Memristor Model [34]

This memristor model is similar to the previous ones, but the applied window function is quite different [34]. It depends not only on the state variable but also on the direction of the memristor current. The Biolek memristor model is represented by (4) [34].

$$i = [R_{ON}x + R_{OFF}(1-x)]^{-1} \cdot v$$

$$\frac{dx}{dt} = k \cdot i \cdot \left\{ 1 - [x - stp(-i)]^{2p} \right\}$$
(4)

The applied window, frequently stated as the classical Biolek window function, uses the standard Heaviside step function [34], which is a non-differentiable one and sometimes is a precondition for the occurrence of convergence problems in the SPICE environment [32,40]. In many of the modified memristor models discussed in the next paragraph, the Heaviside step function is replaced by a smooth and differential sigmoidal alternative for the improvement of the model's convergence. Compared to Joglekar and Strukov-Williams' windows, the standard Biolek window function circumvents the terminal state problems [3,34,36].

# 2.1.4. Lehtonen-Laiho Memristor Model [35]

This memristor model has a higher complexity, according to the previously discussed models. The equation expressing the current–voltage relationship contains two terms. The first one contains the state variable and the memristor voltage and is related to the memory effect of the element [1,3]. The second term expresses the rectifying effect of the memristor in a hard-switching mode. The state differential equation includes the standard Biolek window function, which is applied for the limitation of the state variable and for the representation of the boundary effects [3,34]. The model is presented by (5) [35].

$$i = \beta \cdot x^{n} \cdot \sinh(\alpha v) + \chi[\exp(\gamma v) - 1]$$

$$\frac{dx}{dt} = a \cdot f_{B}(x, i) \cdot v^{m}$$
(5)

The coefficients  $\beta$ , n,  $\alpha$ ,  $\chi$ ,  $\gamma$ , a, and m are applied for tuning the memristor model according to experimental current-voltage characteristics [35]. The Lehtonen-Laiho model is applicable to different types of metal-oxide memristors, such as titanium dioxide, hafnium oxide, and others [1,4,6]. It has high accuracy and can correctly operate at high-frequency signals. A disadvantage of this model, according to the previously discussed models, is its higher complexity, owing to the use of many elementary math operations.

In the next paragraph, several modified and improved metal-oxide memristor models based on the discussed classical memristor models are presented [15,17,19].

# 2.2. Modified Memristor Models

In the considered modified memristor models, an improved version of the standard Biolek window function is used [30]. It is represented by the first equation of formula (6). It includes a flat step-like function, denoted by stpp(i), which is expressed by the next Equation (6) [3,15,17,19].

$$f_{BM}(x,i) = 1 - [x - stpp(-i)]^{2p}$$
  

$$stpp(i) = \frac{1}{2} \cdot \left[ (i^2 + s)^{-0.5} \cdot i + 1 \right]$$
(6)

The second equation of (6) represents a smooth and differentiable step-like function stpp(i), which is a modified version of the original Heaviside step function [41]. The parameter *s* is applied to change the steepness of the function in the region of switching. The modified step function is suitable for realization in the SPICE environment and for a partial circumvention of convergence problems.

# 2.2.1. A Modified Memristor Model Based on Biolek and Lehtonen-Laiho Models (Model $A_{11}$ ) [17]

The considered memristor model is presented by (7). This memristor model uses a simple current–voltage relationship, which is expressed by the first equation of (7). The activation threshold  $v_{thr}$  is applied in the state differential equation. If the memristor voltage is lower than the activation threshold  $v_{thr}$ , then the state variable does not alter, and the memristor behaves as a linear resistor. When the voltage exceeds the activation threshold, then the state variable changes in accordance with the equivalent flux linkage.

$$\begin{vmatrix} i = [R_{ON}x + R_{OFF}(1-x)]^{-1}v \\ \frac{dx}{dt} = k_1 \cdot f_{BM}(x,i) \cdot \sinh(k_2 v), \ |v| \ge v_{thr} \\ \frac{dx}{dt} = 0, \ |v| < v_{thr} \end{aligned}$$
(7)

In the state differential equation, the time derivative of the memristor state variable x is related to the voltage, applying a sine-hyperbolic dependence [17]. The coefficients  $k_1$  and  $k_2$  are used for the adjustment of the memristor model. This model operates at higher frequencies, correctly representing the boundary effects. It has good accuracy. According to the standard Lehtonen-Laiho model, the discussed modified memristor model has a simplified structure and could be applied for the analysis of complex memristor-based electronic circuits and devices [17].

2.2.2. A Modified Model Based on Lehtonen-Laiho Memristor Model A<sub>12</sub>) (Model A<sub>12</sub>) [15]

This metal-oxide memristor model is based on the standard Lehtonen-Laiho model. It is simplified, according to its classical analog, by the approximation of the described equations. It is represented by (8). In the first equation of (8), the relation between the current and voltage is based on a sine-hyperbolic dependence on the voltage and a fixed power of the state variable x.

$$i = k_1 x^3 \cdot \sinh(k_2 v)$$
  

$$\frac{dx}{dt} = [k_3 v \cdot f_{BM}(x, i) \cdot \exp(k_4 v)] \cdot stpp(v - v_{thr})$$
(8)

In the state differential equation, an exponential dependence on the voltage is applied. The model includes an activation threshold  $v_{tlir}$  for the separation of the operating modes of the memristor [15]. The coefficients  $k_1$ ,  $k_2$ ,  $k_3$ , and  $k_4$  are used for the adjustment of the memristor model, which is able to operate with high-frequency signals.

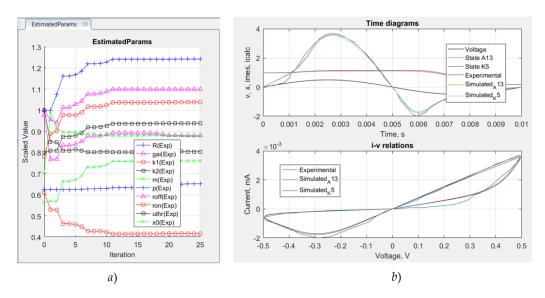
2.2.3. A Modified Memristor Model Founded on Strukov-Williams and Lehtonen-Laiho Models  $(A_{13})$  [19] (Model  $A_{13}$ )

This modified memristor model is based on a simple relation between the current and voltage and includes a nonlinear dependence between the time derivative of the state variable and the memristor voltage. The applied activation thresholds allow the memristor model to be used for the analysis of non-volatile memory crossbars and memristor-based neural networks at high-frequency impulse sequences.

$$\begin{vmatrix} i = [R_{ON}x + R_{OFF}(1-x)]^{-1} \cdot v \\ \frac{dx}{dt} = k_1 \cdot f_{BM}(x,i) \cdot (k_2 \cdot v)^3, \ |v| \ge v_{thr} \\ \frac{dx}{dt} = 0, \ |v| < v_{thr} \end{vmatrix}$$
(9)

Parameter estimation and the adjustment of the considered memristor models (A<sub>11</sub>, A<sub>12</sub>, A<sub>13</sub>, Strukov-Williams, Joglekar, Biolek, and Lehtonen-Laiho models) are realized by comparison to the experimental current-voltage characteristics of metal-oxide-based memristors. Gradient descending and simulated annealing algorithms [19,42] are applied for the minimization of the root mean square error and to obtain the optimal values of the memristor models' parameters.

An example of the optimization procedure is presented in Figure 1. It is related to the modified memristor model  $A_{13}$  for the representation of the processes of parameter estimation and obtaining the optimal values of the coefficients of the model. The trajectories of the memristor model parameters during the estimation procedure are presented in Figure 1a for the observation of their alteration. The derived optimal values of the memristor current and the corresponding current-voltage characteristics, which are presented in Figure 1b for comparison with the experimental memristor characteristics. The estimation procedures of the other considered memristor models are similar to the discussed one.



**Figure 1.** (a) Parameter trajectories obtained during the memristor model parameters estimation; (b) Time diagrams of the memristor voltage, experimental, and simulated currents according the modified model A<sub>13</sub> and the standard Lehtonen-Laiho model, denoted by K<sub>5</sub>, and the corresponding current–voltage relationships.

### 3. A Generation of LTSPICE Memristor Library Models

In this section, a compact equivalent LTSPICE schematic of the previously discussed memristor models is presented and explained in detail. The realization of the LTSPICE models of the considered metal-oxide memristors is based on the schematic, as presented in Figure 2 for a description of its basic blocks and operation. The equivalent circuit shown in Figure 2, is applied for the generation of all the considered standard and modified metal-oxide memristor models. The schematic is based on the two main equations in each memristor model: the current-voltage relationship and the state differential equation, relating the time derivative of the memristor state variable and the current (voltage). The memristor voltage is applied between its terminals, which are usually denoted by te (top electrode) and be (bottom electrode). Frequently, in the scientific literature, these electrodes are denoted by a (the anode) and c (the cathode). The current of the voltage-controlled current source G<sub>2</sub> expresses the time derivative of the memristor state varies according to the corresponding state differential equation of the memristor model. The capacitor  $C_1$  is connected in parallel to it for the integration of its current and to obtain the memristor state variable. Its voltage is proportional to the state variable. The resistor  $R_1$  is connected in parallel to the capacitor  $C_1$  and ensures a smooth change in the voltage across it. In this way, the resistor  $R_1$  partially prevents convergence problems in the SPICE environment. The potential of the terminal Y is proportional to the memristor state variable. The memristor current is represented by the voltage-controlled current source G<sub>1</sub> [3,33,40].

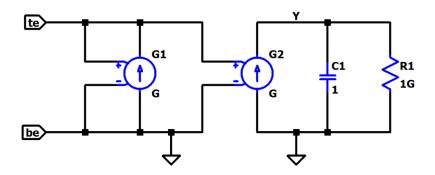


Figure 2. An equivalent schematic of a memristor model in the LTSPICE environment.

- 1 . subckt  $K_2$  te be Y
- 2 . params ron = 100 roff = 16e3 k = 10e3 pp = 1
- 3 C1 Y 0 1 IC = 0.3
- 4 Rad Y 0 10G
- 5 G2 0 Y value = {(k\*V(te,be)\*(1/(ron\*(V(Y)) + roff\*(1 V(Y))))\*(1 pow((2\*(V(Y)) 1),(2\*pp))))}
- 6 G1 te be value = { $V(te,be)^*((1/(ron^*(V(Y)) + roff^*(1 V(Y)))))$
- 7 . ends K<sub>2</sub>

The first row of the code defines the sub-circuit of the Joglekar memristor model, which is abbreviated as  $K_2$ . The electrodes of the memristor element-te, be, and Y is also included. The memristor model parameters  $R_{ON}$ ,  $R_{OFF}$ , k, and pp are included in the second row of the code. The capacitor  $C_1$ , applied for integration of the time derivative of the state variable, is described in the third row, and its initial voltage, denoted by IC, is proportional to the initial value of the memristor state variable  $x_0$ . The additional resistor Rad, attached in parallel to the capacitor  $C_1$ , has a value of 10 G $\Omega$  and is expressed in the next row. The voltage-controlled dependent source  $G_2$  is represented in the fifth row of the code. Its conductance is proportional to the time derivative of the memristor state variable and, in the present case, contains the constant k, the memristor voltage V(te,be), the memristance  $(1/(ron^*(V(Y)) + roff^*(1 - V(Y)))$ , and the Joglekar window function, expressed as  $(1-pow((2^*(V(Y)) - 1),(2^*pp)))$ . The sixth row represents the dependent source  $G_1$ , which conductance is proportional to the memductance  $((1/(ron^*(V(Y)) + roff^*(1 - V(Y))))$ . The description of the memristor model finishes with the command "ends".

Using the discussed equivalent schematic, the respective LTSPICE code could be generated and applied for the creation of LTSPICE memristor library models, which are needed for the analysis and design of different memristor-based electronic circuits [33,40,43].

#### 4. Memristor-Based Electronic Devices–Analysis in LTSPICE Environment

In this section, several memristor-based electronic circuits and devices are presented and analyzed in the LTSPICE environment using the classical and modified memristor models discussed in Section 2.

#### 4.1. A Passive Memristor Memory Crossbar

In Figure 3, a schematic of a passive and simple memristor-based memory array of 25 memristors is shown for the description of its functioning and structure. It includes five-word lines and five-bit lines, respectively. Loading a bit of information in a given memristor cell is realized by its selection, applying a demultiplexer, and using a voltage pulse. The own resistance of the memory crossbar rims is about 2.2  $\Omega$  and could be ignored with respect to the resistance of the memristor cells. For writing a logic unity, a positive impulse with a level of 1.1 V and a period of 100 ns is applied. For writing a logic zero, a voltage pulse with the same duration and a level of -1.1 V is used. For reading the information from the crossbar, positive impulses with a level of 0.1 V are used. The writing and erasing processes in the memristor memory crossbar are represented in Figure 4 with the time diagrams of the memristor voltage, the alteration of their memristance, and the corresponding state variables. The standard and modified memristor models, described in Section 2, are used for the analysis of the memory crossbar in the LTSPICE environment [32]. The amplitude of the reading impulses is lower than the memristor activation threshold v<sub>thr</sub>, and the resistance of the memristor is not changed. In this way, the information stored in the memristor elements is not affected. The parasitic sneak paths in the memristor memory array do not significantly influence its normal operation due to the presence of high-resistance reversely biased memristors [24,33].

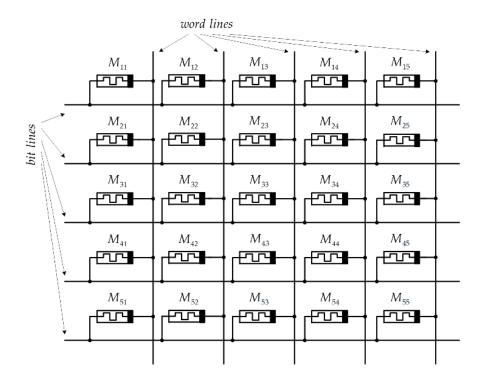
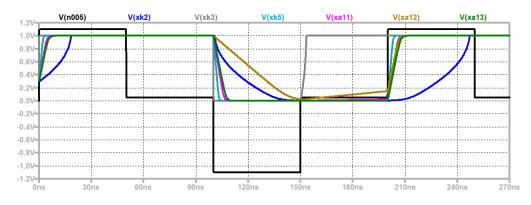


Figure 3. A passive memristor memory crossbar.



**Figure 4.** Time graphs of the voltage and state variable during writing and erasing information in a passive memristor crossbar, according to the described standard models– $K_2$  (Joglekar),  $K_3$  (Biolek),  $K_5$  (Lehtonen-Laiho), and modified memristor models  $A_{11}$ ,  $A_{12}$ , and  $A_{13}$ .

The time diagrams for the voltage, memristance, and state variable, according to the used classical and modified memristor models, are represented in Figure 4 for the confirmation of the proper operation of the considered memory crossbar and for the comparison of the results. The voltage across the memristor is a sequence of pulses with a duration of 50 ns and different polarities and levels. The applied memristor models have a similar behavior, according to the range of memristance's change. The state variable x alters in the range (0.04, 0.97) and is related to a hard-switching operation. The memristance changes between 140.3  $\Omega$  and 14.7 k $\Omega$ . In the writing processes, the Lehtonen-Laiho model and the modified ones represent a rapid change in memristance, presenting their very good switching properties, while the Joglekar and Biolek memristor models are related to a slower alteration of the equivalent resistance. This is an advantage of the modified metal-oxide-based memristor models, according to the Joglekar and Biolek models, and is related to the application of exponential, sine hyperbolic, and polynomial relationships between the time derivative of the memristor state variable and the voltage, representing their better switching behavior. A small change in the memristance during the reading

procedures was observed for the Biolek model due to the absence of activation thresholds. According to the modified memristor models and Lehtonen-Laiho model, the memristance does not alter during the reading processes.

# 4.2. A Hybrid Memory Crossbar

A hybrid memory crossbar based on memristors is presented in Figure 5 for the clarification of its structure and operation. It contains sixteen memristors and eight MOS transistors. The memristors are used as storing elements, while the transistors operate as write-enable and read-enable switches. To enable writing a logical zero or unity in a given memristor, positive voltage pulses must be applied to the gates of the respective MOS transistors.

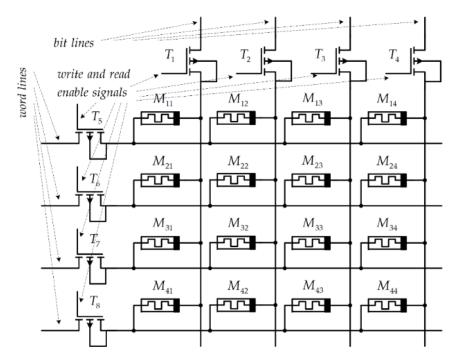
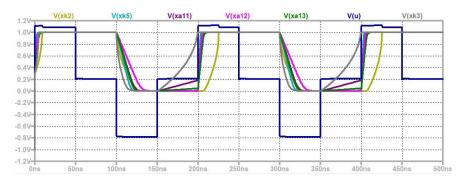


Figure 5. A hybrid memristor memory crossbar.

The time diagrams of the applied pulses, the state variables, and the memristor voltage are presented in Figure 6 for confirmation of the proper operation of the memristor models and for their comparison. According to the rate of changing the state variable and memristance, the modified model A<sub>11</sub> represents the lowest switching time and the highest switching speed, respectively [24,33].



**Figure 6.** Time diagrams of the memristor voltage, memristance, and state variables according to the applied modified and standard metal-oxide memristor models.

### 4.3. A Memristor-Based Neural Network

A simple feed-forward memristor-based neural network is presented in Figure 7 for a further description of its structure and operation [15]. The network is used for the logical XOR function emulation. It contains two input nodes to apply the input logical signals  $x_1$  and  $x_2$ . The neural net contains two hidden layers and an output layer. The first hidden layer is made of four neurons— $N_{11}$ ,  $N_{12}$ ,  $N_{13}$ , and  $N_{14}$  with a nonlinear activation function, and the second one contains three neurons— $N_{21}$ ,  $N_{22}$ , and  $N_{23}$ . The output layer contains a neuron with a linear activation function. The synaptic bonds between the neurons are memristor-based. Supervised learning is applied for the adjustment of the synaptic weights, which are founded on memristors [15,17,19]. The other elements of the artificial neural network are based on CMOS transistors.

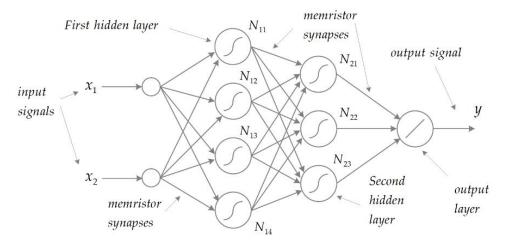


Figure 7. A memristor-based neural network.

The schematic of the applied synaptic circuit is presented in Figure 8 for a description of its topology and principle of operation [15,17,19]. It contains a memristor, three resistors, and an operational amplifier. The input voltage signals are applied to two voltage dividers, which are connected parallel to each other.

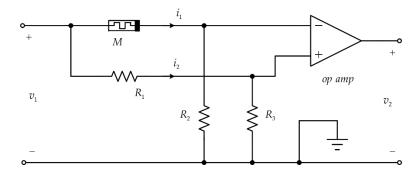


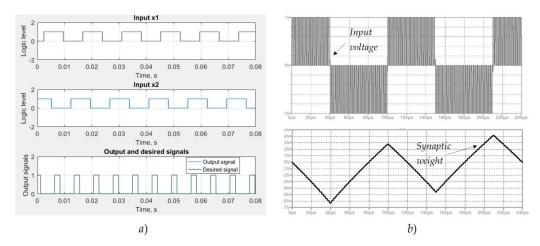
Figure 8. A schematic of the used memristor synapse.

The first voltage divider is made by the memristor M and the resistor  $R_2$ ; the second one contains the resistors  $R_1$  and  $R_3$ . The principle of operation for the considered synaptic circuit is based on a comparison of the voltage drops across the resistors  $R_2$  and  $R_3$ . The weight of the described synaptic circuit w as a function of the memristance M is [15]:

$$w(M) = \frac{v_2}{v_1} = k_v \left(\frac{R_3}{R_1 + R_3} - \frac{R_2}{M + R_2}\right)$$
(10)

The memristance M and the corresponding synaptic weight w are altered by external voltage pulses. After a simple transformation of (10) and applying  $R_2 = R_3$ , it is obtained that if  $R_1 = M$ , then the synaptic weight can be expressed as w = 0. Positive synaptic weights are derived if  $M > R_1$ . When  $M < R_1$ , then w < 0. The scaling of the weights is derived by changing the transfer coefficient  $k_v$  of the operational amplifier. The input voltage signal is a sequence of negative and positive impulse packages. A pulse with a length of 1 µs and a level of 1 V leads to a change in the synaptic weight by 0.23. A negative polarity voltage signal is used for increasing the weights, while their decreasing positive pulses are used. The error signal is represented as a difference between the desired output signal and the respective actual output signal. The level of the error signal is many times lesser than those of the input logic signals. The proper functioning of the neural net and the appropriate convergence of the training procedures are established.

The diagrams of the input voltage signal  $x_1$  and  $x_2$  for testing the neural net are presented in Figure 9a for the expression of the logic signals. They correspond to logic ones and zeros for the emulation of XOR logic operations. The input signals for training the neural net are very similar; they are shifted according to the testing signals to obtain more realistic results. The diagrams of the input signals of the synaptic circuit and its weight alteration are shown in Figure 9b for the description of its adjustment process. The input signals are a sequence of negative and positive packages of pulses.



**Figure 9.** (a) Time diagrams of the input and output signals of the memristor neural network; (b) Diagrams of the input signal of the synaptic circuit and the corresponding alteration of the weight.

The actual and the desired output signals after testing the neural network are represented in Figure 9a for the confirmation of their good matching. The signal of the error of the net is expressed as a difference between the actual and the desired output signals. Its amplitude is about several thousand times less compared to those of the input signals. The proper operation of the net and the good convergence of its training are established.

#### 4.4. A Memristor-Based Amplifier

A differential amplifier based on the metal-oxide memristor and transistors [20] is represented in Figure 10 for the description of its structure and operation. It contains a memristor element in the feedback electronic circuit and three bipolar transistors of the NPN type. The resistors  $R_5$  and  $R_6$  represent the internal resistances of sources  $V_1$  and  $V_2$ . The resistors  $R_1$  and  $R_2$  are applied for the limitation of the collector currents of the transistors  $Q_1$  and  $Q_2$ . The output voltage signal is derived between the collectors of the elements  $Q_1$  and  $Q_2$ , and it is proportional to the difference between the input signals. In the present case, the memristor element operates in a soft-switching state. The tuning of its memristance by external voltage impulses leads to a change in the transfer coefficient of the amplifier.

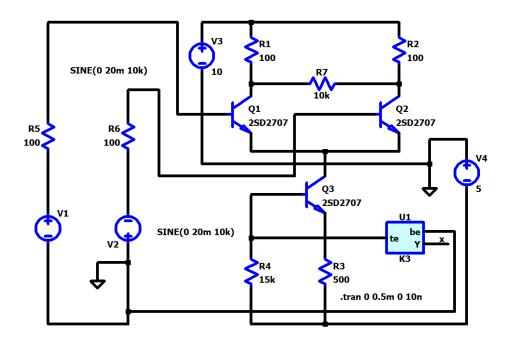
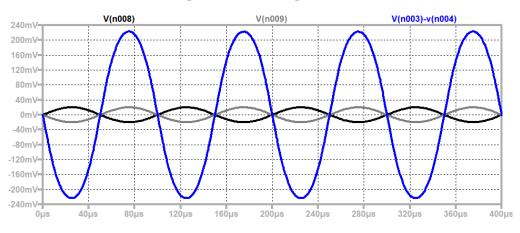


Figure 10. A memristor-based differential amplifier.

The time graphs of the input and output sinusoidal signals are presented in Figure 11 for confirmation of the correct operation of the amplifier.

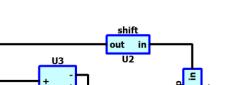


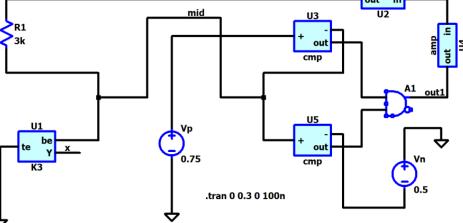
**Figure 11.** Time diagrams of the input and output signals of the memristor-based amplifier presented in Figure 10.

#### 4.5. A Memristor-Based Generator

A schematic of a memristor-based reactance-less oscillator is presented in Figure 12 for the description of its structure and the principle of its operation [22]. It contains a series connection of the resistor  $R_1$  and the memristor  $U_1$ : two comparators based on operational amplifiers, two sources of reference voltages (Vp and Vn), a logical AND gate ( $A_1$ ), an amplifier  $U_4$ , and a shifting module  $U_2$ .

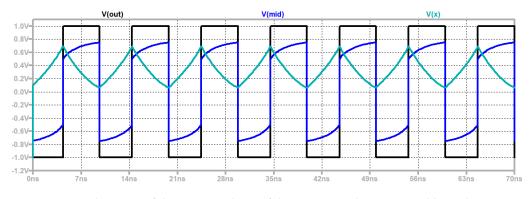
The time diagrams of the signals are presented in Figure 13 for an explanation of the operation of the memristor-based generator.





out

Figure 12. A memristor-based reactance-less generator.



**Figure 13.** Time diagrams of the output voltage of the generator, the state variable, and memristor voltage according to the considered standard and modified memristor models A<sub>11</sub>, A<sub>12</sub>, and A<sub>13</sub>.

In the initial moment, the output voltage of the logic gate AND is zero, and the voltage across the voltage divider  $R_1$ - $U_1$  is equal to -1 V. The metal-oxide memristor  $U_1$  is reversely biased, and the voltage across it leads to an increase in the state variable and a decrease in its memristance. Owing to this, the voltage drop across the memristor increases, and when its value is lower than the reference voltage Vp, then the comparator  $A_1$  produces a logical unity. The output voltage of the generator jumps to 1 V. Then, the state variable of the memristor decreases, and its memristance increases, leading to the arising voltage drop across the memristor. This follows a new jump of the output signal, which becomes equal to -1. In this way, the considered memristor-based schematic produces rectangular impulses. The state variable changes in a comparatively broad range between 0.1 and 0.7, which corresponds to a soft-switching mode. The frequency of the output signal depends on the switching properties of the applied metal-oxide memristor. In the present case, the Lehtonen-Laiho memristor models, followed by the modified models  $A_{11}$ ,  $A_{12}$ , and  $A_{13}$ , represent better-switching properties than the Biolek and Joglekar memristor models.

### 4.6. Memristor-Based Filters

In this section, several memristor-based passive filters are considered and analyzed in the LTSPICE environment [19,32,33].

Low-pass and high-pass passive filters, based on memristors and capacitors, are presented in Figure 14 for a description of their structure and operation [19].

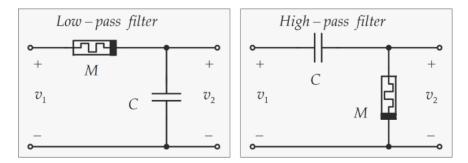


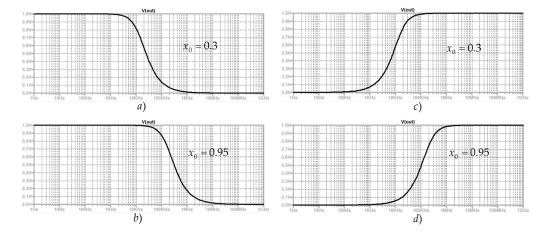
Figure 14. Low-pass and high-pass memristor-based filters.

The capacitance of *C* has a value of 1 nF. In the low-pass filter, the capacitor *C* is connected to the output and shunts it for frequencies higher than the cut-off frequency  $f_{low}$  [19,43]:

$$f_{low}(M) = \frac{1}{2\pi MC} \tag{11}$$

In the high-pass filter, the memristor M is connected in parallel to the output. The cut-off frequency of the high-pass filter is calculated by a formula similar to formula (11). The cut-off frequencies are changed by the alteration in the memristance M, using positive or negative external voltage pulses with levels that are higher than the memristor activation threshold  $v_{thr}$ .

In the considered low-pass and high-pass filters, the memristors operate with signals which have a lower level than their activation thresholds. Memristors behave as linear resistors with a constant value of their resistances. The amplitude-frequency and phase-frequency responses of the low-pass and high-pass filters are presented in Figure 15 for several different values of the memristances to represent the change in the respective cut-off frequencies and the frequency bands.



**Figure 15.** (a) Amplitude-frequency response (AFR) of a low-pass filter for  $x_0 = 0.3$ : (b) AFR of a low-pass filter for  $x_0 = 0.95$ ; (c) AFR for the high-pass filter for  $x_0 = 0.3$ ; (d) AFR for the high-pass filter for  $x_0 = 0.95$ .

By increasing the state variable, the respective memristance decreases, and the corresponding cut-off frequency increases. The change in the state variable in the range from zero to unity ensures the alteration of the cut-off frequency from 9.95 kHz to 1.59 MHz.

In Figure 16, band-pass and band-stop memristor filters are presented for an explanation of their structure and functioning.

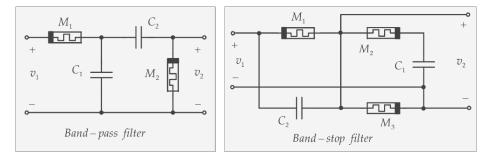


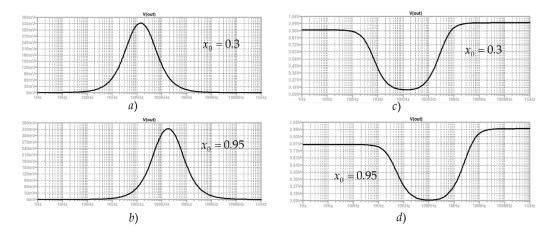
Figure 16. Band-pass and band-stop memristor-based filters.

They are based on the previously discussed low-pass and high-pass filtering groups. The band-pass filter is constructed by a cascade connection of low-pass and high-pass filters. The low-pass filter group contains the elements  $M_1$  and  $C_1$ , while the high-pass filter module is made of the components  $C_2$  and  $M_2$ . In this circuit, the cut-off frequency of the low-pass filter flow is higher than those of the high-pass filter— $f_{high}$ —so the frequency band of the equivalent band-pass filter is [18,43]:

$$f_{bp}(M) = f_{low} - f_{high} \tag{12}$$

The band-stop memristor filter is presented in Figure 16 for a further description of its structure and principle of operation. It is based on a parallel connection of low-pass and high-pass filtering groups. The low-pass filtering module is realized by the elements  $M_1$  and  $C_1$ , and the high-pass filter by  $C_2$  and  $M_3$ . The memristor  $M_2$  is connected in series to the capacitor  $C_1$ . It ensures a non-zero level of the output voltage for very high frequencies.

In Figure 17, the amplitude-frequency and phase-frequency responses of the considered band-pass and band-stop filters at different memristance and state variables are presented for a further explanation and description of the filters' characteristics and properties.



**Figure 17.** (a) Amplitude-frequency response (AFR) of a band-pass filter for  $x_0 = 0.3$ : (b) AFR of a band-pass filter for  $x_0 = 0.95$ ; (c) AFR for the band-stop filter for  $x_0 = 0.3$ ; (d) AFR for the band-stop filter for  $x_0 = 0.95$ .

## 5. A Comparison of the Used Memristor Models

The models of metal-oxide memristor elements, which were applied for the investigation of the discussed memristor-based digital and analog devices, are compared one to another according to their behavior in the electric field and the use of some basic criteria for comparison, including the complexity, operating frequency, simulation time, activation thresholds, RMS error, accuracy, switching properties [6,16]. Some of these criteria are numerically evaluated, while others are introduced by qualitative expressions [3,33,36]. The results are presented in Table 1 for a comparison of the memristor models.

Model	Joglekar	Biolek	Lehtonen-Laiho	A <sub>11</sub>	A <sub>12</sub>	A <sub>13</sub>
Complexity	low	low	high	low	low	low
Operating Frequency	low	middle	high	high	high	high
Simulation Time,ms	16.42	16.74	18.65	17.23	17.34	17.28
Activation Thresholds	no	no	no	yes	yes	yes
RMS Error	6.24	5.87	3.63	3.75	3.68	3.72
Accuracy	low	middle	high	high	high	high
Switching Properties	middle	middle	good	good	good	good

**Table 1.** A comparison of the applied standard and modified memristor models according to several important criteria.

The *complexity* of a metal-oxide memristor model is proportional to the time needed for a computer simulation. It depends mainly on the number of mathematical operations that are included in the respective model. This measure is introduced rendering to the complexity, and the modified metal-oxide memristor models  $A_{11}$ ,  $A_{13}$ , and  $A_{12}$  are simpler compared to the standard Lehtonen-Laiho memristor model. The modified and improved models  $A_{11}$ ,  $A_{13}$ , and  $A_{12}$  have a high operating frequency and accuracy, very near to that of the classical Lehtonen-Laiho model. Due to their lower complexity, the modified models of  $A_{11}$ ,  $A_{13}$ , and  $A_{12}$  are suitable for the analysis of electronic devices and circuits with a large number of memristors.

The *operating frequency* of a metal-oxide memristor model depends on its capability to correctly express the alteration of the memristor state variable and the respective memristance proportionally to the voltage signal across the memristor. For models of metal oxide memristors, a low frequency is recognized between 0.5 Hz and 1 kHz, a middle frequency between 1 kHz and 100 kHz, and high frequencies are higher than 100 kHz. The standard Lehtonen-Laiho memristor model has the highest operating frequency, followed by the enhanced and modified memristor models of  $A_{11}$ ,  $A_{13}$ , and  $A_{12}$  and the standard Joglekar and Biolek memristor models.

The *simulation time* of a metal-oxide memristor model is proportional to the elementary mathematical operations in the equations of the model. In the considered analyses, the classical Joglekar and Biolek memristor models have the lowest simulation time, followed by the modified models  $A_{11}$ ,  $A_{13}$ , and  $A_{12}$  and the standard Lehtonen-Laiho model. Due to this, the Joglekar, Biolek,  $A_{11}$ ,  $A_{13}$ , and  $A_{12}$  have a very high operating speed, followed by the classical Lehtonen-Laiho model.

Memristor models, which include *activation thresholds* are able to operate in different modes in hard-switching, soft-switching, and as linear resistors when the state variable and the respective memristance have constant values and do not depend on the applied voltage. When the applied signal exceeds the activation threshold, then the state variable changes proportionally to the time integral of the applied voltage.

The root mean square (RMS) error of a memristor model is represented as a square root of the sum of the squared differences between the simulated and the experimental currents of the memristor. In the present case, the classical Lehtonen-Laiho memristor model is able to represent current-voltage characteristics of metal-oxide memristors with a minimal RMS error, followed by the modified models of  $A_{11}$ ,  $A_{13}$ , and  $A_{12}$ , and the standard Biolek and Joglekar memristor models.

The *accuracy* of a memristor model is inversely proportional to the root mean square error. It is expressed using a qualitative comparison according to the previously discussed RMS error. When the RMS error of a memristor model is lower than 4%, the respective

accuracy of the model has a high value. If the RMS error of a memristor model is between 4 and 6%, then it has a middle accuracy. A low accuracy is related to an error higher than 6% [33]. The classical Lehtonen-Laiho model has the highest accuracy, followed by the modified memristor models A<sub>11</sub>, A<sub>13</sub>, A<sub>12</sub>, Biolek, and the Joglekar models.

The *switching properties* of a memristor model are associated with the quick change in the state variable and the corresponding memristance under voltage impulses. The Lehtonen-Laiho memristor model has the best switching properties, followed by the modified models A<sub>11</sub>, A<sub>13</sub>, A<sub>12</sub>, and the standard Biolek model.

## 6. Discussion

In this work, several memristor-based electronic devices and circuits are discussed and analyzed. Memristor-based non-volatile memory crossbars, a neural network, an amplifier, a reactance-less oscillator, and filters with memristors were analyzed and designed in the LTSPICE environment using several of the mainly applied standard memristor models, together with some modified and improved metal-oxide memristor models. The analysis of the considered memristor-based circuits confirms that the used modified and enhanced metal-oxide memristor models, together with the standard Lehtonen-Laiho model, have better-switching properties than the classical Biolek memristor model. The standard Biolek and Joglekar models and the user-modified metal-oxide memristor models have a faster operation in SPICE due to their simplified expressions. They are successfully applied for the analyses of complex memristor-CMOS electronic circuits.

# 7. Conclusions

The modified and classical metal-oxide memristor models considered in this work are applied in several memristor-based electronic circuits and devices, including passive and hybrid memory crossbars, a neural network, filters, an amplifier, and a generator, while the analyses and simulations are conducted in LTSPICE environment. The operation of the modified memristor models is compared to those of the classical Joglekar, Biolek, and Lehtonen-Laiho models, according to their behavior in the electric field and several significant criteria for comparison, including complexity, operating frequency, switching properties, simulation time, accuracy and the use of activation thresholds. The modified metal-oxide memristor models and the classical Lehtonen-Laiho model have better-switching properties than the standard Strukov-Williams, Joglekar, and Biolek models and are efficiently applicable in different complex electronic circuits owing to their simplified expressions and good accuracy. The used flat and differentiable step-like sigmoidal function, the respective window functions based on the sigmoidal function, and the application of LTSPICE software partially prevent convergence issues during the analyses. The design of memristor-based electronic schemes in the LTSPICE environment, using modified metal-oxide memristor models, is an important stage in the realization of improved, high-density, and low-power integrated circuits.

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