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Design and Optimization of Collection Efficiency and Conversion Gain of a Buried P-Well SOI Pixel X-ray Detector

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Abstract: Buried P-Well (BPW) technology was used in silicon-on-insulator pixels (SOIPIX) to suppress the back-gate effect, the major challenge in SOIPIX. In this work, we have designed and optimized two novel pixel structures, which are based on different BPW design layouts, to study the carrier collection efficiency and conversion gain of the pixel unit used in SOIPIX X-ray detectors. The first structure has an extended BPW region connected with a P+ node. In the second structure, a separated BPW ring region is formed surrounding the P+ node. Two X-ray sources with different photon energies have been applied in the simulation of excess carrier generation. The results indicated that the first structure had higher collection efficiency while the second structure had a slightly better conversion gain. As a result, the total photoelectric voltage of the first structure is about two times that of the second structure, where low doping concentration (<1 × 10¹⁶ cm⁻³) in the BPW region is preferred. Such a study of design and optimization of BPW technology is very important for applications in SOIPIX detectors.

Keywords: SOI; X-ray detector; collection efficiency; conversion gain

1. Introduction

In the field of medical diagnostics, digital X-ray imaging systems have been under investigation and development for a long time [1]. According to the different conversion principles, there are two kinds of X-ray imaging systems, direct and indirect systems [2]. The indirect imaging system first converts X-ray information to visible light by using scintillators and then converts the visible light into electrical signals through photodiodes [3]. The direct imaging system directly converts the X-ray information into electrical signals by using an X-ray conversion film (i.e., X-ray photoconductor). Since the direct system does not need scintillators, energy absorption efficiency can be improved and the inherent deterioration of spatial resolution can be avoided [3,4].

The SOI X-ray detectors have recently been integrated in the direct imaging system [5]. Some progress in this area has been achieved by Miyoshi et al. [6–8], who studied X-ray astronomy with SOI CMOS technology. Niemiec et al. have also researched monolithic X-ray detectors with thick film SOI technology [9]. These detectors are composed of a thick, high-resistivity substrate (the X-ray absorption layer) and a relatively thin, low-resistivity Si layer (on which to build readout circuits) sandwiching a buried oxide (BOX) layer in a single wafer. It is different with the conventional hybrid X-ray detectors in which the detector chips and readout circuit chips are fabricated on different wafers [10].

Compared with the conventional bulk CMOS circuits, the readout circuits fabricated on the SOI wafer have less parasitic capacitance due to the thin top Si layer, enabling higher speed and lower power consumption [11]. The SOI-based circuits are quite immune to latch-up phenomena [12]. In addition, since the active SOI transistor is very thin, most radiation-induced electron-hole pairs are blocked by the BOX layer, and the SOI device is less sensitive to single event upset (SEU) and single event transient (SET) [13].

As mentioned above, SOI detectors are very promising for X-ray imaging. However, it should be noticed that SOIPIX is difficult to be implemented in circuits [14]. One of the major challenges is the back-gate effect: the silicon substrate under the BOX layer acts as a back gate for the transistors built on SOI. Therefore, the potential of silicon substrate affects the threshold voltage and leakage current of the transistors [15]. Buried P-Well (BPW) technology has been proposed by KEK [16,17] to suppress the back-gate effect. In addition, BPW has an impact on the pixel characteristics because it affects the carrier collection process. In this work, in order to optimize the collection efficiency and the conversion gain, we have designed and optimized SOIPIX X-ray detectors with different BPW structures. By varying the size and position of the buried P-Well, we have studied the dependence of the collection efficiency and the conversion gain on device geometry. We have also studied the impact of BPW doping concentration on the pixel characteristics.

2. Pixel Structure

This study is based on a fully-depleted (FD) SOI 0.2 μ m process [18,19]. In this design and simulation, the SOI wafer consists (after thinning) of a 260 μ m thick, N-type, 700 Ω /cm Si substrate, 200 nm thick BOX and a 40 nm thick SOI on which the readout circuits are fabricated. In this work, a diode structure has been used in the design and simulation of X-ray pixel sensors in the Si substrate.

The device configurations A and B are shown in Figure 1. First, a "window" is opened in the BOX and SOI. Through the window, a P-type doping region is designed to form a P+ node (doping concentration $\approx 1 \times 10^{20}$ cm⁻³, depth = 1 µm). The P+ node is used to connect to the readout circuits. Another important doping region is BPW, which is used to suppress the back-gate effect. The doping level in the BPW region (depth = 0.3 µm) is about three orders lower than that in the P+ node. d_1 is the half width of BPW in structure A. d_2 is the separation distance between BPW ring and pixel center. The detailed explanation of these structures is shown in Table 1. To reverse-bias the photodiode, 400 V is applied at the bottom contact of the N-type Si substrate [20]. Under such a high reverse-bias voltage, full depletion can be achieved in the Si substrate. In addition, two neighboring pixels separated by 20 µm were added to eliminate the boundary effect. The recombination process in the substrate is modeled with the Shockley-Read-Hall model and the lifetime is set as 1 µs. The top silicon is grounded. The BPW ring in structure B is reset as 0 V and will remain floating during the carrier collection process.

Structure	Explanation
Structure A: pixel with extended BPW	The width of the extended BPW is $(2 \times d_1) \ \mu m$
Structure B: pixel with a BPW ring	BPW ring needs other additional P+ nodes to fix its potential; the width of the ring is 3 μ m and the width of these four P+ nodes is 1 μ m; the separation between the P+ node and the BPW ring is $d_2 \mu$ m.

Table 1. Description of two types of pixel structures.



Figure 1. The top view (left) and the cross-section view (right) of Structure A and Structure B.

3. Incident X-ray Penetration Model

After the design of devices, the X-ray detector is simulated and analyzed. Penetration location (d_3) refers to the distance between the X-ray hitting point and the target pixel center. As shown in Figure 2, d_3 has been scanned (from 0 to 7 μ m) during the pixel simulation.



Figure 2. The penetration model with the incident X-ray. The pixel pitch is 20 μ m. During the simulation, we assumed that the X-ray penetrated into the absorption layer vertically from the top surface. The depth of whole absorption layer is 260 μ m.

In this work, the HeavyIon model [21] has been applied to simulate the generation of non-equilibrium carriers. This model is used to define the linear energy transfer (LET). Therefore, it is applicable in modelling the photoelectric effect of incident X-rays. In this work, LET values are computed based on the datasheet of X-ray sources used in the research of Nishimura et al. [22], where two monochromatic X-ray (33.3 keV and 9.5 keV) stations were used. The detailed parameters used in this work are shown in Figure 3.



Uniform excess carrier generation

Piece-wise function of excess carrier generation

Figure 3. Modeling of photoelectric effect of the incident X-rays with different photon energy (33.3 keV, 9.5 keV). For 33.3 keV X-ray source, 5% photons are absorbed uniformly along the penetration path in the substrate. For 9.5 keV X-ray source, 50% photons can be absorbed in 90 μ m substrate. The whole substrate (260 μ m) can absorb 90% incident photons.

4. Simulation Results

4.1. Electric Potential Distribution

In order to study the carrier collection process in different pixel structures, the electric potential distribution has been simulated. As the photodiode is reverse-biased with a high voltage, the depletion region expands to nearly the whole substrate [23]. Therefore, different pixel structures have the same potential distribution in the region far away from the P+ anode or BPW, namely the region at $y > 10 \mu$ m. The potential distribution in these remote regions can be considered by using a simple PN junction model.

However, the electric potential profile of the region close to the pixel P+ node exhibits large differences. As shown in Figure 4, in Structure A, the potential in the extended BPW region is pinned to ground through the P+ node. The potential well induced by the BPW region is connected to the one induced by the P+ node. Therefore, the total space of potential wells which can collect carriers is expanded [24]. In Structure B, the BPW ring is separated from the P+ node, which exhibits three separated potential wells in the two-dimensional (2D) cross-section view.



Figure 4. Two-dimensional electric potential profile near the P+ node in Structure A (**a**) and in Structure B (**b**).

4.2. Carrier Collection Efficiency

During the carrier collection process, the non-equilibrium carriers cannot be completely collected by the target pixel due to carrier lateral diffusion, carrier recombination, and trapping effects [25]. We simulated the collection efficiency of different pixel structures and compared the results by considering all these effects. Here, the carrier efficiency was defined as a ratio of the number of carriers collected by the target pixel over the number of carriers generated in the whole absorption layer [26].

The simulation results shown in Figure 5 illustrate the influence of BPW size (in structure A) and BPW ring location (in structure B) on the collection efficiency with a 33.3 keV X-ray source applied. In Structure A, higher collection efficiency can be obtained with larger BPW. This is because, with the larger expanded BPW region, more holes can be collected by the expanded potential well. It is also noticed that the collection efficiency slightly decreases when d_3 (i.e., the distance between the X-ray hitting point and the target pixel center) increases. The decrease of the collection efficiency is mainly due to the carrier recombination in the bulk and the region beneath the surface. It is found that when $d_3 = 7 \mu m$, half of the excess carriers are recombined in the bulk or in the surface. Only less than 9% of the carriers are collected by neighboring pixels. It should be noted that the carrier collection can be significantly affected by the substrate and interface/surface quality. This is not the focus of this work and will be further investigated in future research.

In structure B, the collection efficiency is much more sensitive to the X-ray penetration location. Most of the lost carriers are collected by the BPW ring. When d_3 is 4 µm, nearly half of excess carriers are collected by the BPW ring and the number of carriers diffusing to the next pixel decreases to less than 5%.

We also studied the influence of BPW doping concentration on the collection process with 33.3 keV X-ray source applied. The penetration location has been set as $d_3 = 4 \mu m$ to better illustrate the doping concentration effect. This is because the BPW effect is relatively less obvious with shorter d_3 . It is expected that the collection efficiency can be improved with the larger BPW in Structure A according to the results in Figure 5. Indeed, as shown in Figure 6 the improvement is achieved only when BPW doping concentration is large enough (>1 × 10¹⁷ cm⁻³). While in Structure B, low concentration of BPW is preferred in order to obtain higher collection efficiency. This is because the BPW ring in structure B can collect more carriers with higher doping concentration, while those carriers are not collected by the target pixel node.



Figure 5. Collection efficiency of Structure A (**a**) and Structure B (**b**) vs. d_3 at different d_1 and d_2 . The BPW ring has constant width of 3 μ m. The X-ray photon energy is 33.3 keV.

Through the integration of the collection efficiency with the X-ray penetration location (shown in Figure 5), it is found that the ratio of the collection efficiency in Structure A and in Structure B is 2.15–2.22. A high ratio can be achieved with a high BPW doping concentration and large BPW in Structure A.



Figure 6. Cont.



Figure 6. Impact of BPW doping concentration on the collection efficiency of Structure A (**a**) and Structure B (**b**). The X-ray penetration location is at the center of the target pixel. d_1 is the BPW width in Structure A. d_2 is the separation distance between BPW ring and the pixel center in Structure B. The BPW ring has constant width of 3 µm.

As two X-ray sources were studied in our research, the effect of different photon energy has been compared. As shown in Figure 7, the pixel exhibits higher collection efficiency with 9.9 keV X-ray source in both structure A and structure B. This effect is related to the shallow energy deposition location of the X-ray with small photon energy. For a 9.9 keV X-ray source, most carriers are generated close to the collection node and their lateral diffusion and recombination would be suppressed.

4.3. Conversion Gain

As discussed above, BPW technology, indeed, creates an additional potential well which is beneficial to the improvement of the carrier collection efficiency. However, it also increases the pixel capacitance, which may decrease the conversion gain [27]. In this work, we have re-designed the pixel with different BPW doping concentration to achieve the optimal conversion gain.



Figure 7. Cont.



Figure 7. The impact of the X-ray photon energy on the collection efficiency of Structure A (**a**) and Structure B (**b**). In both Structure A and Structure B, higher collection efficiency can be observed with $E_{photon} = 9.5$ keV.

Figure 8 shows the conversion gain with a 33.3 keV X-ray source. It is found that the influence of BPW doping concentration on the conversion gain is more significant in Structure A. The conversion gain decreases to $1.5 \ \mu\text{V/e}^-$ from $5 \ \mu\text{V/e}^-$ when the BPW doping concentration increases to $1 \times 10^{17} \text{ cm}^{-3}$. In Structure B, the conversion gain is not significantly affected by the doping concentration and the location of the BPW ring. The gap between the P+ node and the BPW ring is so large that the carriers collected in the BPW ring will not affect the voltage of the P+ node. The ratio of the conversion gain in Structure A and Structure B is about 0.93–0.31. A high ratio can be obtained with low BPW doping concentration in structure A.

The same conversion gain is also obtained with a 9.5 keV X-ray source. This makes sense since the conversion gain is mostly decided by the pixel structure, but not affected by the incident X-ray photon energy.



Figure 8. Cont.





Figure 8. The impact of the BPW doping concentration on the conversion gain for (**a**) Structure A and (**b**) Structure B. The X-ray penetration location is at the center of the target pixel. The BPW ring has a constant width of 3 μ m.

5. Conclusions

In this work, we have designed and optimized two novel SOI pixel structures based on BPW technology. The photoelectric effect of two X-ray sources with different photon energy has been modeled in the simulation. We found that larger extended BPW could offer better collection efficiency. This improvement of collection efficiency prefers a high doping concentration of the BPW region. For the pixel with a separated BPW ring, the large separation between the BPW ring and the P+ node is preferred. Additionally, the low doping concentration of BPW ring offers better carrier collection efficiency in this structure. For the conversion gain optimization, a low doping concentration of the BPW ring, the conversion gain only varies slightly with the different location and doping concentration of the BPW ring. Taking both carrier collection efficiency and conversion gain into account, the photoelectric voltage of the pixel with the extended BPW is preferred. This study of design and optimization of BPW ring, where the low doping concentration of BPW is preferred. This study of design and optimization of BPW ring, where the low doping concentration of BPW is preferred. This study of design and optimization of BPW ring, where the low doping concentration of BPW is preferred. This study of design and optimization of BPW technology is very important for applications in SOIPIX detectors

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