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Impact of the Deposition Temperature on the Structural and Electrical Properties of InN Films Grown on Self-Standing Diamond Substrates by Low-Temperature ECR-MOCVD

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Abstract: The progress of InN semiconductors is still in its infancy compared to GaN-based devices and materials. Herein, InN thin films were grown on self-standing diamond substrates using low-temperature electron cyclotron resonance plasma-enhanced metal organic chemical vapor deposition (ECR-PEMOCVD) with inert N_2 used as a nitrogen source. The thermal conductivity of diamond substrates makes the as-grown InN films especially attractive for various optoelectronic applications. Structural and electrical properties which depend on deposition temperature were systematically investigated by reflection high-energy electron diffraction (RHEED), X-ray diffraction (XRD), scanning electron microscopy (SEM), atomic force microscopy (AFM), and Hall effect measurement. The results indicated that the quality and properties of InN films were significantly influenced by the deposition temperature, and InN films with highly c-axis preferential orientation and surface morphology were obtained at optimized temperatures of 400 °C. Moreover, their electrical properties with deposition temperature were studied, and their tendency was correlated with the dependence on micro-structure and morphology.

Keywords: InN; self-standing diamond substrates; deposition temperature

1. Introduction

Due to their current and potential applications in an extensive range of electronic and optoelectronic devices, group III nitride semiconductors (AlN, GaN, InN) have received significant attention in the last 10 years and are considered as important technical materials [1–6]. Indium nitride (InN), which has a narrow band gap (~0.7 eV) and other extraordinary attributes, plays the key role in expanding the working wavelengths of group III nitride devices from the visible and ultraviolet to the near-infrared spectrum [7–10]. Moreover, the band gaps of ternary alloys, such as InGaN and InAlN, which are composed of InN, GaN and AlN, can cover the whole range of visible light. This enables the design of various colored light-emitting diodes (LEDs) and solar cells with relatively high efficiency. Furthermore, with its good bandgaps and performances, InN has been widely employed in applications of photoelectric devices, semiconductor devices with good properties and high-performance infrared detectors [8–14]. However, the progress of InN semiconductors is still in its infancy. Actually, quantum efficiency inside a GaN/InGaN solar cell prepared on a sapphire substrate is as high as 60% [12]. Therefore, various methods have been used to prepare InN thin films with high quality to achieve wide practical applications [15–23]. Moreover, the wavelength emitted by InN-based materials can

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reach 1.55 μ m, which belongs to a communication band with a long wavelength. They can enrich optional materials of optical communication equipment and can bring new breakthroughs to the development of optical communication devices. In terms of electricity, theoretical studies prove that InN has good properties, for example, better properties of electron transport in both transient and steady-state situations and higher mobility (14,000 cm²/V·s) and top rate (4.3 × 10⁷ cm/s). All these features result in a unique advantage of InN in applications of high-frequency high-speed transistors and other electronic devices.

Although InN materials have unique photoelectric characteristics and potential applications, optimum-quality InN thin film growth is, to date, well developed than the nitrides of other third main group elements. This is mainly due to the following issues that need to be addressed. Firstly, it is difficult to prepare InN bulk materials because of the high pressure of nitrogen decomposition and low temperature of InN decomposition. Secondly, it is difficult to find substrate materials which can match the coefficient of expansion and lattice constant for film growth. Thirdly, optional substrates are hard to find with good performance of heat dissipation, due to the continuous durability and high heat dissipation required by high-power and high-frequency devices. Growth methods, such as metal organic chemical vapor deposition (MOCVD) [24,25], magnetron sputtering and plasma-enhanced molecular beam epitaxy (PE-MBE) [26–31], have already been adopted for the growth of InN films in recent years. Albeit these methods are used to grow InN thin films successfully on heterogeneous substrates such as α -Al₂O₃, the heat dissipation of these substrates is not ideal. With regard to the application of high-frequency and high-power devices, it is still challenging to find suitable substrates.

With good thermal stability and high heat conductivity, diamond has dramatically improved the solidity of devices with high frequency and high power. In recent years, much progress has been achieved in the preparation of diamond films by CVD [32–35]. Diamond films of high quality and large area have been successfully deposited on substrates of metal or silicon. However, these deposition methods usually need a high temperature with relatively high energy consumption. Therefore, growth by low temperature of optimum-quality InN thin films on self-standing diamond substrates is the main issue in applications of InN-based optoelectronics and electronic devices with high power and high frequency. Electron cyclotron resonance plasma-enhanced metal organic chemical vapor deposition is an innovative technology of thin film deposition, which absorbs the advancement of MOCVD and MBE, and this method uses microwave electron cyclotron resonance discharge to generate high-density charged and excited particles at low pressure.

As a new kind of thin film growth technology, ECR-PEMOCVD absorbs the advancement of MOCVD and MBE. A charge with high density and stimulated particles can be produced from this method by use of microwave electron cyclotron resonance discharge at low pressure. When active precursors were provided by the cavity-coupling ECR plasma source, the deposition temperature was effectively reduced and the reactivity of N₂ can be significantly increased by the process of ECR [26]. The formation of InN thin films at a low temperature was achieved by this method. Charges with high density and excited particles can be generated at low pressure. Under such conditions, atmosphere is easy to complete in the chemical reaction on a substrate and then deposit as a thin film. The low decomposition temperature means that the growth temperature of the InN film is generally not higher than 600 °C. This is because the N equilibrium partial pressure in InN growth increases sharply with the increase in growth temperature. In order to inhibit the volatilization of N and In, InN must grow at a low temperature. Since the lattice constant of diamond is 0.357 nm and the lattice constant of InN with a-axis is 0.3533 nm, the mismatch of lattice between diamond (111) and InN (0002) is 19.2%. Therefore, compared with traditional plasma-enhanced chemical vapor deposition (PECVD), ECR-PEMOCVD is more suitable for the deposition of this kind of thin film [36–38]. In this study, InN thin films on a self-standing diamond substrate were successfully prepared by an electron cyclotron resonance plasma-enhanced metal organic chemical vapor deposition [36,37] system and their properties were also studied.

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2. Experimental Methods

InN thin films were produced on a self-standing diamond substrate at various deposition temperatures by ECR-PEMOCVD. Independent CVD thick diamond films were adopted as substrates with thicknesses of 0.5–0.8 mm, which were prepared on Mo substrates by direct current (DC) glow discharge PCVD [33,34]. In order to acquire freestanding thick diamond films, the laser stripping was used to remove the Mo substrates. The independent diamond films cannot be adopted as InN deposition substrates because of the rough surfaces, therefore, diamond films with smooth nucleation surfaces were used for the growth surfaces. Firstly, diamond films with nucleation surfaces were mechanically polished to meet the demand of InN film deposition. Secondly, the diamond substrates were immersed in a mixture of 3:1 sulfuric acid and phosphoric acid at room temperature for 24 h, so a thin layer of metal carbide formed by high temperature for a long time was removed. Finally, toluene, acetone, ethanol and deionized water were used sequentially for substrate ultrasonic cleaning, N₂ was used for drying, and the substrate was ultimately introduced into the reaction chamber. The substrate was polycrystalline diamond, which is diamond with a (111) and (220) preferential orientation. H₂ was discharged through the microwave system. Under the conditions of plasma gas discharge, the self-standing diamond substrate was rinsed at a 60 sccm hydrogen flow rate and a 650 W microwave power for 0.5 h at room temperature. Finally, cleaned substrates were ready for the subsequent deposition.

The trimethyl indium (TMIn) and high-purity N_2 were adopted as indium and nitrogen sources, respectively. The ionization reaction of N_2 was dramatically improved by ECR with H_2 as a carrier gas. A semiconductor cold trap was adopted to accurately keep the TMIn temperature at 20 ± 1 °C. The enhanced effect of ECR collects a large number of activated nitrogen ions on the substrate, which was crucial for the formation of InN films. The lattice mismatch between InN films and diamond substrates should be alleviated effectively, so a buffer layer was required. Buffer layer films were prepared under conditions of a 60 sccm N_2 flow rate at room temperature, a trimethyl indium (TMIn) 0.3 sccm flow rate and 30 min of preparation. The growth was continued for 180 min under the conditions of a microwave power of 650 W and flow rates of TMIn and N_2 of 0.6 and 100 sccm, respectively. When the temperature of deposition varied from 200 to 600 °C, the effects of temperatures on the structure and the thin film properties were studied. The temperature was accurately manipulated by a computer-based temperature controller. The thickness of the InN films was up to ~350 nm.

The properties of structure and preferential orientation of films were characterized by X-ray diffraction (XRD, D/Max-2400, Cu $K\alpha_1$: λ = 0.15406 nm, Rigaku, Tokyo, Japan) and reflection high-energy electron diffraction (RHEED). Film surface morphology was analyzed by scanning electron microscopy (SEM) and atomic force microscopy (AFM). The electrical properties were obtained with a Hall effect measurement system (HL5500, Bio-red, Almada, Portugal) and X-ray photoelectron spectroscopy (XPS).

3. Results and Discussion

3.1. RHEED Measurement of InN Thin Films Prepared at Varying Temperatures

As shown in Figure 1, RHEED was used to characterize InN thin films prepared at different deposition temperatures. As the temperature increased from 200 to 400 °C, RHEED patterns of InN films gradually changed from continuous rings to broken rings, and the resolution of patterns became higher. However, if the temperature further increased from 400 to 600 °C, the RHEED patterns gradually changed from broken rings back to continuous rings, and the pattern resolution became lower. This indicates that the RHEED results were dramatically influenced by deposition temperature. For the temperature at 400 °C, the prepared films showed high-resolution RHEED patterns of broken rings. Further analysis reveals that only (002) and (004) diffractions appeared from the middle of the patterns. (101) and (103) crystal planes were also clearly observed at other l° cations. Prepared InN thin films showed preferential orientation at 400 °C. Their direction of growth was perpendicular to

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the substrate with a preferential orientation of high c-axis. Poor crystallization of prepared films was obtained at other temperatures, indicating that the optimal temperature was 400 $^{\circ}$ C.

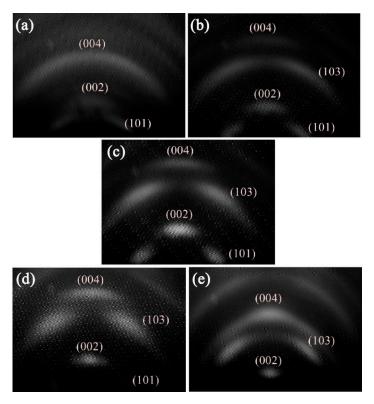


Figure 1. Reflection high-energy electron diffraction (RHEED) patterns of the InN thin films deposited at (a) 200 °C, (b) 300 °C, (c) 400 °C, (d) 500 °C and (e) 600 °C.

3.2. XRD of InN Thin Films Prepared at Varying Temperatures

The XRD pattern was adopted to characterize the crystallization properties of InN thin films, as shown in Figure 2. The lattice orientations at 43.9° and 75.4° are attributed to (111) and (220) diffraction peaks of the substrates. The strongest diffraction peak at 31.3° is the diffraction peak of the InN films, which is the InN (002) diffraction peak. The InN thin film sample showed a preferred orientation of high c-axis. The as-grown InN films were perpendicular to the self-standing diamond substrates. The effect of varying temperatures on the properties of the films was further studied according to Scherrer's formula:

$$d = \frac{0.94\lambda}{B \cdot \cos \theta} \tag{1}$$

where d is grain size, λ is wavelength of Cu $K\alpha$ rays (0.15406 nm), θ is Bragg diffraction angle and B is full width at half maximum (FWHM) of the films. The stress of the films was calculated based on the stress formula:

$$\sigma = \frac{2c_{13}^2 - c_{33}(c_{11} + c_{12})}{2c_{13}} \frac{c - c_0}{c_0}$$
 (GPa) (2)

where c_{11} , c_{12} , c_{13} , c_{33} are elastic constants of InN crystal ($c_{11} = 190$ GPa, $c_{33} = 182$ GPa, $c_{12} = 104$ GPa, $c_{13} = 121$ GPa) [35] and c and c_{0} are the c-axis lattice constants of InN films and complete crystal. The variation of the stress on films prepared at different temperatures was studied. The c-axis diffraction peak positions, FWHM, grain size, tested c-axis lattice constants and calculated film stress under varying deposition temperatures are summarized in Table 1. Compared with the reported lattice constant in previous work [38], the lattice parameter is relatively large, and this is probably caused by the film stress. The increase in deposition temperature from 200 to 400 °C resulted in a gradual reduction in both c-axis lattice constant and FWHM and a gradual increase in grain size. However, a further

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increase in temperature from 400 to 600 °C led to a gradual growth in both c-axis lattice constant and FWHM, and a gradual decrease in grain size. Therefore, the films prepared at 400 °C have a larger grain size and smaller FWHM. Deposited by ECR-PEMOCVD system at lower temperatures, N2 (nitrogen source of the reaction) is not fully dissociated due to less efficient ionization. The dissociated particles could not move to proper positions due to lower migration energy, resulting in a poor crystallization quality of InN films. However, the films at higher temperatures exhibit higher stress, although higher temperatures improve both the ionization reaction efficiency of N₂ and migration energy of particles, and the reason can be ascribed to the lattice and thermal mismatch between film and substrate [35]. According to the stress formula, the negative sign means that the value of c is larger than that of c_0 . This indicates that the lattice along the c-axis is lengthened and, correspondingly, the films have compression stress inside. This is probably caused by the difference in thermal expansion between InN (4×10^{-6}) K) and diamond (3.7×10^{-6}) K). Meanwhile, the films are InN and the substrate is diamond. They have different lattice parameters and thermal expansion. Thus, there exists a mismatch of the lattice parameter and thermal expansion during the heating process. Higher thermal stress generated interfacial defects between the films and the substrates. More importantly, a high temperature may cause the decomposition of InN films and the appearance of metal indium droplets, which would adversely deteriorate the film quality. Therefore, InN films prepared at 400 °C are of higher crystallinity, in good agreement with the RHEED results.

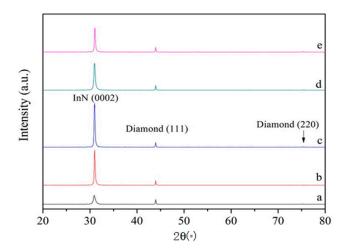


Figure 2. XRD patterns of InN thin films deposited at (a) 200 °C, (b) 300 °C, (c) 400 °C, (d) 500 °C and (e) 600 °C.

Table 1. InN (0002) diffraction peak position, full width at half maximum (FWHM), grain size, lattice	5
constant and stress.	

Sample	Temperature (°C)	2θ (°)	FWHM (°)	Size (nm)	Lattice Constant c ₀ (nm)	Stress σ (GPa)
a	200	31.17	0.38	26	0.5746	-0.69
b	300	31.27	0.24	31	0.5724	-1.24
С	400	31.30	0.17	47	0.5708	-1.96
d	500	31.26	0.28	31	0.5737	-2.52
e	600	31.19	0.31	28	0.5758	-3.42

3.3. SEM Analysis of InN Thin Films Prepared at Varying Temperatures

Due to the great differences in lattice constants between InN films and self-standing diamond substrates, SEM was used to analyze InN films, as shown in Figure 3. The increase in temperature from 200 to $400\,^{\circ}$ C results in higher surface island densities of InN thin films. However, the further increase in temperature from 400 to $600\,^{\circ}$ C results in decreased surface island density of thin films, and disordered crystal grain. Figure 3 shows a typical Volmer–Weber growth mode. The corresponding

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process is proposed as follows. Some of the atoms which hit the substrate still have a lot of energy to return to the gas phase, while others are adsorbed on the surface of the substrate. Due to their own energy and the thermal energy gained on the substrate, atoms will drift on the substrate surface and collide with other atoms to form pairs or groups, which eventually form a condensation nucleus. When a certain concentration is reached, no new condensation nuclei are formed. The new adsorbed atoms will drift to the existing condensation nuclei, which leads to the growth of the condensation nuclei and they become small islands, and most of them form micrograins. As the islands grow, adjacent islands contact and combine with each other, so thin films with channels are formed. As the deposition continues, the adsorbed atoms fill empty channels. It is also possible to form new islands, which fill empty channels and, finally, a continuous film is formed. A proper temperature increases the migration energy of surface atoms, facilitating the formation of InN films. However, the atoms are likely to undergo secondary crystallization due to the excessively high migration energy at a relatively higher temperature.

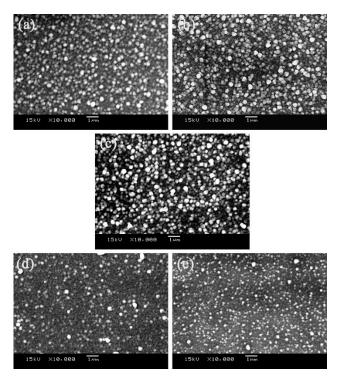


Figure 3. SEM patterns of the InN thin films deposited at (a) 200 °C, (b) 300 °C, (c) 400 °C, (d) 500 °C and (e) 600 °C.

3.4. AFM Analysis of InN Thin Films Prepared at Varying Temperatures

For following up the development of photovoltaic devices based on InN, the flatness of the surface should be controlled to the nanometer level [39–46]. AFM is used to analyze surface morphology of thin films, as shown in Figure 4. The surface roughness of InN films is as low as 10 nm, fully meeting the requirements of surface flatness for a high-power infrared detector. Figure 4 shows a typical Volmer–Weber growth mode. Further analysis shows that surface island density increases as the temperature increases from 200 to 400 °C. However, a further increase in temperature from 400 to 600 °C results in reduced surface island density and disordered grains. This indicates that the temperature has a great influence on the surface morphology. At 400 °C, the prepared thin films show a smooth surface morphology formed by uniformly dense grains and visible crystal lattice boundaries. Further analysis is based on the root mean square of the surface flatness of the films. When the preparation temperature increases from 200 to 400 °C, the root mean square of the surface roughness decreases from 9.8 to 3.7 nm, and the grain size gradually becomes consistent with the same growth direction.

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When the temperature further increases to 600 $^{\circ}$ C, the root mean square of the surface roughness increases gradually with more disordered grain growth. Therefore, the InN film prepared at 400 $^{\circ}$ C has optimum morphological characteristics.

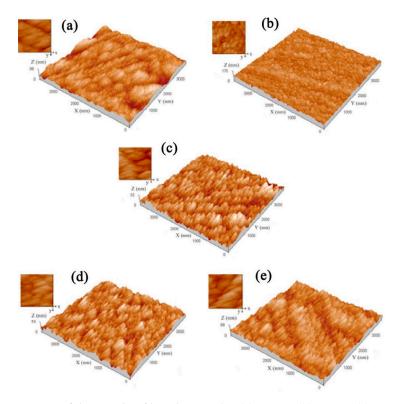


Figure 4. AFM patterns of the InN thin films deposited at (a) 200 °C, (b) 300 °C, (c) 400 °C, (d) 500 °C and (e) 600 °C.

3.5. Electrical Properties of InN Thin Films Prepared at Varying Temperatures

The Hall effect system HL5500, manufactured by Accent, is used and the Van der Pauw method is adopted to test the electrical properties of InN thin films. Electrode contact is used and the magnetic field intensity is 0.338 T. The sample size is 1.2 cm \times 1.2 cm, and four probes are located at a vertex angle of 1 cm \times 1 cm in the middle of the sample surface. Because the self-standing diamond substrate is insulated, the electrical property data are all from the InN thin films. As shown in Table 2, all InN thin films exhibit the conductivity of an *n*-type semiconductor, which is supposed to be a unique feature of unintentionally doped InN thin films. This is related to the inherent defects in the thin films [13–15]. The increase in temperature from 200 to 400 °C leads to a gradual reduction in the carrier concentration but a gradual increase in the mobility of thin films. A further increase in temperature from 400 to 600 °C results in a gradual increase in the carrier concentration but a gradual decrease in the mobility of thin films. According to previous reports [14–17], higher nitrogen partial pressure leads to the generation of nitrogen vacancies, which is the main reason for the high carrier concentration during the preparation of InN thin films. InN thin films have been grown on various substrates in previous works, such as sapphire substrates [47], multicrystalline Si substrates [48] and AlN/Si substrates [49]. The mobility in InN films is mainly dominated by grain boundary scattering and defect scattering. Compared with previous reports [26–28], the mobility of InN films in this study is relatively low (0.68 cm²/(V·S)), but InN films could be prepared at a lower temperature by an ECR-PEMOCVD system. The inert gaseous nitrogen is used as a nitrogen source, increasing the difficulty of preparation. Subsequent work will continue to optimize the experimental parameters for the improvement of mobility. The electrical properties of prepared thin films are consistent with the XRD results. At 400 °C, InN thin films prepared on the self-standing diamond substrate show better electrical properties.

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Sample	Temperature (°C)	Mobility (cm ² /(V·S))	Carrier Concentration (10 ²⁰ cm ⁻³)	Conductivity (Scm ⁻¹)
a	200	21.7	2.05	711.8
b	300	32.5	1.24	644.8
С	400	48.5	0.92	713.9
d	500	38.4	1.15	706.6
e	600	24.2	1.78	689.2

Table 2. Electrical properties of InN films on diamond substrates deposited at various temperatures.

3.6. XPS Analysis of InN Thin Films Prepared at 400 °C

The above results show that the best performance of InN thin films was obtained when the deposition temperature was at 400 °C with a self-standing diamond substrate. XPS is used to study the bonding properties of our best films, as shown in Figure 5. Two peaks of 443.3 and 444.5 eV are observed, corresponding to the binding energies of In-In bonds and In-N bonds, respectively. The peak intensity of the In-N bond was significantly higher than that of the In-In bond [36,37]. This demonstrates that there are a large number of In-N bonds in our prepared films, indicating the good crystalline quality. The In-In bonds in the films correspond to indium particle formation, which originate from a large number of nitrogen vacancies generated by nitrogen atom volatilization during film preparation.

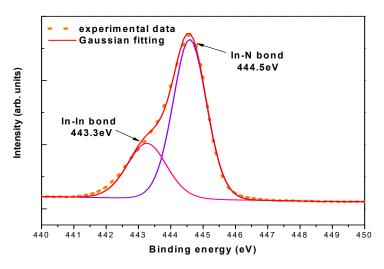


Figure 5. In $3d_{5/2}$ XPS spectra of the InN thin films deposited at 400 °C.

4. Conclusions

The temperature of deposition plays a key role in the preferential growth of the (0002) crystal plane of InN films. In this study, an electron cyclotron resonance plasma-enhanced metal organic chemical vapor deposition (ECR-PEMOCVD) system was used to prepare InN thin films on self-standing diamond substrates at different temperatures. The proper temperature is conducive to the optimum quality of InN films. At 400 °C, InN films with highly *c*-axis preferential orientations are obtained. A higher temperature results in a rougher film surface, deteriorating crystallization quality. XPS spectra show the bonding characterization of In–N and In–In in the films. The preferential orientation, optimum surface morphology and electrical properties of InN films can be obtained at a temperature of 400 °C. Given the better heat dissipation performance of self-standing diamond substrates, our achievement will be of special interest for the development of a wide range of InN-based electronic and optoelectronic devices with high power capability and sufficient power durability.

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Conflicts of Interest: The authors declare no conflict of interest.

Data Availability: The data that support the findings of this study are available from the corresponding author upon reasonable request.

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