

Communication



# Controlled Surface Morphology and Electrical Properties of Sputtered Titanium Nitride Thin Film for Metal–Insulator– Metal Structures

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**Abstract:** Titanium nitride (TiN) is a material of interest for electrodes owing to its high-temperature stability, robustness, low-cost, and suitable electrical properties. Herein, we studied the surface morphology and electrical properties of TiN thin film deposited onto an Si/SiO<sub>2</sub> <100> substrate through direct current (DC) sputtering with a high-purity TiN target in an argon-gas environment. The electrical properties and surface morphology of TiN thin film significantly improved with increased source power and decreased working pressure. The improved electrical properties could be attributed to the suppressed secondary phase (Ti<sub>2</sub>N) formation and the reduced electron scattering on smoother surface. Consequently, high-quality TiN thin film with the lowest resistivity ( $\rho = 0.1 \text{ m}\Omega \cdot \text{cm}$ ) and the smallest surface roughness (RMS roughness, R<sub>q</sub> = 0.3 nm) was obtained under the optimized condition. The TiN film was further used as the bottom electrode for a metal–insulator–metal (MIM) capacitor. Results demonstrated that the electrical properties of TiN film were comparable to those of noble-metal thin films. Therefore, the TiN thin film fabricated by DC sputtering method had excellent electrical properties and good R<sub>q</sub>, indicating its potential applications in MIM capacitors and Si technology.

Keywords: titanium nitride; thin film; surface roughness; electrical properties; magnetron sputtering

## 1. Introduction

Titanium nitride (TiN) is a metal nitride known for its high thermodynamic stability, high corrosion resistance, low friction constant, low electrical resistivity, and high mechanical hardness [1]. Based on these characteristics, TiN is extensively used in the microelectronics industry as a diffusion barrier [2], antireflective coating [3], gate material [4], Schottky barrier contact [5], and adhesion layer [6]. In particular, TiN thin film can be utilized as an electrode in dynamic random-access memory cells and in the back end of line metal-insulator-metal (MIM) capacitor electrode integrated circuits (ICs) [7]. The electrode used in MIM capacitors should have low electrical resistivity, and low surface roughness. Several methods can be used to prepare TiN thin films, such as chemical vapor deposition (CVD) [8], plasma-enhanced CVD (PE-CVD) [9], low-pressure CVD (LP-CVD) [10], and atomic-layer deposition (ALD) [11]. In CVD, high temperature is needed to degrade the strength of tool substrates, and the residual stress of the film is high. Pinholes and carbon contamination may occur in films deposited by organometallic PE-CVD [12]. ALD is a



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**Copyright:** © 2022 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). chemical technique always accompanied by a risk of residues being retained from the precursors. The films' impurity content depends on reaction completeness [13]. In physical vapor deposition (PVD) methods, the environment is much cleaner owing to the high vacuum used and the lack of precursor compounds with unwanted elements. The direct current (DC) magnetron-sputtering method was selected for this study because of its many advantages, including low cost, high purity, uniformity, and good control over stoichiometry. Deposition conditions such as argon content in the chamber, power, pressure, residual oxygen in the chamber, substrate temperature, and substrate bias are important parameters for obtaining high-quality TiN thin film in reactive sputtering. Ponon et al. reported that as-deposited TiN<sub>x</sub> resistivity gradually decreases with increased nitrogen flow rate during deposition [14]. Jin et al. fabricated a  $TiN_x$  thin film on different types of substrate and reported that the resistivity of  $TiN_x$  film is strongly influenced by substrate temperature, deposition time, working pressure, bias power, and Ar and N<sub>2</sub> flow rate [15]. Jeyachandran et al. showed that thickness and  $N_2$  concentration strongly affect the electrical, optical, compositional, structural, and morphology properties of TiN thin films [16]. However, further systematic information on the correlation between sputtering process parameters and properties of TiN is required to produce TiN thin films for a low resistivity and optimized MIM structures. Moreover, a comprehensive study on the effects of operating pressure and source power to improve the quality of TiN thin films that can be used as an electrode material in advanced MIM capacitors and IC technologies has not yet been fully resolved.

Herein, we prepared TiN thin films on an Si/SiO<sub>2</sub> <100> substrate through the DC magnetron-sputtering method. The effects of source power and working pressure on the roughness surface and resistivity of TiN thin film were studied in detail. Our results demonstrated that the surface roughness and resistivity of TiN thin film can be strongly improved by increasing the source power and decreasing the working pressure. We can obtain high-quality of TiN thin film with low resistivity (0.1 m $\Omega$ ·cm) and small surface roughness (RMS roughness, R<sub>q</sub> = 0.3 nm) by using 150 W bias power and 0.5 × 10<sup>-3</sup> Torr as the optimum sputtering conditions.

### 2. Experimental Section

Si/SiO<sub>2</sub> <100> substrates (2 × 2 cm<sup>2</sup>) were degreased with ultrasonic acetone and propanol for 15 min sequentially, rinsed with deionized water for 10 min, and dried in N<sub>2</sub> atmosphere. The TiN thin films were prepared by DC magnetron sputtering system at a base pressure less than  $6 \times 10^{-6}$  Torr using a high-purity (99.5%) 2" diameter TiN target. The target-to-substrate distance was 100 mm for all depositions, and the rotation speed was 5 rpm. The TiN thin films were grown on Si/SiO<sub>2</sub> <100> substrates for 10 min in Ar gas at a flow rate of 30 standard cubic centimeter per minute (SCCM). To investigate the effect of source power, the TiN thin film was deposited at various source power (25, 50, 75, 100, 150, and 175 W). Subsequently, the working pressure was changed to 0.5, 1, 2, 3, and 5 mTorr at a fixed source power of 150 W to further investigate the effect of working pressure.

The surface morphology and thickness of TiN thin films were determined by fieldemission scanning electron microscopy (FE-SEM; S-4800, Hitachi, Ibaraki, Japan). Surface roughness was analyzed by atomic force microscopy (AFM; Asylum Research, Oxford, UK, MFP-3D). The crystal structure of films was characterized by the X-ray diffraction (XRD,  $\lambda = 1.548$  Å, Empyrean, Panalytical, Malvern, UK). Intrinsic electrical characterization of TiN thin film was compared using resistivity, which was measured with the four-point probe method (CMT-SR1000N). The MIM devices were fabricated with circular Ti (5 nm)/Au (200 nm) as a top electrode 150 µm in diameter formed on Si/SiO<sub>2</sub> <100>/TiN/Al<sub>2</sub>O<sub>3</sub> thin films by using a shadow mask. The electric properties of MIM capacitors were studied with a Keithley 4200A-SCS parameter analyzer.

#### 3. Results and Discussion

Figure 1 shows the thickness of the TiN thin film measured as a function of source power, which was adjusted from 50 W to 175 W. Thickness was found to increase linearly

with increased source power primarily because a greater sputtering power corresponded with stronger field strength among electrons. Consequently, the number of sputtered particles increased owing to the increased avalanche multiplication effect [17]. Accordingly, a faster sputtering rate corresponded with a greater thickness of TiN thin film that can be obtained with an increased source power under the same sputtering time. The thickness of TiN thin film measured as a function of source power was fitted by the function

$$Y = A_1 \times X + A_2 \tag{1}$$

where  $A_1$  is the deposition rate ( $A_1 = 0.548 \text{ nm/W}$ ), X is the source power, and  $A_2$  is a small offset ( $A_2 = 5.6 \text{ nm}$ ). The inset in Figure 1 shows the cross-sectional SEM image of TiN thin film on the Si/SiO<sub>2</sub> <100> substrate.



**Figure 1.** Variation in thickness of TiN thin film on the  $Si/SiO_2 <100>$  substrate with various source power.

Figure 2 shows the surface morphology of the as-deposited TiN thin films on the  $Si/SiO_2$  <100> substrate as characterized by SEM. The TiN thin film deposited at a low source power of 50 W had relatively small dense particles with small void spacing and a smooth and uniform surface, as shown in Figure 2a. With increased source power, the grain size moderately expanded and the grain boundaries increased. This tendency became more obvious at higher source power (150 and 175 W), as displayed in Figure 2e,f. The inset images of Figure 2 show the cross-sectional morphology of TiN film at various source power and confirmed that the thickness of TiN film significantly increased with enhanced source power. The surface morphology of TiN thin film, in each case, was analyzed by SEM measurement for the corresponding sample. XRD study revealed the effect of source power on film crystallinity (Figure 3a). At a low source power of 50 W, only two peaks appeared at 43.6° and 51.2° of 2 $\theta$ , corresponding to TiN and Ti<sub>2</sub>N, respectively. With increasing the source power, the peak intensity of Ti<sub>2</sub>N decreased while the TiN peaks became dominant. Above 100 W of source power, well-developed TiN peaks appeared at 36.8°, 42.7°, 62.1°, and 74.3°, corresponding to (111), (200), (220), and (311) orientation of TiN phase, respectively. A high source power formed a thick film with enhanced diffusion of nitrogen, resulting in predominant TiN phases [18]. However, the TiN peak position shift toward the lower diffraction angles at higher source power suggests the deficiency of the Ti component [19].



**Figure 2.** SEM surface morphology of TiN thin film deposited with various source power: (**a**) 50 W, (**b**) 75 W, (**c**) 100 W, (**d**) 125 W, (**e**) 150 W, and (**f**) 175 W, respectively. The inset presents a SEM image of TiN thickness.



**Figure 3.** (a) XRD patters, (b) RMS roughness ( $R_q$ ) and resistivity of TiN thin film on the Si/SiO<sub>2</sub> <100> substrate with various source power.

AFM analysis directly provided the surface roughness of TiN thin film from the three-dimensional AFM image, as shown in the inset of Figure 3b. The surface roughness (RMS roughness,  $R_q$ ) of TiN thin film strongly depended on the source power. At a lower source power (50 W), the film was very smooth and the  $R_q$  value was low (~0.4 nm).  $R_q$ continuously increased with enhanced source power, reaching 1.3 nm at 175 W of source power, as indicated in Figure 3b. A higher sputtering power and higher energy of incident ions or atoms caused clusters or grains to more easily aggregate and grow. Consequently, within a certain range of sputtering power, particle size was proportional to the sputtering power, which primarily explained the increase in  $R_q$  with increased the source power [17]. Moreover, the surface mounds gradually grew and the roughening phenomenon intensified with increased thickness of TiN thin film at a higher source power [20]. Figure 3b also shows the resistivity of TiN thin film as a function of source power. With enhanced source power from 50 W to 100 W, resistivity drastically decreased from 171 m $\Omega$ ·cm to 14 m $\Omega$ ·cm. Then, it gradually decreased with increased source power, reaching 1.47 m $\Omega$  cm at 175 W. An abrupt decrease in resistivity was observed because the stoichiometric TiN deposited at the higher source power remained stable against oxidation [21], and the transformation from  $Ti_2N$  phase to TiN phase. At high source power, the resistivity was low, but  $R_q$  was high. Accordingly, 150 W was selected for further studies to improve the surface roughness and electrical properties of TiN thin film. The surface morphology of TiN thin film on the  $Si/SiO_2$  <100> substrate at various working pressure was investigated by SEM analysis (Figure 4).



**Figure 4.** SEM surface morphology of TiN thin film deposited with various working pressure: (a) 0.25 mTorr, (b) 0.5 mTorr, (c) 1 mTorr, (d) 2 mTorr, (e) 3 mTorr, and (f) 5 mTorr. The inset presents a SEM image of TiN thickness.

At a higher working pressure ( $P_{working} = 5 \times 10^{-3}$  Torr) during films preparations, the evaporated TiN atoms presumably lost considerable energy through their numerous collisions with Ar atoms before they were deposited onto the Si/SiO<sub>2</sub> <100> substrate, resulting in insufficient growth of TiN film. With decreased working pressure, the surface morphology of TiN film drastically improved with small grain and narrow grain boundary. The insets of Figure 4 presented the cross-sectional microstructures of TiN film. As the working pressure was adjusted, film thickness slightly changed owing to the formation on the target surface a gas compound layer with a lower sputtering rate than metals [22]. Moreover, the cross-sectional images revealed growth morphology containing a columnar microstructure of TiN film at high working pressure, consistent with the characterization of the Zone T structure [23]. At lower working pressure ( $P_{working} \leq 1$  mTorr), columnar structures were not clearly observed, and TiN thin films grew well on the Si/SiO<sub>2</sub> <100> substrate.

The XRD patterns of films deposited at various working pressures are shown in Figure 5a. With decreasing working pressure, the peak intensity of Ti<sub>2</sub>N decreased and the TiN peak position shifted toward the lower diffraction angles. A low working pressure seemed to enhanced diffusion of nitrogen, resulting in the transformation from Ti<sub>2</sub>N to TiN. Figure 5b shows that the  $R_q$  of TiN thin films continuously decreased from 1 nm to 0.3 nm at a working pressure of  $5 \times 10^{-3}$  and  $1 \times 10^{-3}$  Torr, respectively. However,  $R_q$  suddenly increased to 0.6 nm at a low working pressure of  $0.25 \times 10^{-3}$  Torr. A smoother surface helped reduce electron scattering and improve the conductivity of TiN film [14]. Consequently, the resistivity of TiN film abruptly decreased from 1.05 m $\Omega$ ·cm to 0.14 m $\Omega$ ·cm at a working pressure of  $2 \times 10^{-3}$  and  $1 \times 10^{-3}$  Torr, respectively, and then slowly decreased to 0.1 m $\Omega$ ·cm at a  $P_{\text{working}}$  of about  $0.5 \times 10^{-3}$  Torr. The decreased resistivity can be attributed to the suppressed secondary phase (Ti<sub>2</sub>N) formation and the reduced electron scattering on smoother surface, which is also consistent with the results shown in Figure 3. The TiN thin films prepared under the optimum conditions at a source power of 150 W and working pressure of  $0.5 \times 10^{-3}$  Torr was utilized as a bottom electrode for MIM devices.

Figure 6a reveals the high-frequency (1 MHz) capacitance–voltage (C–V) characteristic of Al<sub>2</sub>O<sub>3</sub> MIM capacitors of various dielectric thickness, and the inset of Figure 6a shows the schematic of the MIM structure. The capacitance increased as the Al<sub>2</sub>O<sub>3</sub> became thinner, ranging within 3.5– $15 \text{ fF}/\mu\text{m}^2$ . Among the various requirements for an analog MIM capacitor, leakage current density and dissipation factor are critical issues for MIM capacitor applications. Figure 6b shows that 5 nm and 20 nm Al<sub>2</sub>O<sub>3</sub> films exhibited excellent MIM leakage current density (approximately  $10^{-6} \text{ A/cm}^2$ ) at 2 V. The dissipation factor at

1 MHz was also below 0.03 and 0.02 for the 5 nm and 20 nm  $Al_2O_3$  capacitors, respectively, as shown in Figure 6c.



**Figure 5.** (a) XRD pattern, (b) RMS roughness ( $R_q$ ) and resistivity of TiN thin film on the Si/SiO<sub>2</sub> <100> substrate with various working pressure.



**Figure 6.** (**a**) High–frequency (1 MHz) capacitance–voltage (C–V) curves, (**b**) leakage current density, and (**c**) dissipation factor as a function of frequency for MIM devices with various Al<sub>2</sub>O<sub>3</sub> thickness. The inset of (**a**) shows the schematic of MIM structure.

#### 4. Conclusions

The morphology and electrical properties of TiN thin films grown on Si/SiO<sub>2</sub> <100> substrates by using a DC sputtering system were investigated. The effects of source power and the working pressure on roughness and resistivity were systematically studied. The TiN thin films with suppressed secondary phase (Ti<sub>2</sub>N) formation remained stable against oxidation at a high source power, resulting in very low resistance. The smallest R<sub>q</sub> value of 0.3 nm and the lowest resistivity of 0.10 mΩ·cm were obtained for the TiN thin film fabricated at a high source power (150 W) and low working pressure ( $0.5 \times 10^{-3}$  Torr). Moreover, we applied the MIM structure to the TiN thin film and confirmed that its electrical properties were comparable to those of noble-metal thin films. These results suggest that TiN films are potential application candidates in MIM capacitors and Si technology.

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