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Abstract: Aiming at the serious harmonic pollution and the low power factor in the distribution network of industrial enterprises, this paper develops an integrated method for harmonic suppression and reactive power compensation suitable for the distribution network of industrial enterprises. The integrated method realizes the dual functions of harmonic filtering and reactive power compensation, and filters out the harmonic current to get a symmetrical current waveform while ensuring safe operation of the power compensator. In addition, it solves the problems of high harmonic content, small power factor in the distribution network, and device burnout caused by direct input of reactive power compensator. The main contributions of this paper are as follows: (1) According to the demand for the integration of harmonic suppression and reactive power compensation, the steps of integrated method for harmonic suppression and reactive power compensation are proposed, and then the methods for harmonic filtering and reactive power compensation are investigated; (2) a method for designing the capacity of a filter capacitor and the rated parameter of an electromagnetic coupling reactance converter is proposed, and an optimization simulation system is constructed to design the parameters of the filter; (3) a simulation system is developed, followed by parameter design and simulation analysis of harmonic filtering subsystem (HFSS), reactive power compensation subsystem (RPCSS) and the integrated system of harmonic suppression and reactive power compensation. Simulation results verify that the HFSS is put into operation first and then switched off later to ensure the normal operation of other equipment in the distribution network. After the treatment, the power factor, harmonic current content and total distortion rate all meet the national standards. The integrated method can dynamically track harmonics and reactive power changes, filter out harmonics, improve power factor and the symmetry level of the power source, and ensure the normal operation of other equipment in the distribution network. The research results lay a certain theoretical and technical foundation for the harmonic filtering and reactive power compensation theory, technology and its device innovation to achieve effective suppression of power harmonics and reactive power compensation.

Keywords: harmonic filtering; reactive power compensation; symmetry level of power source; power quality; distribution network

1. Introduction

Nowadays, electricity is widely applied in industrial manufacturing. The symmetry level of voltage and current waveform is one of the main indicators for measuring power quality. With the advances in technology, the power demand of Chinese enterprises is increasing dramatically, and a large number of new impact loads and non-linear loads and non-symmetrical loads have emerged, resulting in harmonic amplification and power factor reduction in the distribution network. The power factor affects the deviation of supply voltage to obtain a non-symmetrical voltage waveform, and the harmonics lead to



Citation: Wang, Y.; Yin, K.; Liu, H.; Yuan, Y. A Novel Integrated Method for Harmonic Suppression and Reactive Power Compensation in Distribution Network. *Symmetry* **2022**, *14*, 1347. https://doi.org/ 10.3390/sym14071347

Academic Editor: Mihai Postolache

Received: 2 June 2022 Accepted: 23 June 2022 Published: 29 June 2022

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the distortion of current waveform to get a non-symmetrical current waveform. Therefore, they are important factors affecting power quality. Due to the serious harmonic pollution in the distribution network of industrial enterprises, the existing reactive power compensators are unable to operate safely. This, in turn, results in low power factor, poor power quality, and large power consumption. Thus, energy saving of enterprises cannot be ignored [1–5]. The power quality indices are governed by various standard regulations, such as IEEE-519-2014 [6]. The author used the French CA8335 power quality analyzer to measure the power factor and harmonic current of the power plant from 8:00 to 18:00. It was found that when the measured power quality was the worst, the power factor was 0.51, the 5th harmonic ratio was 18 %, the 7th harmonic ratio was 9%, the 11th harmonic ratio was 10%, the 13th harmonic ratio was 6%, and the total harmonic distortion rate was 23.8%. The reason is that when the harmonics are large, the existing reactive power compensator of enterprises cannot be operated normally, and power devices such as compensation capacitors will be burned in severe cases, and the power factor is much lower than the national standard of 0.9–1.

The author developed a "static var compensator based on electromagnetic coupled reactor" to realize dynamic reactive power compensation, and a "dynamically tuned harmonic filter (DTHF)" to filter the harmonics in the power grid while providing part of the reactive power at the same time [7–11]. However, a single filter or reactive power compensator cannot solve the harmonic pollution and the low power factor problems existing in the enterprise distribution network. To this end, this paper proposes an integrated method for harmonic suppression and reactive power compensation suitable for the distribution network of industrial enterprises.

The power filters have been used to mitigate the power harmonics, such as passive power filter (PPF), active power filter (APF) and series hybrid active power filter (SHAPF). The PPF tunes the harmonics of the specified order through the LC filter circuit, and effectively eliminate the harmonics of this order by taking advantage of its characteristic of minimum impedance at resonance. Thus, the harmonic current of this order cannot flow into the power grid, thereby achieving the purposes of harmonics suppression and limiting the harmonic current below the standard value to get a symmetrical current waveform. Characterized by simple structure, relatively low cost, and convenient maintenance, PPFs are the major method widely applied for harmonic filtering at present. Nevertheless, the capacitance and inductance of traditional PPF are fixed, and cannot be adjusted freely according to harmonic changes, impairing the filtering effect. When the harmonic current increases, it will overload the filter and damage the electrical components. After traditional PPFs work for a long time, their inductance and capacitance will deviate from the standard value as the temperature changes, increasing the total harmonic distortion. In severe cases, the resonance point drifts and resonates with the system, impairing filter efficiency and filter performance [12–19].

Aiming at the shortcomings of traditional PPF, the author proposed a dynamic tuning passive filter(DTPF) based on the traditional LC filtering principle. This method can dynamically adjust the capacitor inductance and filter the harmonics in the circuit, avoid the resonance point deviation resulted from the aging of the medium, for example, and ensure the safe and stable operation of equipment [20]. The APF filter has been investigated and many active power filter topologies have been researched. There are three kinds of typical APF according to their configurations: (1) parallel APF; (2) hybrid active power filter(HAPF); and (3) unified power quality conditioner. Among all these APF, the series hybrid active power filter (SHAPF) has attracted much attention of electrical engineers and researchers, as it can take advantage of the passive power filter and reduce the rating of active power filters, as well as greatly increasing the system harmonic impedance instead of the fundamental impedance [21–23].

With the development of science and technology, the load structure in the distribution network has undergone great changes, with the inductive load increasing. Moreover, the reactive power supplied by generators is far from meeting the reactive power demand of electrical loads. In order to ensure the normal operation of various kinds of power equipment and to improve equipment utilization, it is necessary to compensate reactive power. Parallel capacitors can compensate fixed reactive power, and have a wide range of applications for their flexible compensation methods, low costs, simple maintenance, and small power loss. However, their capacity is fixed and not adjustable, and it is easy to have parallel resonance with the inductor in the system, resulting in amplification of harmonic currents. The amplified harmonic currents are transmitted to the distribution network through the power transformer, increasing the adverse impact of harmonics on the system. In serious cases, they will cause system oscillations, which in turn lead to failure of the equipment in the distribution network and breakdown of the power supply system [24,25]. Therefore, filter devices should be installed in places with high harmonic interference and reactive power compensation.

At present, the commonly used reactive power compensation mainly adopts Static Var Compensator (SVC). SVC mainly controls the capacity of reactors and capacitors connected to the system through the saturation of a controllable saturable reactor or the conduction of thyristor, so as to change the admittance of the power transmission system. Its typical representative is the reactive power compensator using a thyristor as the control device. The TCR + SVC device is the most widely applied SVC at present, and has become the mainstream of high-voltage SVC technology due to its fast response and phase compensation capability [26,27].

It is known from the above analyses that the distribution network of industrial enterprises is facing serious harmonic pollution, which results in the fact that the existing reactive power compensators are unable to be used safely, with low power factor, a non-symmetrical current waveform, and poor power quality. However, these problems are difficult to solve by using reactive power compensators or harmonic filters alone. In recent years, many experts have put forward solutions for the optimization of power quality. The main problems that need to be addressed are as follows. The first is that the harmonic content of the distribution network is large, so the reactive power compensator may be burned out if it is directly applied; the second is the coordination between harmonic filtering and reactive power compensation; the third is the mechanism of harmonic suppression dynamic tuning and dynamic reactive power compensation.

This paper focuses on the serious harmonic pollution and the low power factor in the distribution network of industrial enterprises. A novel integrated method for harmonic suppression and reactive power compensation has been devised. The integrated method realized the dual functions of harmonic filtering and reactive power compensation and filtered out harmonics while ensuring safe operation of the power compensator. The integrated method could dynamically track harmonics and reactive power changes, filter out harmonics, and improve the power factor and the symmetry level of the power source. The research results lay a certain theoretical and technical foundation for the harmonic filtering and reactive power compensation to achieve effective suppression of power harmonics and reactive power compensation.

The performance of the integrated method for harmonic suppression and reactive power compensation in the distribution network is evaluated based on the following objectives.

- To restrict harmonic mitigation as per the IEEE 519-2014 standard in such a way that source currents are sinusoidal, irrespective of harmonic content in distorted source voltage and non-linear load.
- To eradicate the effect of poor load power factor, such that it results in an almost united power factor on the supply side.
- To decrease apparent power supplied from the source for given source voltage conditions.

The rest of the paper is organized under the following headings. Section 2 begins with a discussion on the integration mechanism, with steps of integrated method and topological structure of the integrated system, followed by a detailed description of harmonic current filtering method of HFSS. This includes the dynamic tuning condition for HFSS, parameters design, inductance regulation, and a two-step optimization algorithm in Section 3; Section 4

presents a reactive power compensation method of RPCSS; Section 5 illustrates integration simulation and discussion. Finally, the reported work is concluded with important remarks in Section 6.

2. Integration Mechanism of Harmonic Suppression and Reactive Power Compensation

The integration of harmonic suppression and reactive power compensation in the distribution network is realized through an integrated system composed of a harmonic suppression subsystem (HFSS), a reactive power compensation subsystem (RPCSS), a digital power signal detection unit (DPSCU), controller, etc. Among them, dynamic tuned harmonic filtering is realized by the HFSS and dynamic reactive power compensation by the RPCSS. The HFSS is put into operation first and then switched off later to ensure the normal operation of other equipment in the distribution network.

2.1. Steps of Integrated Method for Harmonic Suppression and Reactive Power Compensation

The power switch is turned on to connect the integrated system to the distribution network. Let the start flag F_s be "0", the stop flag F_p be "0", the start button be S_A , and the stop button be S_B , then the integration of harmonic suppression and reactive power compensation can be realized through the following steps:

• *S*₁: Call the start and stop interrupt service.

The interrupt is adopted for control:

An application for start interruption is submitted if the start button S_A is effective. The controller performs control by judging the start flag F_s and the stop flag F_p . When the start flag F_s and the stop flag F_p are both "0", the controller sets the start flag F_s to "1", and calls "Run Interrupt Service Routine" (S_2);

An application for stop interruption is submitted if the stop button S_B is effective. When the start flag F_s is "1", the controller sets the stop flag F_p to "1" and the start flag F_s to "0", and calls "Stop Interrupt Service Routine" (S_3).

• *S*₂:Run interrupt service routine.

S_{21} : Initialization

The minimum harmonic current that satisfies the harmonic index is I_b , and the power factor index is ph_0 ; the updated flag F_u is "1", harmonic suppression flag F_{rh} is "0", the reactive power compensation flag F_{rh} is "0"; then entering S_{22} ;

 S_{22} : Power signal detection

When the updated flag F_u is "1", the signals in the distribution network are collected, including current, voltage, active power P, reactive power Q, power factor ph, and the h-order harmonic current I_h that needs to be filtered. After the data are collected and stored, the controller sets the updated flag F_u to "1", harmonic suppression flag F_{rh} to "1", and then the updated flag F_u to "0"; then moves on to S_{23} .

 S_{23} : Dynamically tuned filtering

When the harmonic suppression flag F_{rh} is "1" and the reactive power compensation flag F_{rq} is "0", the controller switches on HFSS. The working status of HFSS is determined by any of the following conditions:

When I_h is larger than I_b , HFSS enters the dynamic tuning state, adjusts the resonance frequency, filters out the harmonics, and ensures the stable operation of the power grid until the harmonic current meets the harmonic standards to get a symmetrical current waveform. In addition, it provides a certain amount of reactive power and sets the harmonic suppression flag F_{rh} to "0"; When I_h is less than or equal to I_b ; that is, the harmonic current meets the harmonic standard, HFSS maintains the working state at the previous moment; After that, the reactive power compensation flag F_{rq} is set to "1"; then moves on to S_{24} .

 S_{24} : Dynamic reactive power compensation

When the reactive power compensation flag F_{rq} is "1", the controller switches on the RPCSS, and the working status of RPCSS is determined by any of the following conditions:

When *ph* is smaller than *ph*₀, RPCSS enters the dynamic reactive power compensation state. According to *P*, *Q*, *ph* and *ph*₀, the reactive power capacity Q_b , RPCSS sends reactive power to the distribution network until the power factor meets the requirements; that is, *ph* exceeds *ph*₀. After that, it sets the reactive power compensation flag *F*_{rq} to "0";

When *ph* is larger than *ph*₀, or when the power factor meets the reactive power compensation standard, RPCSS will maintain the working state at the previous moment; After that, the flag F_u is updated to "1"; then moves on to S_{22} .

• S_3 :Stop interrupt service routine.

First, the reactive power compensation subsystem is switched off, and then the HFSS to ensure that the main power components of the RPCSS are not burned out due to harmonics. Then, the incoming power switch is turned off.

Through the above steps, the harmonics and reactive power changes are dynamically tracked, the harmonics are filtered out, and the power factor is improved, thus leading to a more symmetrical waveform and successfully avoiding power grid problems.

2.2. Topological Structure of the Integrated System

To achieve the integration of harmonic suppression and reactive power compensation, the topological structure of the integrated system is constructed, as shown in Figure 1.

In Figure 1, Q_F is a circuit breaker (the incoming power switch), which is responsible for switching the power supply on or off.

The HFSS is composed of a contactor KM_1 , an electromagnetic coupling reactor SS (the equivalent inductance of the primary reactance winding is L_{11}), fast-acting fuses FU_{11} - FU_{1m} , contactors KM_{11} - KM_{1m} , and a filter capacitor bank C_{11} - C_{1m} (the equivalent capacitance is C_1). The controller switches on and off the filter capacitor bank C_{11} - C_{1m} by controlling on/off of contactors KM_{11} - KM_{1m} , and obtains the equivalent capacitance C_1 required for reactive power compensation. L_{11} and C_1 form a low-impedance circuit, so that the harmonic current in the power distribution system directly flows into the HFSS to filter out harmonics.



Figure 1. Equivalent harmonic circuit of the filter system.

The RPCSS is composed of the contactor KM_2 , fast-acting fuses FU_{21} - FU_{2n+1} , contactors KM_{21} - KM_{2n+1} and reactive compensation capacitor bank C_2 (consists of C_{11} - C_{1n}). The RPCSS calculates the reactive power required to be compensated based on the detected reactive power shortage. By controlling the on/off of the contactors KM_{21} - KM_{2n} , the controller switches on the corresponding capacitors C_{21} – C_{2n} to generate reactive power so as to compensate the system.

The DPSCU, by analyzing the voltage and current signals collected by the voltage and current measurement sensor PAV, obtains such signals as the current, voltage, active power P, reactive power Q, power factor ph, and h-order harmonic current I_h that need to be filtered out in the distribution network, and transmits them to the controller via RS485 bus. The coordination between HFSS and RPCSS is realized by the controller.

3. Harmonic Current Filtering Method of HFSS

As shown in Figure 1, when KM_1 is connected and KM_2 is disconnected, the HFSS is put into operation. Aiming at the problem that the parameters for the passive filter cannot be adjusted continuously and the dynamic tuning cannot be realized, the harmonic current filtering method is proposed. The specific steps of the method are as follows:

- 1. Propose the constraint conditions that must be met during dynamic tuning so as to improve the stability of HFSS while filtering out the *h*-order harmonic current;
- 2. Adopt the minimum capacitance method to design the parameters of the electromagnetic coupling reactor (SS) and filter capacitors of the HFSS according to the *h*-order harmonic current *I_h* that needs to be filtered out;
- 3. Propose the method for adjusting the inductance of the SS; that is, the power electronic impedance converter (PEIC) adjusts the equivalent inductance of SS to L_{11} by adjusting its thyristor trigger angle according to the desired control signal U_z ;
- 4. Uses the two-step optimization algorithm to get the control signal U_z needed to make *h*-order harmonic current I_f flowing into the HFSS to reach the maximum;
- 5. Switch on and off the filter capacitor bank C_{11} - C_{1m} by controlling the on/off of the contactors KM_{11} - KM_{1m} , so as to obtain the equivalent capacitance C_1 required for reactive power compensation. Thus, L_{11} and C_1 form a low impedance loop so that the harmonic current in the power distribution system flows directly into the filter subsystem to filter out harmonics.

3.1. Dynamic Tuning Condition for HFSS

There is no background harmonic current in the power grid or distribution system to be treated. The harmonic currents in the system are all generated by the non-linear load, and its magnitude is I_h . In order to filter the *h*-order harmonic current in the distribution system, *h*-order HFSS is installed. Figure 2 shows the flow of the *h*-order harmonic current.



Figure 2. Schematic diagram for the *h*-order harmonic current flow.

In Figure 2, T_1 and T_2 are the installation positions of the DPSCU. After passing through the HFSS, the harmonic current injected by the harmonic source load into the system is

$$I_s = I_h - I_f, \tag{1}$$

According to the impedance frequency characteristics of HFSS, when it is tuned to the resonance frequency f_h , the *h*-order harmonic current I_f absorbed by the HFSS reaches its maximum, and *h*-order harmonic current I_s injected into the power grid reaches the minimum. Therefore, the control objective is to maximize I_f and the following two constraint conditions must be satisfied during dynamic tuning:

- Condition 1: The *h*-order harmonic current *I_h* generated by the the nonlinear load must be less than or equal to the maximum *h*-order harmonic current *I_{f(h)}* that the HFSS can filter.
- Condition 2: Let $I_{ref(h)}$ be the allowable *h*-order harmonic current into the public grid prescribed in the Harmonics standards (IEEE Std 519—2014), and the HFSS is tuned when the *h*-order harmonic current $I_{s(h)}$ in the grid is larger than $I_{ref(h)}$; otherwise, the HFSS continues to output the control signal $U_z(k-1)$ from the previous moment.

Condition 1 is to restrict the magnitude of the *h*-order harmonic current generated by the non-linear loads, also known as non-symmetrical loads; that is, the premise of harmonic filtering is that the *h*-order harmonic current generated by the non-linear loads cannot exceed the capacity of HFSS. Condition 2 is to constrain the tuning conditions to avoid the constant tuning of HFSS. That is, the HFSS starts tuning only when the *h*-order harmonic current in the power grid exceeds the allowable value specified in the national standard. In this way, the *h*-order harmonic current is dynamic filtered while improving the stability of HFSS.

3.2. Parameter Design of Filter Capacitors

The parameters for the filter capacitors of HFSS and electromagnetic coupling reactor SS are designed based on the minimum capacitance method.

In the HFSS, the investment of capacitors is generally large. In order to reduce the cost of filters, it is necessary to minimize the capacity of capacitors while meeting filtering requirements. Therefore, with the minimum capacitance of the filter capacitor determined by the minimum capacitance method, this paper calculates the optimal capacity of the filter capacitor so as to determine the capacitance of the filter capacitor. The specific method is as follows:

When the HFSS resonates, the inductive reactance is equal to the capacitive reactance, i.e.,

$$h\omega_1 L_{11} = \frac{1}{h\omega_1 C_1},$$
 (2)

where ω_1 is the fundamental angular frequency and L_{11} and C_1 are, respectively, the inductance of the electromagnetic coupled reactor and the capacitance of the filter capacitor bank in the HFSS at tuned frequency f_h .

In general, for the distribution system to be treated, the distortion rate of its harmonic voltage is very small. Thus, it can be approximated that the AC bus voltage of the system does not contain harmonic voltage components; that is, the AC bus voltage U_1 of the system is equal to the fundamental component $U_{(1)}$. Then, in addition to the *h*-order harmonic current $I_{f(h)}$, the current in the HFSS branch should also include the fundamental current $I_{f(1)}$ caused by the fundamental voltage $U_{(1)}$. Its magnitude can be determined by the following formula:

$$I_{f(1)} = \frac{U_{(1)}}{1/\omega_1 C_1 - \omega_1 L_1} = \omega_1 C_1 \frac{h^2}{h^2 - 1} U_{(1)}$$
(3)

Both the fundamental current $I_{f(1)}$ and the harmonic current $I_{f(h)}$ will generate reactive power when flowing through the filter capacitors of HFSS. So the total installed capacity $S_{(h)}$ of the filter capacitor bank should be:

$$S_{(h)} = Q_{(1)} + Q_{(h)} = \frac{1}{\omega_1 C_1} I_{f(1)}^2 + \frac{1}{h\omega_1 C_1} I_{f(h)}^2 = \left[\frac{h^2}{h^2 - 1} U_{(1)}\right]^2 \omega_1 C_1 + \frac{1}{h\omega_1 C_1} I_{f(h)}^2$$
(4)

where $Q_{(1)}$ and $Q_{(h)}$ are the fundamental reactive capacity and harmonic reactive capacity respectively.

The magnitude of the fundamental reactive capacity $Q_{(1)}$ generated by the filter branch under the fundamental voltage $U_{(1)}$ is:

$$Q_{(1)} = U_{(1)}I_{f(1)} = \omega_1 C_1 \frac{h^2}{h^2 - 1} U_{(1)}^2$$
(5)

Substituting Equation (5) into Equation (4) to get

$$S_{(h)} = \frac{h^2}{h^2 - 1} \left[Q_{(1)} + \frac{U_{(1)}^2 I_{f(h)}^2}{h Q_{(1)}} \right]$$
(6)

Let the base capacity be $S_{(1)} = U_1 I_{f(h)}$, $S_{(h)}^* = \frac{S_{(h)}}{S_{(1)}}$, and $Q_{(1)}^* = \frac{Q_{(1)}}{S_{(1)}}$, and then substitute them into Equation (6) to get:

$$S_{(h)}^{*} = \frac{h^{2}}{h^{2} - 1} \left[Q_{(1)}^{*} + \frac{1}{hQ_{(1)}^{*}} \right]$$
(7)

Then, the minimum capacity $S_{(h)}^*$ of the HFSS can be obtained:

$$S_{(h)\min}^{*} = \frac{2}{\sqrt{h}} \frac{h^{2}}{h^{2} - 1} \Big|_{Q_{(1)}^{*} = \frac{1}{\sqrt{h}}}$$
(8)

Accordingly, the fundamental reactive capacity generated by the HFSS is:

$$Q_{(1)\min} = Q_{(1)}^* \times S_{(1)} = \frac{1}{\sqrt{h}} \times U_{(1)}I_{f(h)} = \omega_1 C_{1\min} \frac{h^2}{h^2 - 1} U_{(1)}^2$$
(9)

Therefore, the capacitance corresponding to the minimum capacity of HFSS is:

$$C_{1\min 0} = \frac{I_{f(h)}}{U_{(1)}\omega_1} \times \frac{h^2 - 1}{h^2\sqrt{h}}$$
(10)

From Equations (9) and (10), it can be known that so long as the fundamental angular frequency ω_1 , fundamental voltage $U_{(1)}$ and the *h*-order harmonic current $I_{f(h)}$ are filtered, and the *h*-order is determined, the minimum capacity of the capacitors in the HFSS and the corresponding minimum capacitance can be obtained.

Generally, the standard filter capacitors available in the market have relatively fixed capacity. Taking the filter capacitor of a certain company as an example, the capacity of the three-phase filter capacitor with a rated voltage of 400 V is divided into eight levels: 5 kVar, 10 kVar, 20 kVar, 30 kVar, 40 kVar, 50 kVar, and 60 kVar. If the calculated capacity of the filter capacitor does not correspond to the standard filter capacitor, a standard capacitor with capacity slightly larger than the theoretical calculation value can be selected. In addition, the HFSS's capacitors can be switched on/off in groups, so the capacitor capacity can be flexibly determined. For example, if the total installed capacity is satisfied, two or three groups of capacitors (generally no more than four groups) can be installed. Thus, the single-phases capacity of filter capacitor under rated voltage U_{CN} is:

$$Q_{c1} = ceil(2\pi f_h C_{1\min 0} U_{CN}^2 / 5) \times 5$$
(11)

The capacitance corresponding to Q_{c1} is:

$$C_1 = \frac{Q_{C_1}}{2\pi f_h U_{CN}^2}$$
(12)

When HFSS is used in a three-phase system, a three-phase filter capacitor is often selected. Inside the three-phase filter capacitor, the three phases are independent. Generally, a star connection with a neutral line is used to improve the operation safety of the HFSS.

3.3. Parameter Design of Electromagnetic Coupling Reactor

The parameters for the electromagnetic coupling reactor (SS) include inductance, current, voltage, and capacity. The specific calculation method is as follows.

• step 1: Inductance of primary reactance windin

According to the optimal capacitance C_1 of filter capacitor, the *h*-order of the filter's tuning frequency and the fundamental frequency f_1 of the system, the (single-phase) inductance of the SS that meets the resonance condition is:

$$L_{10} = \frac{10^9}{2\pi f_h^2 C_1} \,\,(\text{mH}) \tag{13}$$

Considering the manufacturing error, the inductance of primary reactance winding of the SS is:

$$L_{11} = k_2 L_{10} \,\,(\mathrm{mH}) \tag{14}$$

where k_2 is the inductance expansion coefficient, which refers to the ratio of the actual designed inductance to the theoretical calculated inductance. In general, it ranges 1.05–1.5. According to engineering application experience, its value is set to 1.1.

During engineering design, the inductance adjustment range of SS shall meet the following conditions:

(1) The inductance L_{1k} of SS should be slightly less than L_{11} when fully turned on, where L_{1k} is the minimum of the primary inductance of SS the minimum(see page 13);

(2) L_{10} is the inductance corresponding to the optimal adjustment range $\frac{\pi}{2} \le \alpha < \frac{5\pi}{6}$, which ensures a good regulation performance;

(3) When the resonant frequency increases due to capacitor aging or heating, L_{11} can be enlarged by increasing the trigger angle α of the thyristor so as to restore the resonant frequency to the resonant point;

(4) When the system and HFSS are in parallel resonance, the corresponding parallel resonance impedance mode value is quite large. At this time, a smaller harmonic current will cause a larger harmonic voltage, which will affect the normal operation of equipment. Therefore, the offset of parallel resonance point should also be considered when determining the parameters of SS, so that HFSS can have strong parallel resonance resistance.

step 2: Rated voltage

The rated voltage of the primary and secondary reactance windings of SS.

$$\begin{cases}
U_{Ln11} = U_0 \\
U_{Ln12} = \frac{U_0}{K}
\end{cases}$$
(15)

where *K* is the turns ratio of SS, and is generally set to 4–6.

step 3: Rated current

When the harmonic frequency is f_h , the inductive reactance and capacitive reactance are:

$$\begin{cases} X_{Lh} = 2\pi f_h \times L_{11} = h X_{L11} \\ X_{Ch} = \frac{1}{2\pi f_h \times C_1} = X_{C1}/h \end{cases}$$
(16)

where the harmonic frequency $f_h = hf_1$, f_1 is the fundamental frequency of the power grid (50 Hz), h is the ratio of the harmonic frequency to the fundamental frequency, also known as the harmonic order, X_{L11} and X_{C1} , respectively, are the fundamental inductive reactance of SS primary reactance winding and the fundamental capacitive reactance of

the filter capacitor bank. When L_{11} is in series resonance with the filter capacitor bank C_1 , the imaginary part of the total impedance is 0, and we can get:

$$X_{L11} = \frac{1}{h^2} X_{C1} \tag{17}$$

According to the quality factor q_h of HFSS, the resistance of the HFSS branch can be calculated:

$$R_{fh} == \frac{h\omega_1 L_{10}}{q_h} \tag{18}$$

For HFSS, the quality factor q_h is generally 30–60. Since the internal resistance of the electromagnetic coupling reactor can generally meet the requirements, no external resistor is needed. Therefore, the impedance Z_1 of the primary reactance winding of SS is equal to its inductive reactance X_{L11} . Then, the current RMS of the filter branch is:

$$I_{f(1)} = \frac{K_u U_1}{X_{C1} - X_{L11}} \tag{19}$$

$$I_{1\text{RMS}} = \sqrt{I_{f(1)}^2 + \sum_{i=2}^m I_{f(2m+1)}^2}$$
(20)

where *m* is the number of odd harmonic currents, the value of i is 0, 1, 2, · · ·, *m*, $I_{f(1)}$ is the fundamental current of the power supply system, and $I_{f(2m+1)}$ is the harmonic current. K_u is the voltage fluctuation coefficient, which indicates the approximate fluctuation range of the system's effective voltage under normal operation. Its value ranges from 1.05 to 1.15.

Normally, the rated current of the SS primary reactor winding must be higher than its overcurrent in the case of short circuit (such as a short circuit caused by the damage of a filter capacitor). Thus, the rated current I_{Ln1} of the primary reactor winding is equal to k_2 times I_{1RMS} , where k_2 is the current expansion coefficient. According to engineering application experience, its value ranges from 1.1 to 1.3. The rated current I_{Ln2} of the SS secondary reactance winding is equal to k_2 times I_{Ln1} .

step 4: Rated capacity of electromagnetic coupling reactor

The three-phase capacity of SS is calculated by

$$P_n = \sqrt{3} U_{Ln11} I_{Ln1} \tag{21}$$

3.4. Parameter Checking of Filter Capacitor

In the parameter design of the filter capacitor, its safe operation must be considered. When the filter capacitor is in continuous operation, its rated voltage U_{CN} , rated current I_{CN} and rated capacity Q_{CN} must meet the voltage balance, current balance, and capacity balance, respectively. Therefore, the parameter checking of the filter capacitor includes the current checking and peak voltage.

Current checking

The current allowed by the capacitor bank I_{Cx} is equal to Q_{CN} divided by U_{CN} . When $I_{Cx} > 1.35I_{1RMS}$, the capacitor current meets the requirements; otherwise, it does not and needs to be redesigned and optimized.

Peak voltage

The voltage of the capacitor bank under the action of fundamental and harmonic currents is:

$$\begin{cases} U_{C(1)} = I_{f(1)} X_{C1} \\ U_{C(h)} = \frac{I_h}{h} X_{C1} \end{cases}$$
(22)

The actual peak voltage of the capacitor bank is:

$$U_{Cpk} = \sqrt{U_{C(1)}^2 + \sum_{i=2}^m U_{C(2m+1)}^2}$$
(23)

where $U_{C(1)}$ is fundamental voltage and *m* is the number of odd harmonic currents. The allowable voltage of the capacitor bank is:

$$U_{Cpkx} = K_u \times \sqrt{2}U_1 \tag{24}$$

When $U_{Cpkx} > 1.1U_{Cpk}$, the capacitor voltage meets the requirements; otherwise, it does not meet the requirements and needs to be redesigned and optimized.

After the filtering parameters are designed, they need to be checked by the above steps and can be adopted only when they meet the requirements.

3.5. Parameter Design System of HFSS

For the calculation of various design parameters of the filter, Matlab graphical user interface development environment (GUIDE) is adopted to design the parameter design system of HFSS. The design method is to first add the required components in GUIDE according to the needs of parameter design, then set the properties of each component, then write the callback function program according to the parameter design method of HFSS, and finally complete the debugging and improvement of the program. Based on the requirements of HFSS parameter design, the parameter design system is mainly composed of five modules: filter parameter setting, capacitor design, inductance design, checking capacitance parameters, and result output. Figure 3 shows the GUI of the parameter design system of HFSS.



Figure 3. GUI of the parameter design system of HFSS.

In Figure 3, the buttons are associated with various callback functions (by setting the buttons' properties), and the functions can be performed by pressing the corresponding buttons. The main functions of five modules are as follows.

Parameters setting module

This module is responsible for setting various parameters of the filter, such as harmonic order, the magnitude of each harmonic current, the line voltage and phase voltage of the system, etc.

Capacitor design module

According to the harmonic order, the magnitude of the harmonic current in the system, and the minimum capacitance of filter capacitor, this module calculates the optimal capacity of filter capacitor so as to determine its capacitance. On this basis, the module selects the

type of filter capacitor and then calculates its parameters, including capacitance (uF), nominal voltage (V), and capacitor capacity (kVar).

Inductance design module

According to the magnitude of the fundamental current and the capacitance of the filter capacitor, this module calculates the parameters of the electromagnetic coupling reactor, including inductance, current, voltage, and capacity.

Checking capacitance parameters module

This module checks whether the rated voltage and rated current of the filter capacitor meet the requirements.

Result output module

3.6. Inductance Regulation of Electromagnetic Coupling Reactor

Based on the structure of a traditional core reactor, the core reactor with single winding is designed as SS with primary reactance windings (W_1) and secondary reactance windings (W_2). Its secondary reactance winding access impedance conversion circuit (PEIC). Its topological structure is shown in Figure 4.



Figure 4. Topological structure of SS.

In Figure 4, PEIC is composed of an anti-parallel thyristor, which has three working states: full on, full off, and regulation. Taking R phase as an example, the equivalent circuit of SS is shown in Figure 5.



Figure 5. Equivalent circuit of SS (R phase).

In Figure 5, U_{Ln11} and U_{Ln12} are the terminal voltages of W_1 and W_2 of SS, respectively; I_1 and I_2 are the currents flowing through W_1 and W_2 , respectively. As shown in Figure 5a, when the thyristor is fully turned on, W_2 is equivalent to a short circuit. In this case, i_2 is the largest, and so is i_1 , so the equivalent inductance of SS is the smallest. As shown in Figure 5b, when the thyristor is fully turned off, W_2 is equivalent to an open circuit. In this case, i_2 is the smallest, and so is i_1 , so the equivalent inductance of SS is the largest. It can be seen from Figure 5c that when the thyristor is in normal operation, the thyristor circuit shifts between off and on during a power cycle.

According to the principle of electromagnetic transformation, there is

$$Z_1 = \frac{U_{Ln11}}{I_1} = \frac{KU_{Ln12}}{I_2/K} = K^2 \frac{U_{Ln12}}{I_2} = K^2 Z_g$$
(25)

Set the voltage across PEIC is:

$$u_2 = \sqrt{2U_{Ln12}\sin\omega t} \tag{26}$$

Z' is assumed to be the equivalent impedance of W_2 in the regulation state, then the positive and negative half-waves of current waveform of W_2 are symmetrical. From the Fourier transform formula, we can get:

$$i_{2} = U_{Ln12} \frac{\sqrt{\sin^{2} \alpha + (\pi - \alpha) \sin 2\alpha + (\pi - \alpha)^{2}}}{\pi Z_{g}}$$
(27)

where Z_g is the equivalent impedance of PEIC:

$$Z_g = \frac{\pi Z'}{\sqrt{\sin^2 \alpha + (\pi - \alpha)\sin 2\alpha + (\pi - \alpha)^2}}$$
(28)

The equivalent impedance of W_1 :

$$Z_1 = \frac{K^2 \pi Z'}{\sqrt{\sin^2 \alpha + (\pi - \alpha) \sin 2\alpha + (\pi - \alpha)^2}}$$
(29)

So the equivalent inductance of W_1 (the primary inductance of SS) is:

$$L_{11} = \frac{Z_1}{2\pi f_1} \tag{30}$$

The following conclusions can be drawn from Equations (29) and (30):

- 1. When $\alpha = 0^{\circ}$, the thyristors are fully turned on, in which case the primary inductance of SS is the minimum, denoted by L_{1k} .
- 2. When $\alpha = 180^{\circ}$, the thyristors are fully switched off, in which case the primary inductance of SS is the maximum, denoted by L_{1m} .
- 3. The inductance of SS ranges within $[L_{1k}, L_{1m}]$. By adjusting the trigger angle α of the thyristor, the primary inductance of SS can be continuously adjusted in this range, and its value will increase with the rising trigger angle α .
- 4. When $\frac{\pi}{2} \le \alpha < \frac{5\pi}{6}$, the linearity of the primary inductance is relatively good, so it is the optimal regulation range.
- 5. The variation of the magnetic circuit of SS is complex and thus difficult to control quantitatively.

In summary, the controller can set the initial trigger angle to 0° according to the relationship between the inductance (impedance) and the trigger angle α . Then it adjusts the trigger angle α according to the required control signal U_z to change the inductance L_{11} of SS, so that the HFSS is in series resonance with the assistance of equivalent filter capacitor C_1 , thereby realizing dynamic tuning and optimizing the filtering effect.

3.7. Two-Step Optimization Algorithm

Assuming that the control signal U_z is x, the h-order harmonic current $I_{f(h)}$ that needs to be filtered is the control objective, and that $I_{f(h)}$ and x have a function relationship of $I_{f(h)} = f(x)$, the tuning process of HFSS can be regarded as solving the optimal solution x_{opt} corresponding to $I_{f(h)}$ at its maximum I_{fmax} .

For the HFSS, the harmonic *h*-order and the capacity of the filter capacitor is generally fixed, so its tuning is mainly realized by adjusting the equivalent reactance L_{11} of the primary reactance winding of SS. The value of L_{11} is mainly adjusted by controlling the trigger angle α of the thyristor, whose effective range is $\alpha = 30^{\circ}$. Therefore, assuming that

the control signal corresponding to $\alpha = 30^{\circ}$ is x_0 , and that to $\alpha = 150^{\circ}$ is x, the change range of the control signal is $[x_0, x_n]$.

During the operation of HFSS, the harmonic current I_h generated by the harmonic source load will change with the working conditions, the frequency of the power grid will sometimes fluctuate, and the system impedance will vary occasionally. After the HFSS is switched on, the *h*-order harmonic current flowing into the HFSS branch is $I_{f(h)}$. The approximate curve of $I_{f(h)}$ varying with the control signal *x* in a certain period of time is shown in Figure 6.



Figure 6. Approximate curve of I_f varying with the control signal.

It can be seen from Figure 6 that the curve of the harmonic current $I_{f(h)}$ is characterized by random, time-varying, and non-linear features. It has no unimodality; instead, there are multiple extreme points. Thus, based on the control objective of HFSS, i.e., *h*-order harmonic current $I_{f(h)}$, this paper adopts a two-step optimization algorithm to solve the optimal solution x_{opt} corresponding to $I_{f(h)}$ at its maximum I_{fmax} .

Principle of two-step optimization algorithm.

Initial optimization: The optimization interval is $[x_0, x_n]$, and the optimization step length is θ_1 ; that is, the initial value of the control signal is x_0 . Then, through a consecutive increment of θ_1 , the point (x_t, I_{ft}) at which $I_{f(h)}$ reaches the maximum under this step length is found.

Second self-optimizing: The optimization interval is $[x_t - \theta, x_t + \theta]$ and the optimization step length is $\theta_2(\theta_2 < \theta_1)$; that is, the initial value of the control signal is $x_t - \theta$. Then, through a consecutive increment of θ_2 , the point (x_{opt} , I_{fmax}) at which $I_{f(h)}$ reaches the maximum with this step length is found. This point is the optimal operating point in a certain period of time.

Advantages of two-step optimization algorithm.

The step length θ_1 of initial optimization is large, so it takes a short time to find the operating point (x_t, I_{ft}) at which $I_{f(h)}$ maximizes. However, this point may not be the optimal operating point due to the large step length. For this reason, another smaller step length θ_2 is adopted to perform the second self-optimization in the interval $[x_t - \varphi, x_t + \varphi]$ with x_t as the center and φ as the radius. In this way, the optimization time is shortened and the optimization accuracy is improved. Generally, $\theta_2 = (0.1 \ 0.5) \ \theta_1$.

Process of two-step optimization algorithm.

According to the control objective and two constraints of HFSS, the specific process of the two-step optimization algorithm is shown in Figure 7.



Figure 7. Process of the two-step optimization algorithm.

4. Reactive Power Compensation Method of RPCSS

Through the power factor of the system before compensation and the power factor after compensation, the required reactive power compensation capacity Q_{csj} can be calculated. On this basis, the reactive power capacity provided by the reactive power compensation subsystem (RPCSS) can be obtained to determine the capacitance of the compensator bank C_2 . By controlling the on/off of contactors $KM_{21}-KM_{2n}$, the capacitor $C_{21}-C_{2n}$ is switched on in groups to generate reactive power.

According to Figure 1, when KM_1 is disconnected and KM_2 is closed, the RPCSS is put into operation. To realize reactive power compensation, the following equation must be satisfied:

$$Q_{S} = Q_{F} - Q_{p} - Q_{C2} \tag{31}$$

where Q_S is the reactive power of the system, Q_F is the reactive power of the load, Q_{C2} is the reactive power provided by the RPCSS, and Q_p is the fundamental reactive power compensated for the distribution system when the HFSS is in series resonance.

4.1. Capacitance Determination of Reactive Power Compensation Capacitor

After the RPCSS is put into operation, the phase angle between the voltage and the current decreases from φ_1 to φ_2 , and the power loss is reduced.

Required reactive power compensation capacity.

Based on the active power P_{av} and power factor $\cos \varphi_1$ of the system before compensation as well as the power factor $\cos \varphi_2$ after compensation, the required reactive power compensation capacity Q_{csj} can be calculated:

$$Q_{csj} = P_{av} \tan \varphi_1 - \tan \varphi_2 = P_{av} \left(\frac{\sqrt{1 - \cos \varphi_1^2}}{\cos \varphi_1} - \frac{\sqrt{1 - \cos \varphi_2^2}}{\cos \varphi_2} \right)$$
(32)

The total reactive power capacity provided by RPCSS.

In practical engineering applications, the capacity of capacitor can only be a multiple of 5, so it is set according to the following formula:

$$Q_{C2} = ceil[Q_{csi} - Q_P/5] \times 5 \tag{33}$$

• The equivalent capacitance *C*₂ of RPCSS.

$$C_2 = \frac{Q_{C_2}}{2\pi f_1 U_{CN}^2}$$
(34)

4.2. Control Strategy of RPCSS

The capacitance of RPCSS to the system is regulated by controlling the on/off of contactors. The specific capacitance is determined according to the actual situation of the system. If the number of compensator groups is n, the level number n shall meet the following equation:

$$N = C_n^1 + C_n^2 + C_n^3 + \dots + C_n^n = 2^n - 1$$
(35)

Regardless of the same level number, it can be seen from Equation (35) that *n* groups of capacitor banks have a total of $2^n - 1$ level number, which makes up for the disadvantage of single capacitance of traditional PPF and enlarges the compensation range.

For the switching control of *n* groups of compensators, when a single power factor index is selected as the switching standard, it is easy to reduce the service life of the compensator resulted from frequent switching of the compensator, cause switching oscillations, and affect the operation of other equipment. Therefore, an appropriate control strategy should be adopted so that the compensator can switch on and off smoothly and so that the system's reactive power is maintained within the range of national standards.

The range of the target power factor selected is:

$$\Delta ph = ph_{\max} - ph_{\min} \tag{36}$$

where ph_{max} is the maximum target power factor and ph_{min} is the minimum target power factor which can be selected based on actual enterprise requirements; for example, $\Delta ph = 1 - 0.9 = 0.1$. Δph is the control variable for evaluating the effect of reactive power compensation, and it improves the frequent switching caused by a single value as the control standard. The real-time power factor deviation of the system is:

$$\Delta ph_0 = ph_{\max} - ph_0 \tag{37}$$

where ph_0 is the measured real-time power factor. The switching strategy of compensator is:

- 1. When $0 < \Delta ph_0 \le \Delta ph$, the reactive power of the system conforms to the standard, and the reactive power compensation is maintained;
- 2. When $\Delta ph_0 > \Delta ph$, the system is in an under-compensated state. The controller switches on the compensators in groups according to the principle that the actual capacity of compensator should be equal or slightly higher than the required theoretical capacity Q_{C2} obtained from Equations (32) and (33) to make.

5. Integration Simulation of Harmonic Suppression and Reactive Power Compensation

5.1. Simulation Cases Design

By switching the on/off state of KM_1 and KM_2 in Figure 1, three simulation cases are designed to analyze the load characteristic, harmonic suppression effect, reactive power compensation effect, and comprehensive effect of harmonic suppression and reactive power compensation, as shown in in Table 1. In the table, "0" means that the contactor is off, and "1" means that the contactor is on.

Case	Scheme	KM ₂ KM ₁	Simulation Model Applied
Ι	Load characteristic analysis	0 0	Without
II	harmonic suppression effect analysis	01	HFSS
III	analysis of the integration system	11	HFSS + RPCSS

5.2. Load Characteristic Analysis

• Scheme I: According to the power quality parameters of a power plant's distribution network and the simulation model of the integrated system of harmonic suppression and reactive power compensation, the system simulation time of the system is set to 0.1 s after several experiments. The connection mode of the three-phase power supply adopts a star connection, and RMS of the line voltage is 380 V. The frequency is 50 Hz; in the series RLC circuit, $R = 1 \Omega$, L = 5 mH, and C = 0; the sampling time $T_s = 1/512/50 \approx 3.906 \times 10^{-5}$ s.

In the NL simulation model, different load characteristics can be obtained by changing the phase shift angle of the pulses (β), resistance R_L (Ω) and inductance H_L (mH). Herein, under $R_L = 5 \Omega$ and $H_L = 30$ mH, the phase shift angle of β varies within 0–85° (every increment of 5°), and then the reactive power data of the system can be obtained.

With the load characteristic simulation and analysis system developed in this paper (see Figure 8), the reactive power and harmonic characteristic simulation results are obtained, as shown in Tables 2 and 3, respectively.

Table 2. Reactive power characteristic simulation results (Scheme I).

Case	β/°	P/W	Q/Var	ph
I-1	0	32,056	467	0.9999
I-2	5	31,825	1372	0.9991
I-3	10	31,193	3297	0.9945
I-4	15	29,838	6351	0.9781
I-5	20	27,741	9809	0.9428
I-6	25	25,486	12,584	0.8967
I-7	30	23,224	14,516	0.8480
I-8	35	20,466	16,297	0.7823
I-9	40	17,781	17,367	0.7154
I-10	45	14,971	17,757	0.6446
I-11	50	11,836	17,595	0.5581
I-12	55	9265	16,659	0.4860
I-13	60	8309	16,276	0.4547
I-14	65	7164	15,337	0.4232
I-15	70	4978	13,517	0.3456
I-16	75	3043	11,050	0.2655
I-17	80	1503	7660	0.1925
I-18	85	506	4059	0.1238

Table 3. Harmonic characteristic simulation results (Scheme I).

Case	β/°	$I_1(A)$	THD _i /%	I5/%	I7/%	<i>I</i> ₁₁ /%	<i>I</i> ₁₃ /%	<i>I</i> ₁₇ /%	THD _v /%
I-1	0	86.91	23.66	18.56	12.29	6.09	4.36	1.92	8.39
I-2	5	86.56	26.05	19.26	13.23	7.45	5.92	3.62	9.23
I-3	10	85.55	29.16	19.91	13.97	8.66	7.35	5.31	10.26
I-4	15	83.40	30.67	20.56	13.72	9.27	7.31	6.03	11.40
I-5	20	80.38	31.08	20.67	13.59	9.19	7.32	5.94	13.29
I-6	25	77.20	31.09	20.85	13.41	9.20	7.27	5.94	14.53
I-7	30	73.43	31.17	21.15	13.13	9.27	7.09	5.99	11.94
I-8	35	69.14	31.08	21.28	12.95	9.19	7.09	5.89	12.68

Case	$\beta /^{\circ}$	$I_1(A)$	THD _i /%	I ₅ /%	<i>I</i> ₇ /%	$I_{11}/\%$	$I_{13}/\%$	$I_{17}/\%$	$THD_v/\%$
I-9	40	64.30	31.00	21.32	12.83	8.97	7.17	5.69	10.06
I-10	45	58.70	31.14	21.77	12.40	9.10	6.98	5.83	9.94
I-11	50	52.50	31.29	22.28	11.95	9.28	6.80	5.99	9.03
I-12	55	46.05	31.25	22.46	11.81	8.95	6.90	5.60	6.72
I-13	60	42.15	34.38	20.22	10.34	7.48	5.93	3.82	6.92
I-14	65	40.10	31.56	23.29	11.05	9.27	6.51	5.88	6.59
I-15	70	33.22	31.74	24.07	10.25	9.13	6.31	5.72	5.24
I-16	75	26.05	31.99	25.16	9.15	8.76	6.27	5.22	4.05
I-17	80	17.81	34.35	28.47	6.00	9.27	5.08	5.41	2.65
I-18	85	43.83	4059	36.39	1.79	10.22	2.13	6.03	1.57

Table 3. Cont.

Load Characteristic Simulation and Analysis System



Figure 8. GUI of load characteristic simulation and analysis system.

In order to verify the effectiveness of the method proposed in this paper, a power distribution system with small harmonic voltage distortion rate, small power factor, and larger harmonic current, i.e., Case I-13, is selected as the research object of this paper. The waveforms of active power and reactive power of the system are shown in Figure 9.



Figure 9. Waveforms of active power and reactive power on the system (Case I-13).



In Figure 9, *P* and *Q*, respectively, are the active power and the reactive power of the system. Figure 10 shows the waveforms of the voltage and current in the system.

Figure 10. Waveforms of the voltage and current on the system (Case I-13).

In Figure 10, U_a and I_a , respectively, are the single-phase voltage and current on the system. The waveform of the phase voltage U_a is roughly sinusoidal and symmetrical, with small distortion, but the waveform of the phase current I_a is not sinusoidal and non-symmetrical because of the large distortion. Its harmonic spectrum is shown in Figure 11.



Figure 11. Harmonic frequency spectrum of the current on the power supply side.

The following conclusions can be drawn from the data of Case I-13 in Tables 2 and 3, waveforms in Figures 10 and 11:

- 1. In case of the phase shift angle of the pulses (β) of 60°, the active power *P* is 8309 W, the reactive power *Q* is 13,276 Var, and the power factor is 0.4547. In this case, the power factor is too small, so reactive power compensation is required;
- 2. The waveform distortion of the current in the system is great. The main harmonics exceeding the harmonic current standard are the 5th, 7th, 11th, and 13th harmonics, which need to be filtered.

5.3. Harmonic Suppression Effect Analysis

• Scheme II: Only HFSS is put into operation to analyze the suppression effect of HFSS on the harmonics in distribution network in Case I-13. The active power and reactive

power and power factor before and after filtering are also analyzed for the next dynamic reactive power compensation.

With the harmonic parameters (Case I-13) in Table 3, as well as the HFSS parameters design and simulation system, the LC parameters of HFSS that filter harmonic current I_{sd} are obtained, as shown in Table 4.

Order	I_{sd}/A	C_1/uF	$3Q_{C1}/Var$	L_{11}/mH	I_{L1}/A	P_n/VA
5th	11	75	25	5.67	38.27	14,583.00
7th	6	32	15	6.78	25.08	9557.00
11th	4	21	15	4.19	24.86	9472.93
13th	3	17	15	3.71	27.83	9761.50

Table 4. HFSS design parameters (Case I-13).

According to the design parameters in Table 4, the harmonic filter simulation system developed in this paper (see Figure 12) is used for traditional filtering simulation. Voltage and current waveforms on the system after applying HFSS are shown in Figure 13.



Harmonic Filter Simulation System

Figure 12. GUI of harmonic filter simulation system.

It can be observed from Figure 13 that after filtering out the 5th, 7th, 11th, and 13th harmonics, the current waveforms for the system are basically sinusoidal and symmetrical. Compared with the current waveform in Figure 10, the current distortion is greatly reduced, the amplitude of oscillations is reduced, and the overall waveform is smoother. The harmonic data and power data before and after application of HFSS are shown in Tables 5 and 6, respectively.

Table 5. Harmonic data before and after application of HFSS.

Case	I ₁ /A	THD _i /%	I5/%	I7/%	<i>I</i> ₁ 1/%	<i>I</i> ₁ 3/%	THD_V /%
I-13	42.15	34.38	20.22	10.34	7.48	5.93	6.92
II	32.62	4.7	3.13	2.45	1.99	1.53	6.48
Reduction rate/%	23	86	85	76	73	74	6

Case	P/W	Q/Var	ph
I-13	8309	16,276	0.45
II	8887	10,956	0.63
Reduction rate/%	-7	33	-38

Table 6. Power data before and after application of HFSS.



Figure 13. Waveforms of the voltage and current on the system (Case I-13).

The reduction rate *Y* in Tables 5 and 6 is given by

$$Y = 1 - \frac{X_1}{X_0} * 100 \tag{38}$$

where X_1 and X_0 are, respectively, the simulation data before and after HFSS is put into operation. A negative value of Y indicates the rate of increase. The following conclusions can be drawn from Tables 5 and 6 and Figure 13:

- 1. In Case I-13, after HFSS is put into use, the harmonic currents of the main orders all drop significantly (all above 70%), and the main harmonic current content of the system falls below the national standard. This indicates a significant filtering effect and verifies the rationality of filter parameter design;
- 2. The reactive power reduction rate of the system is about 30%, and the power factor improvement rate is 38%, indicating that the HFSS has a certain effect in reactive power compensation. However, the filtered power factor is still not up to the requirements, so it is necessary to use RPCSS to compensate the system so that the power factor can reach 0.98 or more.

5.4. Analysis of the Comprehensive Effect of Harmonic Suppression and Reactive Power Compensation

 Scheme III: On the basis of Scheme II, the reactive power compensation subsystem (RPCSS) is added to analyze the active power, reactive power, and power factor of the system.

It can be seen from Table 6 that the power factor of the filtered system is 0.63, and the active power is 10,956 W. From Equations (33) and (34), the required capacity Q_{C2} of the system's compensator is obtained: 20 kVar, and its capacitance C_2 is 300 µF.

Waveforms of active power, reactive power and power factor of the system after applying HFSS and RPCSS are shown in Figure 14. The voltage and current waveforms on the power supply side after applying HFSS and RPCSS are shown in Figure 15.



Figure 14. Waveforms of active power, reactive power, and power factor of the system after applying HFSS and RPCSS.



Figure 15. Voltage and current waveforms on the power supply side after applying HFSS and RPCSS (Case III).

The harmonic data and power data before and after applying HFSS and RPCSS are shown in Tables 7 and 8, respectively.

Table 7. Harmonic data before and after applying HFSS and RPCSS.

Case	I ₁ /A	THD _i /%	I ₅ /%	I ₇ /%	<i>I</i> ₁ 1/%	<i>I</i> ₁ 3/%	THD_V /%
I-13	42.15	34.38	20.22	10.34	7.48	5.93	6.92
III	25.35	7.43	3.98	4.55	3.55	2.563	2.79
Reduction rate/%	40	78	80	56	53	57	60

Case	P/W	Q/Var	ph
I-13	8309	16,276	0.45
III	11,170	1626	0.99
Reduction rate/%	-34	90	-120

Table 8. Power data before and after applying HFSS and RPCSS.

From Figures 13 and 15, and Tables 7 and 8, the following conclusions can be drawn.

- 1. After HFSS and RPCSS are used, the reactive power of the system is reduced from 16,276 to 1626 Var, with a reduction rate of 90%, and the power factor increases from 0.45 to 0.99. These indicate that the capacitive reactive power and inductive reactive power are roughly balanced, and that the compensation effect is obvious;
- 2. After filtering the 5th, 7th, 11th, and 13th harmonic currents, the current distortion rate is reduced to below 5%, and the reduction rate ranges from 50% to 80%. Moreover, the voltage distortion rate is also reduced by 60% and the harmonic voltage of the power supply is sinusoidal and symmetrical, indicating an obvious filtering effect;
- 3. When there are harmonics in the distribution network, HFSS should be applied for filtering first until the harmonics in the system meet the national regulations. After that, RPCSS is used for reactive power compensation;
- 4. After the comprehensive treatment is completed, HFSS is put into operation first and then switched off later to avoid the harmonic amplification caused by the direct input of RPCSS, the burn out of main power components of RPCSS, and the system oscillation, so as to ensure the smooth operation of the system.

6. Conclusions

Aiming at the serious harmonic pollution and the low power factor in the distribution network of industrial enterprises, a novel integrated method for harmonic suppression and reactive power compensation has been devised. The integrated method realizes the dual functions of harmonic filtering and reactive power compensation and filters out harmonics while ensuring safe operation of the power compensator. A method for designing the capacity of filter capacitor and the rated parameter of electromagnetic coupling reactance converter has been proposed, and an optimization simulation system has been constructed. The simulation system has been developed and the simulation has been finished. After the treatment, the power factor increased from 0.45 to 0.99, the 5th, 7th, 11th, and 13th harmonic currents, and the current distortion rate been reduced to below 5%, which all meet the national standards. The integrated method could dynamically track harmonics and reactive power changes, filter out harmonics, and improve the power factor and the symmetry level of the power source. Simulation results have verified that the HFSS is put into operation first and then switched off later to ensure the normal operation of other equipment in the distribution network. The research results lay a certain theoretical and technical foundation for the harmonic filtering and reactive power compensation theory, technology, and its device innovation to achieve effective suppression of power harmonics and reactive power compensation. The future development direction of harmonic suppression and reactive power compensation in distribution network will be to seek the development of core components, and try to replace filters and reactive power compensators. The harmonic suppression and reactive power compensation device with high reliability developed by using the method proposed in this paper can filter harmonics first and then compensate reactive power to improve power factor and realize energy saving. It will produce certain economic benefits.

Author Contributions: Y.W. contributed to the conception of the study, the background research, method design and experimental results analysis, and wrote the manuscript; K.Y. helped perform the analysis with constructive suggestions; Y.Y. and H.L. provided an important suggestion about the framework of this paper and revised the manuscript. All authors have read and agreed to the published version of the manuscript.

Funding: This work was supported by the Fundamental Research Funds for the National Natural Science Foundation of China under Grant [61703318].

Institutional Review Board Statement: Not applicable.

Informed Consent Statement: Not applicable.

Data Availability Statement: Not applicable.

Conflicts of Interest: The authors declare no conflict of interest.

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