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Surface Treatments on the Characteristics of Metal–Oxide Semiconductor Capacitors

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Abstract: The properties of metal-oxide semiconductor (MOS) capacitors with different chemical treatments have been examined in this study. A MOS capacitor consists of an Al_2O_3/n -GaN/AlN buffer/Si substrate. Four chemical treatments, containing organic solvents, oxygen plasma and BCl₃ plasma, dilute acidic and alkali solvents, and hydrofluoric acid, were used to reduce the metal ions, native oxides, and organic contaminants. The n-GaN surface was treated with these chemical treatments before Al_2O_3 was grown on the treated n-GaN surface to reduce the interface state trap density (D_{it}). The value of D_{it} was calculated using the capacitance–voltage curve at 1 MHz. The D_{it} of a u-GaN surface was modified using various solutions, which further influenced the contact properties of GaN.

Keywords: chemical treatment; capacitor; interface state trap density

1. Introduction

Surface cleaning treatments are the foundation of a semiconductor device fabrication process [1,2]. Surface cleaning significantly affects the epitaxial defects [2], metal contact resistance/stability [3], and overall device quality of GaN-based devices [4]. Evaluating surface cleanliness requires considering the electrical properties of the device, structure, and interface state trap of the surface. Moreover, a surface treatment is used to remove the native oxides, organic contaminants, metal ions, particulates, residual species, and weaknesses in atomic bonding.

Recently, AlGaN/GaN high electron mobility transistors (HEMTs) were demonstrated for use in power electronic devices. In an HEMT device, a high saturation current, low leakage current, and high transconductance are necessary. Therefore, a low-resistance ohmic contact and low interface state trap density (D_{it}) must be obtained for an HEMT device. Interface states may cause various operational stability and reliability drawbacks in GaN-based HEMTs such as threshold voltage instability [4] and current collapse phenomena [5]. A surface treatment not only improves the device performance but also enhances the ohmic contact characteristics of GaN with metals [3].

In addition, surface treatments have been proposed to improve the ohmic contact properties between a low work function metal contact and a GaN or AlGaN surface. N vacancies are created during surface treatment and act as shallow donors for electrons and increase the surface doping concentration to overcome the Schottky barrier height for carrier transport. Therefore, improving the contact properties and reducing contact degradation are crucial for an AlGaN/GaN HEMT. A poor surface quality causes surface defects and contamination of the interface states, which originate from dangling bonds. The surface chemical treatment is not the only way to improve AlGaN/GaN HEMT device performance. The phosphorus-based annealing processes (POCl₃, P₂O₅) also modified

surface conditions before dielectrics material deposition to further modify the AlGaN/GaN HEMT [6]. The metal-oxide-semiconductor HEMTs(MOS-HEMTs) structure using different dielectrics material causes different device performance of MOS-HEMTs, such as different C–V characteristics, specific resistance ($R_{\rm on}$), breakdown voltage, D_{it} value, saturation drain current of the devices [7]. The MOS-HEMTs structure using Al_2O_3 [8] will be effective to enhance the breakdown voltage generated from the gate leakage. The ALD is a surface-controlled layer-by-layer process for the deposition of thin films with atomic layer accuracy. The Al_2O_3 used for MOS-HEMTs not only improve the basic electronic properties but also show low leakage current and high breakdown voltage. In this study, four chemical pretreatments were used for MOS capacitors before atomic layer deposition (ALD) of Al_2O_3 to modify the surface quality. The characteristics of MOS capacitors and the ohmic contact characteristics of GaN with the four chemical treatments are discussed.

2. Experiments

Figure 1 presents a schematic of an MOS capacitor. The MOS capacitor was grown on silicon (111) substrate through metal-organic chemical vapor deposition. The MOS capacitor consisted of an AlN nucleation layer, 2 μ m GaN buffer layer, and 1 μ m n-GaN layer Figure 1. Dilute HCl was used to remove the native oxides, and an organic solution was then used to remove organic contaminants in an ultrasonic cleaner before the MOS capacitor fabrication process. Mesa isolation was achieved using inductively coupled plasma-reactive ion etching with BCl₃/Cl₂/Ar plasma, and the sample was subjected to four chemical pretreatments before Al₂O₃ oxide layer deposition. The four chemical pretreatments are shown in Table 1.

Table 1. The four chemical pretreatment descriptions.

Subsequently, a 50-nm-thick Al_2O_3 layer as a gate oxide was deposited using ALD at 300 °C under 6 mbar. In the ALD process, water vapor and trimethylaluminum were respectively used as O and Al sources, which were alternate pulse forms, resulting in the formation of the Al_2O_3 layer. Ti/Al/Ti/Au (25 nm/125 nm/45 nm/55 nm) alloyed for an ohmic contact with n-GaN was then formed through rapid thermal annealing (RTA) at 870 °C for 30 s in N_2 ambient. Finally, Ni/Au metal was deposited on the Al_2O_3 by using an E-gun evaporating system. The contact characteristics of Ti/Al/Ti/Au deposited on the GaN were evaluated by circular transmission line model (CTLM). The CTLM structure's inner and outer circle were 50, 100, 150, 200, and 250 μ m, respectively. The inner radius of the pad was 100 μ m.

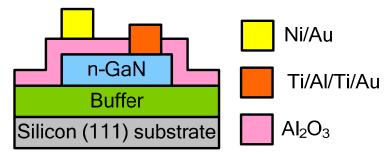


Figure 1. Schematic of an MOS capacitor.

The chemical bonding states on the GaN surface were characterized using X-ray photoelectron spectroscopy (XPS) with a monochromate Al $K\alpha$ X-ray (energy; 1486.6 eV). The shift in the XPS spectra was calibrated using a charge neutralization gun because of surface charge accumulation by emitting photoelectrons. The angle between the incident photons and the detected photoelectrons was set at 45° , which is sensitive to an analysis of surface chemical states.

3. Results and Discussion

The hysteresis behavior of capacitance–voltage (C–V) curves is strongly correlated to the trap density at the GaN/Al_2O_3 interface of a MOS capacitor. Figure 2 shows the hysteresis behavior of the C–V curves of MOS capacitors treated with various chemical treatments. Obviously, the MOS capacitor treated with treatment 1 exhibited the largest amount of hysteresis. Treatment 1 consisted of an organic solvent and O_2 plasma, which were used to remove particles from the air ambiance, and stripped residual photoresist and organic contaminants. The dangling bond, weaknesses in atomic bonding, and native oxides are difficult to remove using treatment 1. Generally, they are removed by complexes composed of organic and inorganic solvents, such as treatments 2–4.

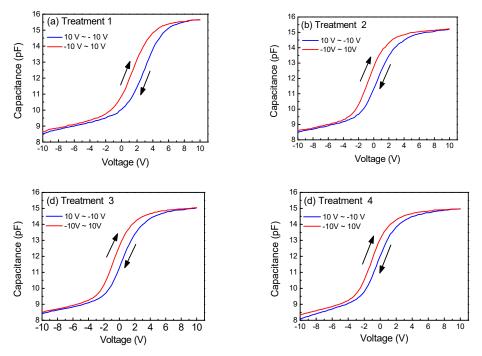


Figure 2. The Capacitance–voltage curves of MOS capacitors treated with (**a**) treatment 1, (**b**) treatment 2, (**c**) treatment 3, and (**d**) treatment 4.

To calculate the D_{it} value, the oxide capacitance (capacitance at accumulation, C_{ox}) was measured using the C–V curves. The flat band voltage (V_{FB}) was calculated using Equation (1) to quantify the

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relative shifts for analyzing the hysteresis behavior of the capacitors; V_{FB} was measured using the C–V curves at a point of C_{FB} obtained using [9]:

$$C_{FB} = \frac{C_{ox}\varepsilon_s A/\lambda}{C_{ox} + \varepsilon_s A/\lambda} \tag{1}$$

where $\varepsilon_s = 9.5$ is the dielectric constant of GaN, $\lambda = (\varepsilon_s \varepsilon_0 k_B T/q^2 N_D)^{1/2}$ is the Debye length of n-GaN [10], T is the absolute temperature, q is the electron charge, k_B is the Boltzmann constant, and $N_D = 6 \times 10^{17}$ cm⁻³ is the electron concentration of n-GaN. The relative shifts in V_{FB} during the sweep down (10 to -10 V) and up (-10 to 10 V) for the surface treatments of GaN with various treatments are consistent with the presence of interface state trap densities at or near the GaN/Al₂O₃ interface, which was based on the C–V curve (Figure 2). The hysteresis at V_{FB} (shift in V_{FB}) was used to approximate the interface state trap densities in each sample, according to the C–V characteristics. The flat-band voltage V_{FB} of the sample is shown in Table 2, and the threshold onset voltage V_{th} is obtained using [11]:

$$V_{th} = V_{FB} - 2|\phi_b| - \frac{\sqrt{4q\varepsilon_s\varepsilon_o N_D|\phi_b|}}{\varepsilon_{ox}\varepsilon_o/t_{ox}}$$
 (2)

where $\phi_b = (k_BT/q)\ln(N_D/n_i)$, $n_i = 2.0 \times 10^{-10}$ cm⁻³ is the intrinsic carrier concentration of GaN at room temperature [12], $\varepsilon_{ox} = 9.9$ is the dielectric constant of Al_2O_3 , and t_{ox} is the thickness of the Al_2O_3 dielectric. The relative shift (ΔV_{th}) is the variation in V_{FB} during the sweep down (10 to -10 V) and up (-10 to 10 V) and is calculated using Equation (2) for the surface treatment of GaN with different treatments. A small voltage shift (ΔV_{th}) is attributable to the different charging conditions of the interface states with different chemical treatments. The interface state trap densities (D_{it}) can be estimated using [13,14]:

$$D_{it} = \frac{C_{ox}\Delta V_{FB}(T)}{q} \tag{3}$$

where q and C_{ox} are the electron charge and accumulation capacitance per unit of area, respectively. In the worst case scenario, treatment 1 showed a D_{it} value of 1.74×10^{12} cm⁻². The lowest D_{it} , 8.30×10^{11} cm⁻², was obtained using treatment 4. The D_{it} of GaN treated with treatment 4 was reduced by approximately 50% compared with that of GaN treated with treatment 1. Treatment 4 consisted of HF, HCl, and NH₄OH, which is used for removing native oxides, metal ions, and organic contaminants of GaN. Therefore, the GaN surface treated with treatment 4 showed the cleanest surface, and the lowest D_{it} was obtained. The oxide capacitance (capacitance at accumulation, C_{ox}), flat band voltage (V_{FB}), real thickness of Al₂O₃ measured using transmission electron microscopy, flat band capacitance (C_{FB}), and interface state trap densities (D_{it}) are summarized in Table 2. The different chemical treatment causes a different D_{it} value, the chemical treatment not only affects the D_{it} , but also modified the GaN surface contact properties with Ti/Al/Ti/Au. Therefore, D_{it} value was related to surface contact resistance with Ti/Al/Ti/Au. The high D_{it} value will result in the contact resistance. Because the D_{it} value is sensitive to the GaN surface condition with different chemical treatment, in order to further understand the surface condition after chemical treatment, the XPS spectra analysis and XPS Ga-O/Ga-N ratio were used to explain the D_{it} value changed with different treatment recipes.

The chemical treatment technology modified the n-GaN surface, subsequent dielectrics material growth, contact resistance with metal material, and HEMT device performance. A circular transmission line model was used to evaluate the ρ_c of GaN contacted with Ti/Al/Ti/Au. The results indicated that the ρ_c of the samples treated with treatments 1–4 were 2.77×10^{-4} , 3.51×10^{-4} , 2.63×10^{-4} , and 2.20×10^{-4} Ω -cm², respectively. The different ρ_c value was related to the GaN surface barrier height, the details were described in Figure 5. The ρ_c of GaN treated with treatment 4 was reduced by approximately 22% compared with that of GaN treated with treatment 1. The contact characteristics of GaN with Ti/Al/Ti/Au were affected by the coverage of oxide and carbon contaminants. Complex cleaning agents, such as treatments 2–4, were used to remove or reduce the contaminants. Treatments

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2–4 contained HCl and HF, which are known to remove oxides from Ga-based semiconductors [15]. However, HCl and HF wet-chemical pretreatments are more effective in producing the lowest coverage of oxide and carbon contaminants [16] to modify the contact characteristic of GaN with Ti/Al/Ti/Au. Treatment 4 consisted of an alkaline solution, acidic solution, and diluted HF, and was used to remove organic contaminants, metal ions, and native oxides. Notably, NH₄OH:H₂O (1:3) predominantly removes gallium oxide (Ga₂O₃) from the GaN surface [17] and organic contaminants, thus improving the adhesion ability of the metal film. Therefore, treatment 4 resulted in the lowest contamination and cleanest surface; thus, the lowest ρ_c was obtained.

Table 2. The C_{ox} , V_{FB} , hysteresis at V_{FB} , and D_{it} of an MOS capacitor after different chemical treatments
and the specific contact resistance (ρ_c) of GaN contacted with Ti/Al/Ti/Au.

Item	Treatment 1	Treatment 2	Treatment 3	Treatment 4
C_{ox} (pF)	15.6	15.1	14.9	14.9
C_{FB} (pF)	13.98	13.57	13.41	13.41
Hysteresis at V_{FB} (V)	1.4	1.3	0.9	0.7
t_{ox} (nm) (Thickness observed by TEM)	49.02	48.50	47.13	48.16
D_{it} (cm ⁻²)	1.74×10^{11}	1.56×10^{12}	1.07×10^{12}	8.30×10^{11}
ρ_c of CTLM (Ω -cm ²)	2.77×10^4	3.51×10^4	2.63×10^{4}	2.20×10^4

The ρ_c is related to the surface barrier height of the GaN surface. The Ga–O to Ga–N ratio of the Ga3d peak was used to facilitate the analysis of the surface barrier height of GaN through various chemical treatments. XPS was used to study the surface composition on the GaN surface by using different chemical treatments. Figure 3a-d shows the Ga3d core level of the XPS spectra. The Ga3d peaks of GaN obtained using treatments 1-4 appeared at 19.4, 19.4, 19.6, and 19.4 eV, respectively. A blue shift of approximately 0.2 eV was observed toward the high binding energy in a sample treated with treatment 3, compared with samples treated with the other treatments. This type of shift is assumed to have been caused by the loss of N at the surface or the creation of N vacancies, which would increase the n-type doping at the surface [18]. In addition, the shift could have been caused by the BCl₃ plasma. Moreover, the Ga3d of XPS spectra photoelectrons can be separated into Ga–O and Ga-N components for various treatments. The main peak at a binding energy of 19.3 eV corresponded to the Ga-N bond, and the second peak at 20.3 eV corresponded to the Ga-O bond, thus confirming the presence of Ga_2O_3 as the native oxide layer on top of the GaN layer. The intensity and area of the Ga-O core level of the Ga3d peak after different treatments are functions of surface conditions. The Ga-O core level is reduced by a more complex chemical treatment association. The reduction of the Ga–O core level indicated that the Ga₂O₃ layer was effectively removed or reduced. Otherwise, the remaining Ga₂O₃ layer might affect the quality of the ohmic contact and thus increase the contact resistance between GaN and metal.

Figure 4 shows the integrated Ga–O core level levels, normalized using the Ga–N core level as a function of surface conditions to evaluate the residual native oxide layer on the GaN surface. The lowest Ga–O to Ga–N ratio of the Ga3d peak was obtained after treatment with treatment 4. Therefore, the ρ_c of GaN contacted with Ti/Al/Ti/Au was the lowest for the sample treated with treatment 4. The area ratios of Ga–O to Ga–N obtained after different treatments are consistent with the contact characteristics of GaN with Ti/Al/Ti/Au (Table 2). In addition, the peak ratio of Ga–O to Ga–N obtained using treatment 2 was the highest. This is because the Ga–O area of Ga3d obtained using treatment 2 was the highest, resulting in a high contact resistance. The Ga–O area was the highest because the fresh dangling bonds created by O₂ plasma and trap more O₂ existing in HCl:H₂O and HF:H₂O solutions. The [Ga–O]/[Ga–N] ratios decreased because of the reduction in the O concentration and/or increase in the N concentration at the GaN surface [2]. In our study, samples treated with treatment 4 had the lowest [Ga–O]/[Ga–N] ratio. After O₂ plasma treatment, the residual GaO could be further etched by HF:H₂O, following oxidation by NH₄OH:H₂O and etched away by HCl:H₂O and HF:H₂O. The [Ga–O] decreased and [Ga–N] increased during GaO etching.

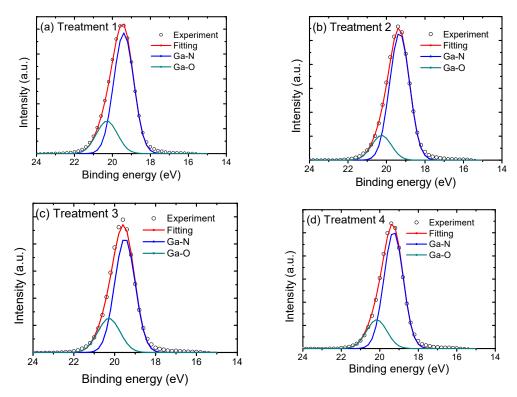


Figure 3. XPS spectra of the Ga3d core levels of the GaN layer after treatments with (a) treatment 1, (b) treatment 2, (c) treatment 3, and (d) treatment 4.

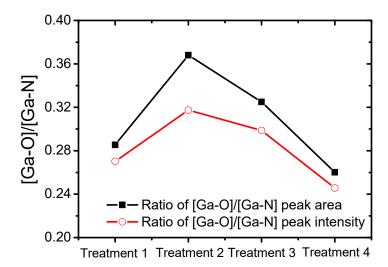


Figure 4. Ratio of Ga–O/Ga–N obtained using the XPS spectra from the Ga3d core levels of the GaN layer.

Figure 5 shows the XPS valence band spectra of GaN treated with various treatments. A binding energy of 0 eV on the horizontal axis corresponded to the energy position of the Fermi level (E_f) at the surface. The energy position of the valence band maximum (VBM) was determined by linearly extrapolating the spectrum near the onset [19] to calculate the surface barrier height of GaN. The surface barrier height (Φ_B) is defined as $E_c - E_f$, where E_c is the energy position of the conduction band minimum. The VBM of GaN treated with different treatments was lower than the Fermi level by approximately 1.68–2.26 eV.

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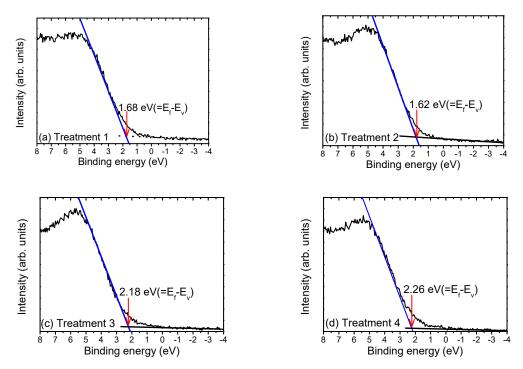


Figure 5. XPS valence band spectra of GaN after treatments with **(a)** treatment 1, **(b)** treatment 2, **(c)** treatment 3, and **(d)** treatment 4.

The surface treatment of GaN modifies the GaN surface condition, including surface barrier height, binding energy and surface quality, which will further change the GaN ohmic contact properties with Ti/Al/Ti/Au. The XPS valence band and XPS spectra were powerful enough to determine the surface barrier height of GaN after treatment. The high surface barrier height caused the poor ohmic contact. The surface barrier height of GaN after treatment with treatment 1-4 is shown in Figure 6. The trend of the surface barrier height is consistent with that of the ρ_c . A high ρ_c indicates a high surface barrier height of GaN. The surface barrier height of the GaN surface after BCl₃ plasma treatment (in treatment 3) was lower than that of GaN after the treatment 1 and 2 treatments. The BCl₃ plasma increased the surface N vacancy of GaN, which acts as a donor-type density for electrons, thus increasing the surface doping concentration [20]. The increase in the donor-type density of the GaN surface further improved the metallurgical process to reduce the surface barrier height of GaN during RTA. During RTA, Ti undergoes a metallurgical reaction with GaN, forming interfacial nitrides such as TiN. This can cause the GaN subsurface below the TiN to be heavily doped (n-type) [21]. Therefore, the surface barrier height of GaN after treatment 3 was lower than that after treatments 1 and 2, and the surface barrier height of GaN after treatment 4 was the lowest. This result indicates that treatment 4 had the highest surface donor density, which contributed to a reduction in the ohmic contact resistance. A lower surface barrier height of GaN indicated a low contact resistance of GaN with Ti/Al/Ti/Au, thereby easily forming an ohmic contact for device applications.

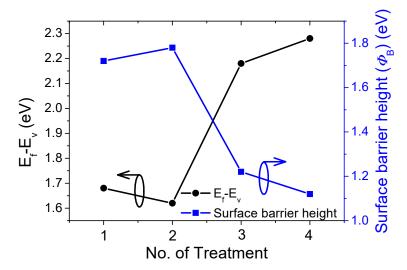


Figure 6. $E_f - E_v$ and surface barrier heights of GaN after treatments with treatment 1–4.

4. Conclusions

In summary, chemical treatments were successfully used to reduce the D_{it} and improve the surface quality. The lowest D_{it} of 8.30×10^{11} cm⁻² of the MOS capacitor was obtained when the sample was treated with treatment 4 because treatment 4 consisted of an alkaline solution, acidic solution, and diluted HCl, and was used to remove organic contaminants, metal ions, and native oxides. The chemical solution treatment not only reduced the contamination but also introduced the donor density to change the ohmic contact property of n-GaN with metal material.

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Conflicts of Interest: The authors declare no conflict of interest.

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