



# Article Flux Weakening Controller Design for Series-Winding Three-Phase PMSM Drive Systems

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Abstract: Series-winding three-phase PMSMs have a higher bus voltage utilization than the conventional three-phase PMSMs with star connection. This topology is suitable for applications with a limited bus voltage. However, the zero-sequence current controller will reduce the bus voltage utilization of the series-winding PMSMs, which causes problems in the flux-weakening controller design. The conventional flux-weakening control algorithms will cause the series-winding PMSMs to enter the overmodulation region early and degrade the performance of the zero-sequence current suppression algorithm. In this paper, a new flux-weakening controller with a dynamic fundamental voltage limit (FW-DFVL) is designed for the series-winding three-phase PMSM traction system. Firstly, the space vector modulation method combines the proposed virtual zero-sequence vectors to realize both the fundamental current generation and the zero-sequence current suppression. The accurate bus voltage utilization in the fundamental current subspace can be derived from the proposed modulation method. Secondly, the gradient descent method generates the flux-weakening d-axis reference current with the dynamic fundamental voltage, which will converge faster than the conventional PI-based flux-weakening control scheme. Thirdly, the flux-weakening controller in the overmodulation region is also designed where the zero-sequence current will no longer be suppressed. The bus voltage utilization is  $V_{dc}$  in this operation mode. Finally, both the simulation and experimental results are utilized to verify the effectiveness of the proposed FW-DFVL, where faster dynamic performance and higher bus utilization are observed.

**Keywords:** series-winding PMSM; flux-weakening control; overmodulation region; zero-sequence loop; high bus voltage utilization

# 1. Introduction

The emerging magnetic-field-modulated motor design [1] and the high bus voltage utilization drive topologies [2] will promote the use of permanent magnet synchronous motor (PMSM) drive systems in low-speed and high-torque applications such as the servo motor drive system [3] and electrical vehicles [4].

Among these new drive topologies, the series-winding PMSM [5,6] is suitable for electric vehicles because of its higher bus utilization and better fault-tolerant capability. Moreover, the concept of series-winding PMSM can be combined with the multiphase PM machines to obtain the advantages of both higher torque density and bus voltage utilization [7,8].

The flux-weakening control algorithms are necessary to extend the operating speed range of PMSMs [9,10]. The central concept of the flux-weakening control scheme is to reduce the active flux in the rotor by negative *d*-axis currents to keep the PMSM operating over the rated speed [11] (with the fixed bus voltage). This concept has been widely applied in the conventional three-phase PMSM [12].

Generally, the flux-weakening control scheme in the three-phase PMSM is divided into two types. The first concept is the feedback method, where the reference *d*-axis current is



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**Copyright:** © 2023 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). determined according to the residual between the fundamental voltage limit and required voltage amplitude [13,14]. These control strategies do not depend on the motor parameters and have excellent steady-state performance [15,16].

Another concept is the feedforward method [17], where the flux-weakening current is calculated directly. However, these controllers always neglect the voltage drop caused by the resistance [18]. Moreover, the parameter mismatches will degrade algorithm performance [19]. In contrast, the fast dynamic performance of feedforward methods is preferred in traction systems. Some hybrid approaches have been developed to obtain fast dynamic performance and good steady-state performance [20,21].

However, the existing research on the flux-weakening controller applied in the conventional three-phase PMSM with star connection only utilizes the fixed value as the voltage limit [22]. In contrast, the fundamental voltage limit in the series-winding PMSM is not a constant [23]. Compared with the three-phase PMSM, there is an additional zero-sequence loop in the series-winding PMSM [24]. Since the zero-sequence currents generally do not produce electromagnetic torque, most literature suppresses the zero-sequence currents to reduce the copper losses [25]. Thus, the controller realizes both the fundamental current generation and the zero-sequence current (ZSC) suppression. In this case, the fundamental voltage limit will be affected by the zero-sequence voltage (ZSV) amplitude.

Similarly, the harmonic currents are always suppressed in the multiphase PMSMs to reduce the copper losses [26,27]. Thus, the bus voltage utilization of multiphase PMSM controllers is also affected by the harmonic current controller [28,29]. The degradation in bus voltage utilization is inevitable in PM machines with an additional current loop [30]. Suppose the improper bus voltage limit is applied in the conventional PI-based flux-weakening controller, the larger *d*-axis current or the distorted phase currents will cause additional copper losses [31,32].

In this paper, the flux-weakening control strategy for series-winding PMSM is designed. Firstly, a new modulation method is provided where the accurate fundamental voltage limit is expressed as the function of the required ZSV. Secondly, the gradient descent method solves the reference flux-weakening current in real time. Finally, the overmodulation method of series-winding PMSM will be discussed to extend the operation region further. Both the simulation and experimental results verify the effectiveness of the proposed strategy.

# 2. Topology of Series-Winding PMSM Drive System

This section introduces the topology and mathematical model of the series-winding PMSM drive system. The drive system of series-winding PMSM is shown in Figure 1, where four half-bridges drive the phase winding of three-phase PMSM. In the series-winding permanent magnet synchronous motor (PMSM), there are three distinct current axes. The *z*-axis represents the zero-sequence loop, as shown in Figure 1a, and the zero-sequence inductance describes the motor windings' ability to store energy in a magnetic field common to all three phases of the motor. The *d*-axis (also known as the direct axis) is a reference axis that corresponds to the motor's magnetic pole axis. Typically, the *d*-axis coincides with the motor's rotor axis, as depicted in Figure 1b. The *q*-axis (also known as the quadrature axis) is another reference axis that is perpendicular to the *d*-axis. The *q*-axis is oriented at a 90-degree electrical angle to the *d*-axis, which means that it is perpendicular to the direction of the motor's magnetic poles, as shown in Figure 1b.

There is a mapping between phase and half-bridge currents, phase voltages, and half-bridge switching states.



Figure 1. Drive system of series-winding PMSM.

Firstly, the transformation matrix between the half-bridge currents to the phase currents is shown in Equation (1). With this transformation, the phase currents are reconstructed from the half-bridge currents [33].

$$\begin{bmatrix} i_a \\ i_b \\ i_c \end{bmatrix} = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 1 & 1 & 0 & 0 \\ 1 & 1 & 1 & 0 \end{bmatrix} \begin{bmatrix} i_{l1} \\ i_{l2} \\ i_{l3} \\ i_{l4} \end{bmatrix}$$
(1)

where  $i_a$ ,  $i_b$ , and  $i_c$  are the phase currents, and  $i_{l1}$ ,  $i_{l2}$ ,  $i_{l3}$ , and  $i_{l4}$  are the currents in four half-bridges.

Secondly, the phase voltages are also obtained from the duty cycles of four half-bridges. The mapping relationship is expressed as:

$$\begin{bmatrix} u_a \\ u_b \\ u_c \end{bmatrix} = \begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 1 & -1 & 0 \\ 0 & 0 & 1 & -1 \end{bmatrix} \begin{bmatrix} d_{l1} \\ d_{l2} \\ d_{l3} \\ d_{l4} \end{bmatrix}$$
(2)

where  $u_a$ ,  $u_b$ , and  $u_c$  are the phase voltages, and  $d_{l1}$ ,  $d_{l2}$ ,  $d_{l3}$ , and  $d_{l4}$  are the duty cycles of four half-bridges.

According to Equations (1) and (2), the phase voltages and currents of series-winding PMSM are obtained from voltages and currents in four half-bridges. Then, the voltage equation of the series-winding PMSM is described in the fundamental subspace and zero-sequence loop.

The Clarke transform decouples the phase components into the orthogonal coordinate subspace and the zero-sequence loop as shown in Equation (3).

$$\begin{bmatrix} F_{\alpha} \\ F_{\beta} \\ F_{z} \end{bmatrix} = \frac{2}{3} \begin{bmatrix} 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \\ \frac{1}{2} & \frac{1}{2} & \frac{1}{2} \end{bmatrix} \begin{bmatrix} F_{a} \\ F_{b} \\ F_{c} \end{bmatrix}$$
(3)

where *F*<sup>\*</sup> represents voltage or current.

Then, these components are further decoupled into the rotating coordinate system as shown in Equation (4).

$$\begin{bmatrix} F_d \\ F_q \\ F_z \end{bmatrix} = \begin{bmatrix} \cos\theta_e & \sin\theta_e & 0 \\ -\sin\theta_e & \cos\theta_e & 0 \\ 0 & 0 & 1 \end{bmatrix} \begin{bmatrix} F_\alpha \\ F_\beta \\ F_z \end{bmatrix}$$
(4)

where  $\theta_e$  is the electrical angle of the rotor.

The voltage equation is established in the *dqz* subspace and expressed as [34]:

$$\begin{bmatrix} u_d \\ u_q \\ u_z \end{bmatrix} = R_s \begin{bmatrix} i_d \\ i_q \\ i_z \end{bmatrix} + \begin{bmatrix} L_d & 0 & 0 \\ 0 & L_q & 0 \\ 0 & 0 & L_z \end{bmatrix} \frac{d}{dt} \begin{bmatrix} i_d \\ i_q \\ i_z \end{bmatrix} + \omega_e \begin{bmatrix} -L_q i_q \\ L_d i_d + \psi_f \\ -3\psi_{f3}\sin(3\theta_e) \end{bmatrix}$$
(5)

where  $\psi_f$  and  $\psi_{f3}$  are the fundamental and third harmonic rotor flux,  $R_s$  denotes the stator resistance,  $L_d$ ,  $L_q$ , and  $L_z$  denote the *d*-axis, *q*-axis, and *z*-axis inductance, and  $\omega_e$  denotes the rotor electrical angle speed.

The electromagnetic torque is:

$$T_e = 1.5n_p \left( \psi_f i_q + (L_d - L_q) i_d i_q \right) \tag{6}$$

where  $n_p$  is the number of pole pairs.

Finally, there are 16 voltage vectors in the series-winding PMSM. The distribution of these voltage vectors is shown in Figure 2a, where the voltage vectors have components in both the fundamental subspace and the zero-sequence loop. Moreover, Figure 2b provides the mapping of these voltage vectors in the fundamental subspace. The yellow area is the fundamental voltage subspace without ZSV.



Figure 2. Voltage vectors of series-winding PMSM.

### 3. Conventional Flux-Weakening Strategy

In this section, the conventional flux-weakening control strategy is introduced. Since there is no literature on the flux-weakening control strategy of series-winding PMSM, we will introduce the conventional flux-weakening control algorithm for three-phase PMSM [23] and combine it with the existing current control algorithm for series-winding PMSM [30].

The total control diagram is shown in Figure 3, divided into three parts. The first part is the outer speed loop, where the *q*-axis current reference is determined by the residual error between the reference speed and the actual speed.

The second part is the flux-weakening control section, where the reference *d*-axis current is determined by a PI controller. If the required fundamental voltage amplitude exceeds the fundamental voltage limit, this PI controller will adjust the reference *d*-axis current to ensure the feasible fundamental voltage vector [35]. It needs to be mentioned that the fundamental voltage limit in the conventional flux-weakening control scheme is constant and equal to  $V_{dc}$ .

The third part is the current control section. The voltage reference is determined by the residual error between the reference current from the first and second parts and the actual current from sensors. Then, according to the voltage reference, the space vector modulation method [30] will be applied to generate the duty cycle of four bridges. The carrier-based

pulse-width modulation (CBPWM) [8] can also be utilized to generate the duty cycles of four half-bridges.



Figure 3. The control diagram of the conventional flux-weakening control strategy.

The main problem of the conventional flux-weakening controller is the inaccurate fundamental voltage limit. Since the suppression of ZSC requires the utilization of additional voltage vectors, the fundamental voltage limit cannot reach  $V_{dc}$  and will vary in real-time. This inaccurate fundamental voltage limit will increase total harmonic distortion (THD) and worsen the performance [36].

# 4. Proposed Improved Flux-Weakening Control Strategy

This section will introduce an improved flux-weakening control strategy with a dynamic fundamental voltage limit. The purpose of this control algorithm is to avoid the performance degradation of the controller in the flux-weakening operation region caused by the inaccurate fundamental voltage limit.

# 4.1. Dynamic Fundamental Voltage Limit

The fundamental voltage limit considering ZSV is derived in this subsection. The improved modulation methods are proposed to obtain an accurate fundamental voltage limit. The basic voltage vectors in Figure 2 are divided into two parts. The first part is the fundamental voltage vectors, as shown in Figure 4a. These voltage vectors have no components in the zero-sequence loop. The second part is the zero-sequence voltage vectors, as shown in Figure 4b, with no fundamental subspace components. The yellow area is the fundamental voltage vectors without ZSV.

The first virtual zero-sequence vector  $ZSV_1$  is expressed as:

$$ZSV_1 = \frac{1}{3}v_{05} + \frac{2}{3}v_{03} = \frac{1}{3}V_{dc}$$
<sup>(7)</sup>

Similarly,  $v_{12}$  and  $v_{10}$  can also generate the second virtual zero-sequence vector  $ZSV_2$  as:

$$ZSV_2 = \frac{2}{3}v_{12} + \frac{1}{3}v_{10} = -\frac{1}{3}V_{dc}$$
(8)

The preselected fundamental voltage vectors and the virtual zero-sequence voltage vectors are combined to generate the required voltage vectors in the modulation process. Then, the synchronized switching signals should be designed. For instance, a voltage vector with the positive zero-sequence voltage vector is shown in Figure 5a. The switching signals on the left (orange) represent the fundamental voltage vectors, and the switching signals on the right (green) represent the virtual zero-sequence voltage vectors.



(a) Preselected fundamental voltage vectors



 $v_{03}$ 



Figure 4. Preselected voltage vectors.



Figure 5. Switching signal centralization.

Then, the synchronized switching signals are shown in Figure 5b to avoid the increasing switching frequency. The high-level switching signals in one cycle will be added and generated as a standard PWM wave.

Finally, the maximum linear fundamental voltage limit can be obtained from Figure 5a. Since the virtual zero-sequence voltage vectors consume the action time, the maximum fundamental voltage amplitude  $V_{\max|\alpha\beta}$  can be expressed as:

$$V_{\max|\alpha\beta} = V_{dc}(1 - T_{ZSV}/T_s) \tag{9}$$

where  $T_s$  is the control period, and  $T_{ZSV}$  is the control action time of virtual zero-sequence voltage vectors.

Moreover,  $T_{ZSV}$  can be expressed as:

$$T_{ZSV} = \left| u_z^{ref} \right| T_s / (V_{dc}/3) \tag{10}$$

Thus, the dynamic fundamental voltage limit  $V_{\max|\alpha\beta}$  is the function of the required zero-sequence voltage vector, which is changed with time.

#### 4.2. Flux-Weakening Current Generation

With the dynamic fundamental voltage limit obtained from the last section, a feedforward flux-weakening control scheme is introduced. Firstly, the voltage limit in the fundamental subspace is:

$$\sqrt{u_d^2 + u_q^2} = u_{s|dq} \leqslant V_{\max|\alpha\beta} \tag{11}$$

Then, without the varying currents, this equation can be expressed as:

$$\left(R_{s}i_{q}^{ref} + \omega_{e}L_{d}i_{d}^{ref} + \omega_{e}\psi_{f}\right)^{2} + \left(R_{s}i_{d}^{ref} - \omega_{e}L_{q}i_{q}^{ref}\right)^{2} \leqslant \left(V_{\max|\alpha\beta}\right)^{2}$$
(12)

In general, if the system operates in the flux-weakening condition, the current trajectory in Equation (12) is the optimum current trajectory to fulfill the fundamental voltage limit. However, in the series-winding PMSM,  $V_{\max|\alpha\beta}$  varies with time, and the corresponding current trajectory cannot be applied directly. The minimum value of  $V_{\max|\alpha\beta}$  is treated as the optimal value of the fundamental voltage limit.

Moreover, since the inner voltage source in the zero-sequence loop is the third back EMF component, the amplitude of the required zero-sequence voltage will also change with the speed. Thus, the reference speed change can be treated as a reset signal for the voltage limit. The dynamic fundamental voltage limit (DFVL) determination process is shown in Figure 6. Firstly, the required zero-sequence voltage vector from the PIR controller is applied in Equations (9) and (10). Then, the minimum value of the real-time  $V_{\max|\alpha\beta}$  is applied as the fundamental voltage limit, and this minimum value will reset if the reference speed had a step change.



Figure 6. The process of dynamic fundamental voltage limit.

Finally, to verify the performance of DFVL, Figure 7 provides the experimental results under speed step change, where the reference speed is set from 400 rpm to 1000 rpm. According to the experimental results, it can be observed that with the increasing speed, DFVL will decrease. The blue area in Figure 7 represents the distribution of DFVL.



Figure 7. Experimental results of DFVL from 400 rpm to 1600 rpm.

Then, the flux-weakening current trajectory is expressed as:

$$f\left(\omega_{e}, i_{d}^{ref}, i_{q}^{ref}\right) = \left(R_{s}i_{q}^{ref} + \omega_{e}L_{d}i_{d}^{ref} + \omega_{e}\psi_{f}\right)^{2} + \left(R_{s}i_{d}^{ref} - \omega_{e}L_{q}i_{q}^{ref}\right)^{2} - \left(\min\left[V_{\max|\alpha\beta}\right]\right)^{2} = 0$$
(13)

However, it is difficult to express the flux-weakening current as an explicit speed and *q*-axis current function due to the resistive voltage drop. Therefore, an online solution method, the gradient descent method, is used to solve this problem. The optimal process can be expressed as:

$$i_d^{ref}(k+1) = i_d^{ref}(k) - \eta d \frac{f\left(\omega_e, i_d^{ref}, i_q^{ref}\right)}{d\left(i_d^{ref}\right)}$$
(14)

where  $\eta$  is the learning rate set as  $1 \times 10^{-6}$  in this paper.

After several iterations, the reference value of the flux-weakening current reference will converge to the solution of Equation (13) and eventually stabilize around the optimal value.

### 4.3. Overmodulation Design

In this section, the overmodulation strategy is designed. According to the last subsection, DFVL decreases with increasing speed. Thus, the over-modulation approach is necessary to take full advantage of higher bus utilization.

If the motor operates in the over-modulated region, there is an increasing harmonic current in the fundamental currents. Thus, considering the total harmonic distortion (THD), suppressing the zero-sequence current is unnecessary in the overmodulation operation region. Thus, if the amplitude of reference fundamental currents exceeds the maximum limit, the flux-weakening equation is:

$$g\left(\omega_{e}, i_{d}^{ref}, i_{q}^{ref}\right) = \left(R_{s}i_{q}^{ref} + \omega_{e}L_{d}i_{d}^{ref} + \omega_{e}\psi_{f}\right)^{2} + \left(R_{s}i_{d}^{ref} - \omega_{e}L_{q}i_{q}^{ref}\right)^{2} - V_{dc}^{2} = 0 \quad (15)$$

The corresponding optimization process is expressed as:

$$i_d^{ref}(k+1) = i_d^{ref}(k) - \eta d \frac{g\left(\omega_e, i_d^{ref}, i_q^{ref}\right)}{d\left(i_d^{ref}\right)}$$
(16)

When this flux-weakening operation equation is utilized, the zero-sequence loop cannot be controlled, and the inner voltage sources cause ZSC. Then, the maximum modulation region will reach the size of the outer yellow hexagon in Figure 4a. Define the utilized fundamental voltage vectors as  $v_a$  and  $v_b$ ; the corresponding duty cycles are  $d_a$  and  $d_b$ , the sum of duty cycles is  $d_{all}$ , and the overmodulation process is expressed as:

$$\begin{cases} d_a = d_a/d_{all} \\ d_b = d_b/d_{all} \end{cases}, if d_{all} > 1 \tag{17}$$

# 4.4. Total FW-DFVL Control Strategy

In this subsection, we will provide a block diagram of the FW-DFVL.

Figure 8 provides the total block diagram of the proposed FW-DFVL. Firstly, the outer speed loop gives the *q*-axis current reference, and the PI controller controls *q*- the axis current.



Figure 8. Control diagram of FW-DFVL.

Secondly, the *d*-axis current reference is given by three different methods. The first method is the maximum torque per ampere (MTPA) [31], where the equation is expressed as:

$$i_{d|MTPA}^{ref} = \frac{\sqrt{1 + 4\xi^2 i_q^{ref^2} - 1}}{2\xi}, \xi = \frac{L_d - L_q}{\psi_f}$$
(18)

The second method is the proposed flux-weakening solution with DFVL, and the corresponding d-axis current reference has been expressed in Equation (14).

The third method is the proposed overmodulation method, and the corresponding *d*-axis current reference has been expressed in Equation (16).

We utilize the switching mode number Q to represent these three methods, and the expression of Q is shown in Equation (19).

$$Q = \begin{cases} 1, f\left(\omega_{e}, i_{d}^{ref}, i_{q}^{ref}\right) \leq 0\\ 2, \left[f\left(\omega_{e}, i_{d}^{ref}, i_{q}^{ref}\right) > 0\right] \land \left[\left(i_{d}^{ref}\right)^{2} + \left(i_{q}^{ref}\right)^{2} < I_{\max}^{2} \right]\\ 3, \left[f\left(\omega_{e}, i_{d}^{ref}, i_{q}^{ref}\right) > 0\right] \land \sqrt{\left(i_{d}^{ref}\right)^{2} + \left(i_{q}^{ref}\right)^{2}} \geq I_{\max} \end{cases}$$
(19)

If Q equals one, the controller operates in the MTPA mode, where the reference d-axis current is determined from Equation (18). Then, if the amplitude of the reference voltage vectors in the fundamental subspace is higher than DFVL and the amplitude of reference currents in the d- and q-axes is lower than Imax, Q equals two, which means the controller operates in the flux-weakening region with DFVL. Moreover, suppose the amplitude of the reference voltage vectors in the fundamental subspace is higher than DFVL, and the amplitude of reference currents in the d- and q-axes is higher than DFVL, and the amplitude of reference currents in the d- and q-axes is higher than DFVL, and the amplitude of reference currents in the d- and q-axes is higher than Imax. In that case, Q equals three, which means the controller operates in the flux-weakening region with over-modulation.

Thirdly, the modulation method generates the output reference of current controllers. We determine the sector based on the angle of the fundamental voltage, where the angle of reference voltage vector  $\theta^{ref}$  is expressed as:

$$\theta^{ref} = \operatorname{atan}\left(u_{\beta}^{ref}/u_{\alpha}^{ref}\right) \tag{20}$$

It needs to be mentioned that in the actual calculation process, atan2 function is utilized to ensure that the range of  $\theta^{ref}$  is proper.

Then, define the utilized non-zero fundamental voltage vectors as  $v_a$  and  $v_b$ , the corresponding duty cycles are  $d_a$  and  $d_b$ , there is:

$$\frac{V^{ref}}{\sin(120^\circ)} = \frac{d_a V_a}{\sin\gamma} = \frac{d_b V_b}{\sin(60^\circ - \gamma)}$$
(21)

where the definition of  $\gamma$  is shown in Figure 9,  $V_a$  and  $V_b$  are the amplitude of  $v_a$  and  $v_b$ , respectively, and  $V^{ref}$  is the amplitude of reference fundamental voltage, which is:

$$V^{ref} = \sqrt{\left(u_{\alpha}^{ref}\right)^2 + \left(u_{\beta}^{ref}\right)^2} \tag{22}$$



Figure 9. Definition of modulation parameters.

Moreover,  $V_a$  and  $V_b$  can be expressed as:

$$V_a = V_b = \left(2/\sqrt{3}\right) V_{dc} \tag{23}$$

The duty cycle of ZSV is:

$$d_{ZSV} = u_z^{ref} / (V_{dc}/3)$$
(24)

 $ZSV_1$  is utilized as the zero-sequence voltage vector if the required zero-sequence voltage is positive. Otherwise,  $ZSV_2$  is applied.

The duty cycle of zero voltage vector  $v_{00}$  and  $v_{15}$  is shown in Equation (25):

$$d_0 = 1 - d_a - d_b - d_{ZSV} (25)$$

The final output of the switching signal sequence can be expressed as:

$$SW = d_a v_a + d_b v_b + d_{ZSV} v_{ZSV} + d_0 v_{00}/2 + d_0 v_{15}/2$$
(26)

Moreover, it needs to be mentioned that if the switching mode number Q equals three, ZSC will not be suppressed, and  $u_z^{ref}$  is replaced by zero. The yellow area in Figure 9 is the feasible region covered by  $v_a$  and  $v_b$ .

# 4.5. Comparison with Conventional Flux-Weakening Strategy

This section compares the proposed FW-DFVL with the conventional flux-weakening control scheme in Figure 3.

The main difference between the two controllers is the fundamental voltage limit. In the conventional controller, the fundamental voltage limit is  $V_{dc}$ , which is unreachable due to the existence of ZSV. Moreover, the PI controller is utilized to generate the reference *d*-axis currents, which means the dynamic performance of the conventional controller is affected.

In contrast, the proposed FW-DFVL considers the existing ZSV, and the precise DFVL has been derived. Thus, the voltage limit of the proposed DFVL is different from the conventional controller. The voltage limit trajectories are shown in Figure 10a to further express this concept. The voltage limit trajectories without ZSV differ from those with 5% ZSV.



Figure 10. Operation region comparison.

For instance, in an 800 rpm situation, the flux-weakening operation will not be applied in the conventional controller since the voltage limit trajectory, the blue line, is out of range. However, in the proposed FW-DFVL, the flux-weakening operation will be applied since the green line is inside the range. In other words, the voltage trajectories of the conventional flux-weakening controller are not achievable.

Finally, assume there is a 5% required ZSV in FW-DFVL. Then, the maximum torque versus speed capability will be divided into two parts. The first part is the operation region with ZSC suppression, as marked yellow in Figure 10b, where the zero-sequence current is well suppressed. The second part is the operation region without ZSC suppression, as observed in Figure 10b. The bus voltage is completely applied for the fundamental currents, and the zero-sequence current is generated by the high order back EMF.

# 5. Simulation Results

The simulation results are provided in this section to assess the proposed FW-DFVL. The conventional flux-weakening controller is also applied. The parameters of the utilized series-winding PMSM are shown in Table 1. The inductance parameters listed in the table were measured while the system was at rest, and they are considered constant since the proposed SW-PMSM operates in a linear stage where the phase currents have little effect on the inductance value. FFT (Fast Fourier Transform) is used to obtain the values of the magnet flux linkage and third magnet flux linkage during the no-load back EMF test. It should be noted that in a conventional star-connected PMSM, the utilization of the bus voltage is only 13.86 V. However, in the proposed series-winding PMSM, the bus voltage utilization is increased to 24 V. This significant increase in bus voltage utilization can expand the operational range of the drive system even further.

Firstly, the dynamic performance of the conventional flux-weakening controller and proposed FW-DFVL is shown in Figure 11. The load is set as 1 Nm, and the reference speed is changed from 800 rpm to 1200 rpm and then 1550 rpm. The large speed error and current ripples in the *dq* subspace are two main disadvantages of the conventional flux-weakening controller.

Parameter	Description	Value
n <sub>p</sub>	Number of pole pairs	5
$R_s(\Omega)$	Stator resistance	1.4
$L_d$ (mH)	Inductance in <i>d</i> -axis	3.7
$L_q$ (mH)	Inductance in <i>q</i> -axis	5.0
$L_{z}^{\prime}$ (mH)	Inductance in the <i>z</i> -axis	8.4
$\psi_f$ (Wb)	Magnet flux linkage	0.04
$\psi_{f3}$ (mWb)	Third-order magnet flux linkage	12
J (kg.m <sup>2</sup> )	Rotational inertia	0.00005
$V_{DC}$ (V)	DC bus voltage	24
Speed (r/min)	Rated speed	800
Torque (N.m)	Rated torque	4.5
F (kHz)	Sampling frequency	20

Table 1. Parameter of series-winding PMSM.



Figure 11. Simulation dynamic performance of the conventional flux-weakening controller.

In contrast, the proposed FW-DFVL can obtain better dynamic performance, as shown in Figure 12. The series-winding PM machines can reach reference speed and rapid dynamic performance.

To further reveal the problems with the conventional algorithm, the steady-state system properties are shown in Figures 13–15. In Figure 13, the series-winding PM machines operate at 800 rpm, where the ZSC is well suppressed, and the fundamental currents are well generated.

In Figure 14, the series-winding PM machines should operate in the flux-weakening condition. While since the utilized voltage limit  $V_{dc}$  cannot be reached, the system cannot work correctly in the flux-weakening operation region. Both the fundamental currents and the phase currents are distorted. Similar performance is also observed in Figure 15.

On the contrary, in the proposed FW-DFVL, the voltage vectors are always feasible because of the DFVL, and the steady-state performance is shown in Figures 16–18. In Figure 16, the proposed FW-DFVL also operates in MTPA condition, where the performance is similar to the conventional controller. However, the performance in the flux-weakening operation region of the proposed FW-DFVL is much better. In Figure 17, the proposed FW-DFVL can realize precise fundamental current generation and ZSC suppression. Furthermore, in Figure 18, the proposed FW-DFVL operates in the overmodulation region where ZSC is not controlled. In this operation region, FW-DFVL will give up control of



ZSC in exchange for higher bus voltage utilization. Thus, the precise fundamental current generation and larger ZSC are observed.

Figure 12. Simulation dynamic performance of proposed FW-DFVL.



Figure 13. Simulation steady-performance of the conventional controller under 800 rpm reference.







Figure 15. Simulation steady-performance of the conventional controller under 1550 rpm reference.



Figure 16. Simulation steady-performance of proposed FW-DFVL under 800 rpm reference.



Figure 17. Simulation steady-performance of proposed FW-DFVL under 1200 rpm reference.



Figure 18. Simulation steady-performance of proposed FW-DFVL under 1550 rpm reference.

In summary, the conventional flux-weakening controller cannot work properly in the series-winding PMSM. The reason is that the ideal fundamental voltage limit is infeasible. Thus, the proposed FW-DFVL, which utilizes the DFVL, and gradient descent method, can obtain better steady-state performance and dynamic performance.

#### 6. Experimental Results

In this section, the performance of the proposed FW-DFVL will be further discussed with experimental results. The platform is shown in Figure 19, where the parameter of the series-winding PMSM has been demonstrated in Table 1, and the deadtime of the inverter is two us. The DC power supplies the bus voltage, and DS1202 performs the control actions. The 200 W magnetic powder brake is taken as the load.

#### 6.1. Dynamic Performance

In this subsection, the dynamic experiment is designed to verify the dynamic performance of this strategy. Moreover, the conventional flux-weakening control scheme is utilized to express the advantages of the proposed controller.

During the experimental section, the parameters of the speed PI controller are fixed as Kp = 0.03 and Ki = 0.2, and the control frequency is fixed as 20 kHz. In Figure 20, the dynamic performance is tested under a no-load condition, where the reference speed is set from 600 rpm to 1400 rpm. ZSC is all suppressed in Figure 20, and the maximum speed is about 1400 rpm. Moreover, it needs to be mentioned that under 1400 rpm, DFVL is about 36 V because of the increasing inner voltage sources in the zero-sequence loop. Thus, the maximum speed obtained with ZSC suppression is lower than the simulation results.



Figure 19. Experimental platform.



Figure 20. Dynamic performance of FW-DFVL under no load.

Moreover, another acceleration experiment is provided in Figure 21, where the load is set as 0.6 Nm, and the reference speed is set from 600 rpm to 1200 rpm. The performance trend is like the no-load condition situation. Moreover, the maximum speed is lower than the no-load condition because the torque generation also consumes bus voltage, and PMSM cannot reach 1400 rpm.

Then, the maximum speed of the proposed FW-DFVL will further increase without ZSC suppression. Figure 22 provides the acceleration performance of FW-DFVL from 1200 rpm to 1600 rpm. It can be observed that there is a sudden increase in ZSC from the 1400 rpm situation. The mechanism is that ZSC current is not further suppressed, and the higher bus voltage utilization is obtained. Finally, we will assess the performance of the conventional flux-weakening controller in Figure 23, where the load is 0.6 Nm, and the reference speed is set from 600 rpm to 1200 rpm. The performance is similar to the simulation results, where the speed cannot reach the reference speed because of the



inaccurate fundamental voltage limit. Thus, the conventional flux-weakening controller with the improper fixed fundamental voltage limit is unsuitable for series-winding PMSM.

Figure 21. Dynamic performance of FW-DFVL under 0.6 Nm.



Figure 22. Overmodulation dynamic performance of FW-DFVL under 0.6 Nm.

Moreover, the computation burden of two controllers is compared in the utilized space. The execution time of the conventional flux-weakening controller is 28.2  $\mu$ s, and the execution time of FW-DFVL is 32.4  $\mu$ s. The increase in the execution time is mainly caused by the gradient descent method.



Figure 23. Dynamic performance of conventional controller under 0.6 Nm.

# 6.2. Steady-State Performance

Four operation conditions are tested in this section, and the phase currents, ZSC, and the Fast Fourier Transform (FFT) results are provided. Figure 24 provides the steady-state performance of the proposed FW-DFVL under no-load conditions. Figure 24 includes two different operation points. In Figure 24a, the operation point is located in the MTPA operation region. Then, the proposed FW-DFVL does not enter the flux-weakening operation region, and ZSC can be well suppressed. In Figure 24b, FW-DFVL operates in the flux-weakening region where the phase currents are mainly applied to offset the back electromotive force components, allowing the series-winding PMSM to operate over the rated speed. Moreover, the total harmonic distortion (THD) is still more significant at 600 rpm than at 1400 rpm. The phase current amplitude is smaller at 600 rpm; therefore, ZSC will cause more THD.



**Figure 24.** Steady-state performance of FW-DFVL under no load (**a**) 600 rpm situation (**b**) 1400 rpm situation.

Figure 25 also includes the operation point in the MTPA region and the flux-weakening region. In Figure 25a, the system operates in the MTPA region, and in Figure 25b, the system operates in the flux-weakening region. The performance trend is similar to the no-load condition, where ZSC is well suppressed in both the normal and the flux-weakening operation conditions.



**Figure 25.** Steady-state performance of FW-DFVL under 0.6 Nm (**a**) 600 rpm situation (**b**) 1200 rpm situation.

Figure 26 includes the overmodulation operation points. In Figure 26, ZSC is not suppressed, and THD will increase. However, considering the increasing speed, the rising THD is acceptable.



**Figure 26.** Overmodulation steady-state performance of FW-DFVL under 0.6 Nm (**a**) 1400 rpm situation (**b**) 1600 rpm situation.

## 7. Conclusions

In this paper, a novel FW-DFVL scheme is designed for the traction system with a series-winding connection, which enables the series-winding motor with limited bus voltage to run above the rated speed.

The dynamic fundamental voltage limit is crucial in the flux-weakening controller design for series-winding PMSM or multiphase PMSM, which have multiple current

subspaces. The fixed fundamental voltage limit is far from the actual fundamental voltage limit and cannot ensure the feasible voltage vectors in the fundamental subspace. This problem is solved in this paper with DFVL, and the contribution of this paper can be divided into three parts:

(1) The concept of dynamic fundamental voltage limit is proposed in this paper. This DFVL can ensure the feasible voltage vectors in both the fundamental subspace and the zero-sequence loop.

(2) The gradient descent method is applied to solve the optimal flux-weakening current online. This feedforward solution will improve dynamic performance.

(3) The overmodulation solution for series-winding PMSM is provided. The essence of this overmodulation solution is to actively release the control of ZSC and obtain higher bus voltage utilization.

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