

## Article

# Stability Enhancement Method of Standalone Modular Multilevel Converters Based on Impedance Reshaping

Youzhuo Zheng<sup>1</sup>, Long Hua<sup>1</sup>, Yekui Yang<sup>1</sup>, Chun Li<sup>1</sup>, Chaoyi Luo<sup>1</sup>, Zihong Song<sup>1</sup>, Xingwu Yang<sup>2</sup> and Haibo Feng<sup>2,\*</sup>

<sup>1</sup> Electric Power Research Institute, Guizhou Power Grid Co., Ltd., Guiyang 550002, China

<sup>2</sup> College of Electrical Engineering Shanghai University of Electric Power, Shanghai 200090, China

\* Correspondence: haibofeng@mail.shiep.edu.cn

**Abstract:** Modular multilevel converters (MMCs) are susceptible to subsynchronous oscillations (SSOs) caused by impedance interactions in the power line. Current research into the stability of MMCs focuses mainly on voltage feed-forward control, while the effect of current feed-forward control is neglected. This paper proposes a current feed-forward compensation method based on impedance reshaping for standalone MMCs. Initially, an impedance model was developed to identify the stability risks caused by the interaction between the MMC and power line impedance. The proposed method feeds the current compensation signal into the modulation circuit, thereby improving the control signal and suppressing the impedance interaction between the MMC and the power line. The analysis of the harmonic state space (HSS) method verifies that the proposed approach effectively reduces the negative damping region in the frequency band where the SSO is located. Additionally, the impedance frequency scanning method confirms the accuracy of impedance modeling. Using the MATLAB/Simulink platform and StarSim HIL hardware-in-the-loop experimental platform, the SSO phenomenon of the MMC is simulated, and the results show that the proposed method can effectively suppress harmonic currents during SSO, which verifies the accuracy of the stability analysis and the feasibility of the proposed method.



**Citation:** Zheng, Y.; Hua, L.; Yang, Y.; Li, C.; Luo, C.; Song, Z.; Yang, X.; Feng, H. Stability Enhancement Method of Standalone Modular Multilevel Converters Based on Impedance Reshaping. *Energies* **2024**, *17*, 895. <https://doi.org/10.3390/en17040895>

Academic Editors: Giovanni Migliazza, Emilio Lorenzani and Giampaolo Buticchi

Received: 11 January 2024

Revised: 1 February 2024

Accepted: 9 February 2024

Published: 14 February 2024



**Copyright:** © 2024 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (<https://creativecommons.org/licenses/by/4.0/>).

**Keywords:** modular multilevel converter; subsynchronous oscillation; harmonic state space; stability; feed-forward control

## 1. Introduction

Modular multilevel converters are extensively utilized in high-voltage direct current (HVDC) transmission systems owing to their modularity [1,2], extensibility [3,4], and favorable output performance [5–7]. Two operational modes are identified for MMC-HVDC projects: grid-connected mode and standalone mode [8,9]. In the grid-connected mode, active and reactive power control outer loops primarily govern MMC operations. Conversely, the standalone mode operations of MMC-HVDC projects are directed by controlling the AC voltage and frequency.

However, when interfacing with the impedance of the power line, MMCs tend to exhibit oscillatory behavior [10–12]. Recent commissions of MMC-HVDC projects have highlighted relevant stability issues. For instance, a 1500 Hz resonance was detected during AC side charging in the Zhangbei MMC-HVDC project in China [13], the Borwin2 project in Germany encountered a 451 Hz resonance during the supply of offshore wind power via MMC-HVDC [14], and a 42.5 Hz subsynchronous oscillation transpired in the Zhoushan multiterminal project in China when the operating mode transitioned from grid-connected to standalone during line maintenance shutdown [15]. The increase in the capacity and number of MMC-HVDC projects considerably increases the impact of these engineering projects on power quality. In milder scenarios, these mishaps may degrade grid power quality, while in severe instances, they may necessitate project shutdowns [16,17].

Addressing the power oscillation predicament in MMC-HVDC is critical for ensuring smooth operation and facilitating future expansions of MMC technology. An initial step toward alleviating power oscillation risks involves establishing a stability impedance model for the MMC system. Impedance modeling methodologies for MMC systems span two domains: time and frequency domain modeling. Time domain modeling primarily employs a dynamic phaser based on Park transform [18], which is applied across various frequencies to obtain the corresponding frequency components. However, the complexity of expressing Park transform increases considerably with the inclusion of high-order harmonics owing to its reliance on trigonometric functions. Frequency domain modeling predominantly encompasses multiharmonic modeling. J. Lyu proposed a novel MMC system method employing the HSS [19]. HSS methodology, which leverages Toeplitz matrices, simplifies the extension to all harmonic orders and elucidates harmonic interrelationships. Furthermore, the matrix-centric modeling approach of HSS provides a clearer expression compared with time domain modeling, facilitates computation, and renders a clear physical interpretation. Consequently, this study adopts the HSS method for impedance modeling.

Following impedance modeling, stability enhancement methods can be employed in system areas identified with oscillation risk to mitigate it. Three primary methods exist to enhance the stability of the MMC system: control parameter optimization, additional passive damping, and additional active damping [19–26]. Control parameters are optimized using an impedance stability criterion that evaluates the magnitude and phase of the MMC system and the corresponding single-input single-output system on the Bode plot to determine appropriate control parameters for oscillation mitigation. In [19,20], proportional-integral control parameters are designed based on whether the impedance of the MMC system and the wind farm adhere to the stability criterion. However, if operational environment alterations occur, the stabilization region deduced from this method may become ineffective. Additional passive damping encompasses the inclusion of extra passive devices and harmonic injection. The deployment of additional passive devices results in considerable power losses, rendering this method infrequently utilized in engineering practices [21,22]. In [23], transient harmonic injection disrupts the original in-system characteristic coupling, consequently modifying the MMC impedance damping response in the low-frequency range and destabilizing the MMC system. Additional active damping involves the introduction of a corresponding feedback relationship in the original control circuit. By adjusting the feedback, the amplitude and phase of the impedance react correspondingly, thereby altering the impedance characteristics [24–27]. In [24], a feed-forward voltage branch incorporating active damping based on the round function is discussed, which curtails overcurrent during fault scenarios, enhancing MMC operation stability. In [25], a comparison of three different orders of active feed-forward voltage damping is presented, along with the design of active damping controllers to suppress high-frequency oscillations as a function of gain and cutoff frequency. In [26], a phase compensation method was introduced in active damping control to suppress oscillations. In [27], active damping control is improved by combining a high-pass filter with a low-pass filter.

However, existing studies [24–27] have focused on active damping controllers with voltage feed-forward. Given the diverse effects of different feed-forward control loops on stability, it is crucial to explore the impact of loops beyond voltage on stability. Hence, this study investigates the effect of current feed-forward on stability enhancement. The stability of standalone operating MMC systems is investigated. The results reveal that in a closed-loop voltage scenario, the MMC system is susceptible to SSO issues. A large low-frequency harmonic current at SSO is also observed. To suppress this harmonic current, an impedance reshaping method using feed-forward current compensation is proposed, and corresponding design guidelines are given. Related experiments show that the proposed method mitigates SSOs.

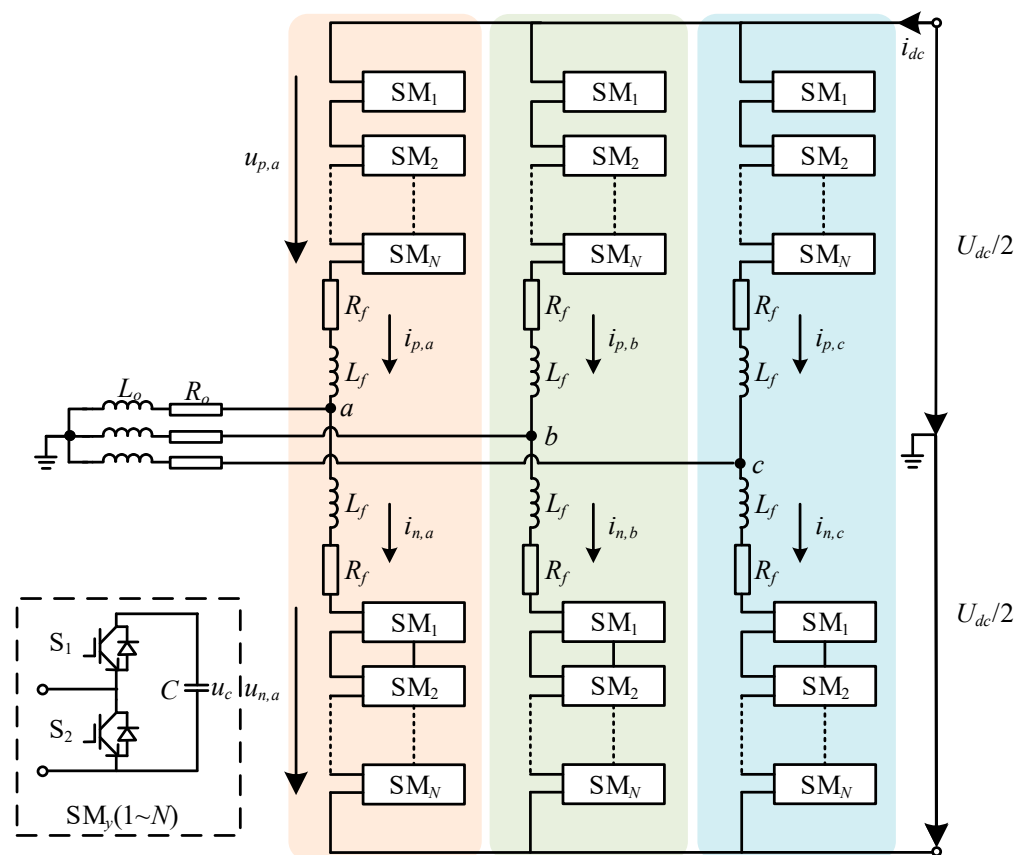
The remainder of this paper is organized as follows: Section 2 reviews the modeling of MMC systems using the HSS approach. Section 3 introduces an impedance reshaping

method to mitigate SSOs, accompanied by design guidelines. Section 4 provides a case study of MMC, substantiated by simulation verification. Section 5 presents the validation of the proposed strategy's effectiveness using a hardware-in-the-loop platform. Section 6 presents the conclusions of this study.

## 2. An HSS-Based Approach to MMC Impedance Modeling

### 2.1. Average MMC Topology

As can be seen from Figure 1, the MMC topology consists of upper and lower arms, each of which is connected in series with  $N$  half-bridge submodules  $SM_y (y=1\sim N)$ , an arm resistor  $R_f$ , and an arm inductor  $L_f$ .  $U_{p,j} (j = a, b, c)$  and  $U_{n,j} (j = a, b, c)$  represent the voltages of the series-connected half-bridge submodules of the upper and lower arms.  $i_{p,j} (j = a, b, c)$  and  $i_{n,j} (j = a, b, c)$  represent the currents flowing through the upper and lower arms.  $U_{dc}$  and  $i_{dc}$  represent the voltages and currents flowing through the DC side of the MMC.



**Figure 1.** Standalone MMC exact model topology.

Resistor  $R_o$  and inductor  $L_o$  are used to simulate the load on the AC side of the MMC.

The number of three-phase upper and lower arm submodules for MMCs in HVDC transmission projects is often very high, making the detailed model difficult to construct. Therefore, it is essential to address the difficulty in modeling by using the average value model [28] presented in Figure 2.

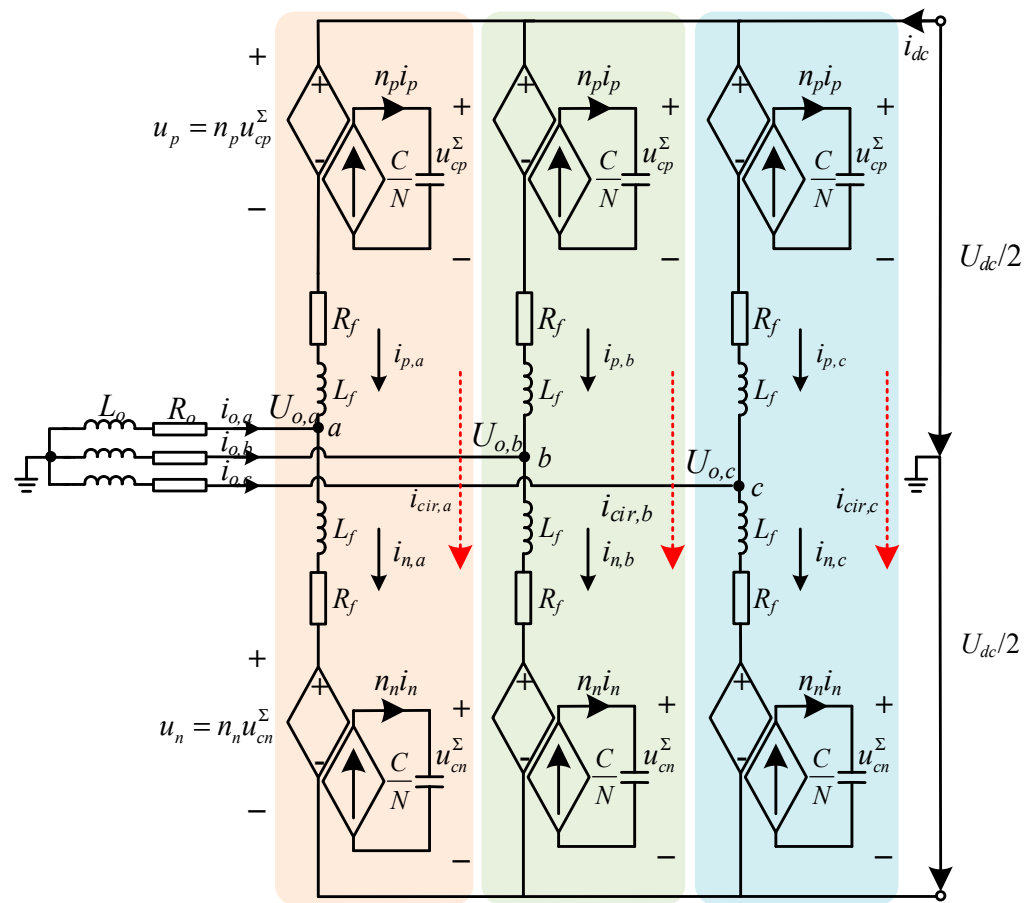


Figure 2. Standalone MMC average model topology.

For analysis purposes, define  $i_{o,j}$  ( $j = a, b, c$ ) as the AC side output current with the expression  $i_{o,j} = i_{n,j} - i_{p,j}$  ( $j = a, b, c$ ); define  $i_{cir,j}$  ( $j = a, b, c$ ) as the circulating current with the expression  $i_{cir,j} = (i_{n,j} - i_{p,j})/2$  ( $j = a, b, c$ ). Define  $C_{arm}$  ( $C_{arm} = C/N$ ) as the equivalent submodule capacitance of  $N$  half-bridge submodules  $SM_y$  ( $1 \sim N$ ) after the MMC uses the average model.

Based on the above definitions, the Kirchhoff voltage and current equations for the circuit shown in Figure 2 are established to obtain the MMC time domain model equations:

$$\frac{U_{dc}}{2} - U_o = n_p u_{cp}^{\Sigma} + R_f i_p + L_f \frac{di_p}{dt} \quad (1)$$

$$\frac{U_{dc}}{2} + U_o = n_n u_{cn}^{\Sigma} + R_f i_n + L_f \frac{di_n}{dt} \quad (2)$$

where  $u^{\Sigma} cx$  ( $x = p, n$ ) and  $n_x$  ( $x = p, n$ ) represent the cumulative value of the submodule capacitance voltages of the upper and lower arms of the MMC and the superimposed value of the submodule control signals.

The internal dynamic characteristics of the MMC after using the average value model can be expressed as follows:

$$\frac{du_{cp}^{\Sigma}}{dt} = \frac{n_p}{C_{arm}} i_p \quad (3)$$

$$\frac{du_{cn}^{\Sigma}}{dt} = \frac{n_n}{C_{arm}} i_n \quad (4)$$

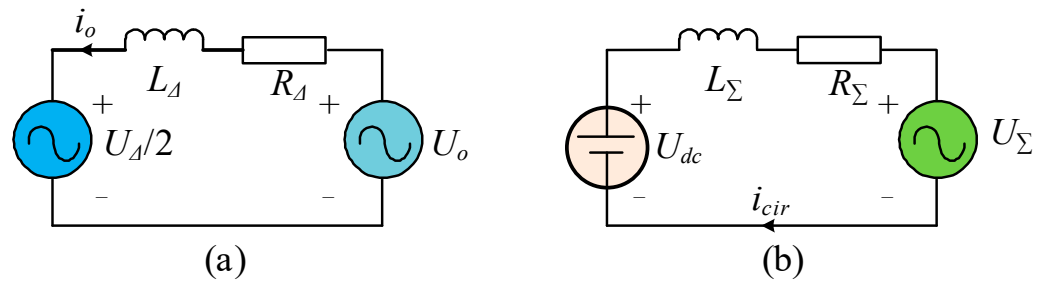
The derivation of Equations (1) and (2) leads to the MMC equivalent differential mode circuit (DM) and common mode circuit (CM):

$$\frac{U_{\Delta}}{2} - U_o = L_{\Delta} \frac{di_o}{dt} + R_{\Delta} i_o \quad (5)$$

$$U_{dc} - U_{\Sigma} = L_{\Sigma} \frac{di_{cir}}{dt} + R_{\Sigma} i_{cir} \quad (6)$$

where  $U_{\Delta} = (U_n - U_p)/2$ ,  $U_{\Sigma} = (U_n + U_p)$ ,  $L_{\Delta} = L_f/2$ ,  $R_{\Delta} = R_f/2$ ,  $L_{\Sigma} = 2L_f$ ,  $R_{\Sigma} = 2R_f$ .

Based on Equations (5) and (6), Figure 3 illustrates that the MMC topology's control circuit is related to the DM and CM voltages. Therefore, the control of the MMC is equivalent to the control of DM voltage and CM voltage. The DM voltage is reflected in the output voltage  $U_o$ , and the CM voltage is reflected in the circulating current  $i_{cir}$ . Therefore, the MMC only needs to control  $U_o$  and  $i_{cir}$  to realize the stable operation of the circuit. For controlling  $U_o$  and  $i_{cir}$ , a proportional resonance controller (PR) is employed.



**Figure 3.** Equivalent circuit diagram of the MMC system: (a) differential mode circuit diagram; (b) common mode circuit diagram.

This paper focuses on the SSOs caused by the outer loop. Therefore, the current inner loop is not considered [29]. The transfer function of the proportional resonant controller that regulates  $U_o$  and  $i_{cir}$  is shown as:

$$G_{dm}(s) = K_{Pdm} + \frac{2K_{Rdm}\omega_v s}{s^2 + 2\omega_v s + \omega_0^2} \quad (7)$$

$$G_{cm}(s) = K_{Pcm} + \frac{2K_{Rcm}\omega_c s}{s^2 + 2\omega_c s + (2\omega_0)^2} \quad (8)$$

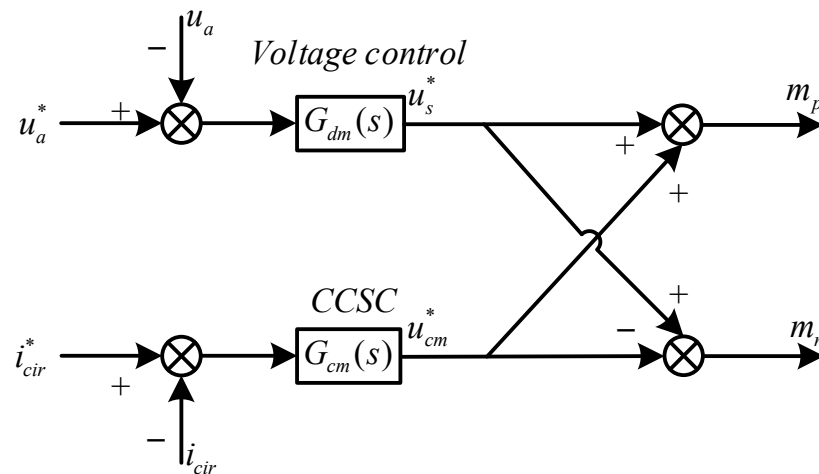
where  $K_{Pdm}$  and  $K_{Pcm}$  are proportional resonance controller proportional gains,  $K_{Rdm}$  and  $K_{Rcm}$  are proportional resonance controller proportional partial resonance gains,  $\omega_v$  and  $\omega_c$  are proportional resonance controller proportional partial resonance bandwidths, and  $\omega_0$  is the fundamental frequency angular frequency.

## 2.2. HSS Impedance Modeling of MMC

Equations (3)–(6) provide the basic MMC circuit equations. In contrast to the TL-VSC, the MMC topology exhibits intricate internal dynamics. This is evident in the submodule capacitance of the MMC topology, in which the submodule capacitance voltages interact with the harmonics in the circuit, resulting in a more intricate analysis. The HSS methodology was selected for the stability modeling to comprehensively consider the internal dynamics of the MMC system. Refer to Appendix A for the modeling procedure.

Figure 4 shows the block diagram of the modulation signal of the MMC control circuit controlled by the voltage control loop and the circuit current suppression control (CCSC) loop, from which the relationship between the modulation signals of the upper and lower arms and the control loop can be deduced:

$$\begin{aligned} \tilde{m}_p(s + jh\omega_0) &= G_{dm}(s + jh\omega_0)\tilde{u}_a + G_{cm}(s + jh\omega_0)\tilde{i}_{cir} \\ \tilde{m}_n(s + jh\omega_0) &= -G_{dm}(s + jh\omega_0)\tilde{u}_a + G_{cm}(s + jh\omega_0)\tilde{i}_{cir} \end{aligned} \quad (9)$$



**Figure 4.** Block diagram of the modulation signal of the MMC.

It should be noted that both  $u_a^*$  and  $i_a^*$  are fixed reference values that are unaffected by small signal disturbances, hence implying that both  $u_a^*$  and  $i_a^*$  are zero.

By substituting Equation (9) into Equation (A15), the AC side impedance  $Z_0(s)$  can be obtained. For clarity in what follows, it is defined that the open-loop impedance  $Z_{0\_ic}(s)$  is the AC side impedance solely present in CCSC, while the closed-loop impedance  $Z_{0\_close}(s)$  is the AC side impedance present both in CCSC and voltage control.

The impedance of the MMC can be calculated using Equation (A23) and the circuit parameters detailed in Appendix B.

Figure 5 shows that the MMC impedance phase diagram under open-loop control remains in the range of  $\pm 90^\circ$  with no negative damping characteristics, and the system is stable. In contrast, the MMC impedance phase diagram extends beyond  $\pm 90^\circ$  under closed-loop control, indicating negative damping characteristics and risk of oscillation. At 14 Hz, the MMC intersects the power line impedance with a phase difference of over  $180^\circ$ , pointing towards a possibility of SSOs in the system. For the sake of clarity, this point will henceforth be referred to as the risk point.

The analysis of the figure above illustrates that the introduction of closed-loop control leads to negative damping characteristics, which in turn makes the MMC system unstable. As noted in the introduction, controller parameters affect stability. Therefore, it is necessary to investigate whether the negative damping characteristics, which cause instability in the MMC system, are due to the inappropriate selection of controller parameters.

Figure 6 compares the impedance plots of the MMC system voltage loop parameters  $K_{Pdm}$  and  $K_{Rdm}$  for different control parameters. As illustrated in Figure 6a, changing the control parameter  $K_{Pdm}$  has little effect on improving the negative damping characteristics of the MMC system. Figure 6b shows that increasing the parameter  $K_{Rdm}$  does not change the negative damping characteristics of the MMC system and even creates new risk points in the low-frequency range. In addition, if the  $K_{Rdm}$  parameter is excessively small, the impedance map under voltage control evolves into the open-loop impedance map. It can be concluded that if the  $K_{Rdm}$  parameter is too small, the control becomes ineffective, which falls outside the scope of this paper.

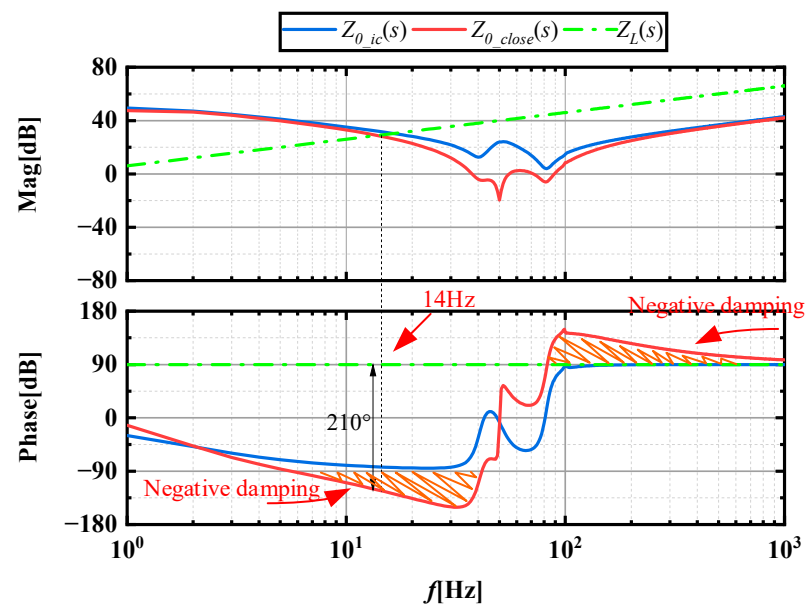
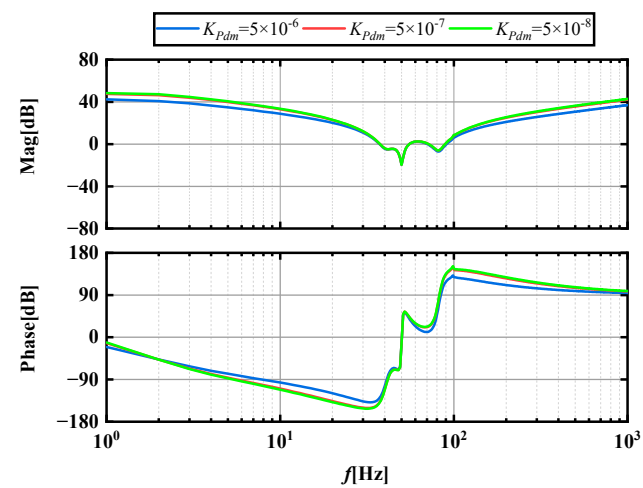
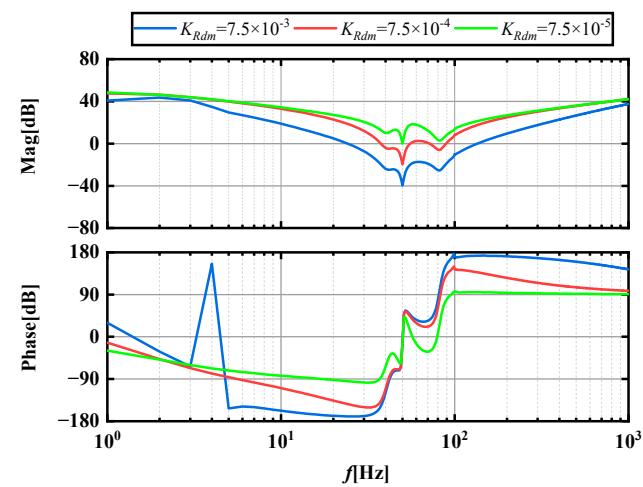


Figure 5. Impedance Bode diagram for open-loop and closed-loop control.



(a) Bode diagram for different  $K_{Pdm}$  parameters



(b) Bode diagram for different  $K_{Rdm}$  parameters

Figure 6. Closed-loop impedance Bode diagram for different voltage loop parameters.



The analysis above shows that the negative damping characteristics of the MMC system are caused by the use of closed-loop control. Furthermore, systems using only PR control pose a risk of SSOs.

### 3. Proposed Method for Improving the Stability of MMCS Based on Impedance Reshaping

To confirm the risk of SSOs in the MMC system caused by negative damping characteristics, the current output  $i_o$  spectrum was simulated, and is presented in Figure 7, by the MATLAB/Simulink platform. The harmonic analysis reveals that when circuit control is switched from open-loop to closed-loop control, the system displays harmonics at both 17 Hz and 83 Hz frequencies due to mirror coupling [30]. The oscillation frequency coincides closely with the risk point frequency shown in Figure 5, confirming the accuracy of the impedance modeling.

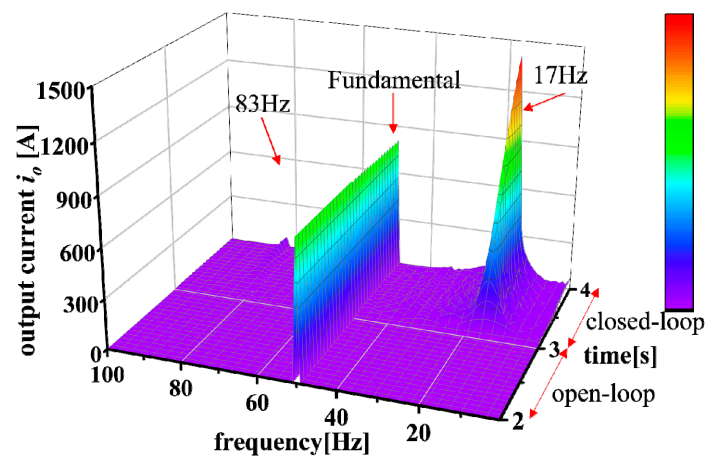


Figure 7. Output current change after SSO event.

To mitigate the low-frequency harmonic and prevent SSOs, this paper suggests a feed-forward current compensation method to enhance the stability of the MMC system. Figure 8 shows the control block diagram of the proposed feed-forward current compensation. In Figure 8, the left side is the control block diagram of the MMC system, and the right side is the simplified plant diagram of the MMC system. This paper aims to improve the stability of the MMC system by implementing a feed-forward control compensation path between the output current  $i_o$  and the control circuit. This path suppresses harmonic currents in the output current, reshapes the impedance, and achieves harmonic suppression in the control loop.

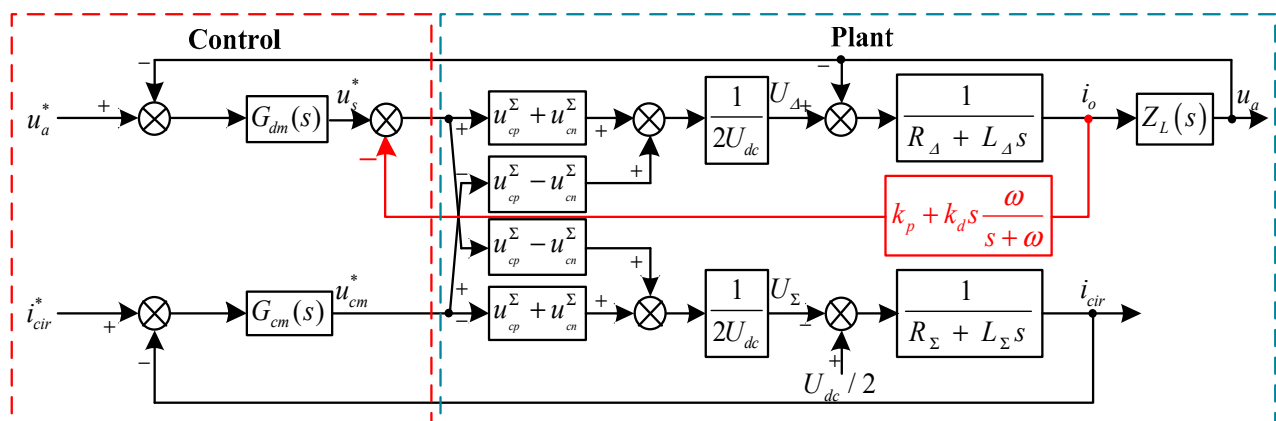


Figure 8. Block diagram of the control of an isolated MMC system.



The magnitude of the output current is obtained by subtracting the upper and lower arms  $U_{\Delta}$  and multiplying by the differential mode resistor  $R_{\Delta}$  and differential mode inductor  $L_{\Delta}$ , as illustrated in Figure 8, to obtain the output voltage  $u_a$ . To output the harmonic current back to the voltage modulation signal in the modulation circuit, the current must first be converted into a voltage form. This process can be achieved through a PD controller (transfer function  $G_{PD} = k_p + k_d s$ ).

In engineering applications, the use of PD controllers may result in high-frequency noise amplification. To address this issue, a first-order filter is added to adjust the poles, which works at attenuating the high-frequency noise of the differential link [31]. The value for  $\omega$  is often empirically set at 100.

The transfer function of the feed-forward control loop can be obtained and expressed based on the analysis provided above.

Figure 9 displays the Bode plot of  $G_{PD}$ ,  $G_{feed}$ , and  $G_{filter}$ . A comparison between  $G_{PD}$  and  $G_{feed}$  reveals that the addition of a low-pass filter effectively limits the high-frequency gain of  $G_{PD}$  to the range of  $G_{feed}$ , preventing the amplification of high-frequency signals by the transfer function. A comparison between  $G_{feed}$  and  $G_{filter}$  shows that the phase of  $G_{feed}$  always precedes that of  $G_{filter}$ , which is crucial to the effectiveness of the proposed feed-forward current method.  $G_{filter}$  exhibits a decaying frequency response as frequency increases, while  $G_{feed}$  maintains a consistent gain across frequencies.

$$G_{feed}(s) = k_p + k_d s \frac{N}{s + N} \quad (10)$$

$$G_{filter}(s) = k_p + k_d \frac{N}{s + N} \quad (11)$$

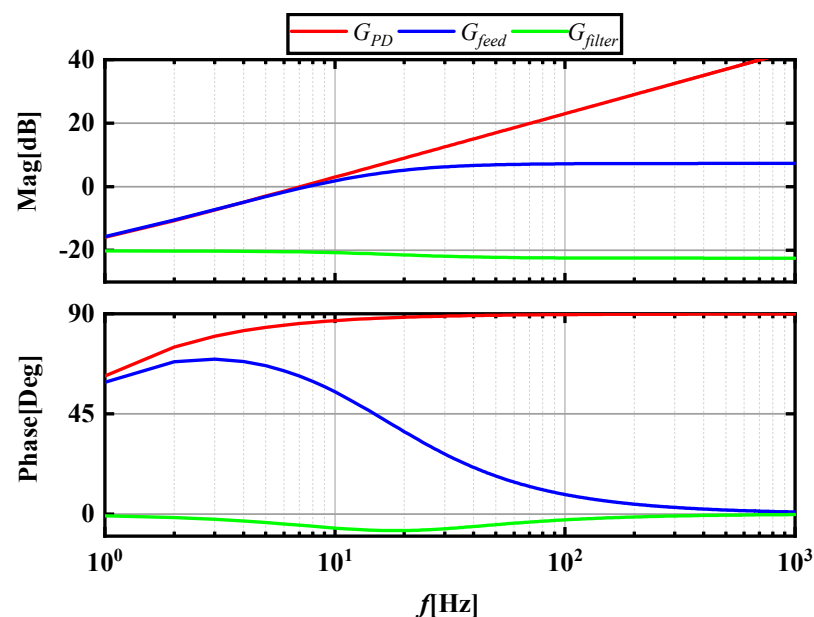


Figure 9. Bode diagram of conventional controller  $G_{PD}$  vs.  $G_{feed}$  vs.  $G_{filter}$ .

The modulation function of the proposed method using feed-forward current compensation can be obtained, as shown in Figures 4 and 8.

$$\begin{aligned} \tilde{m}_p(s + jh\omega_0) &= G_{vd}(s + jh\omega_0)\tilde{u}_a + G_{ic}(s + jh\omega_0)\tilde{i}_{cir} + G_{feed}(s + jh\omega_0)\tilde{i}_o \\ \tilde{m}_n(s + jh\omega_0) &= -G_{vd}(s + jh\omega_0)\tilde{u}_a + G_{ic}(s + jh\omega_0)\tilde{i}_{cir} - G_{feed}(s + jh\omega_0)\tilde{i}_o \end{aligned} \quad (12)$$

By substituting the modulation function (12) into Equation (A15), the impedance after compensation using the feed-forward current is obtained. This is defined as  $Z_{o\_proposed}$

(s). To achieve harmonic suppression in the control loop, the design criterion for the feed-forward compensation path is used. The values of the parameters  $k_p$  and  $k_d$  are then designed concerning the differential mode resistance  $R_\Delta$  and differential mode inductance  $L_\Delta$ , which are associated with the output current  $i_o$ .

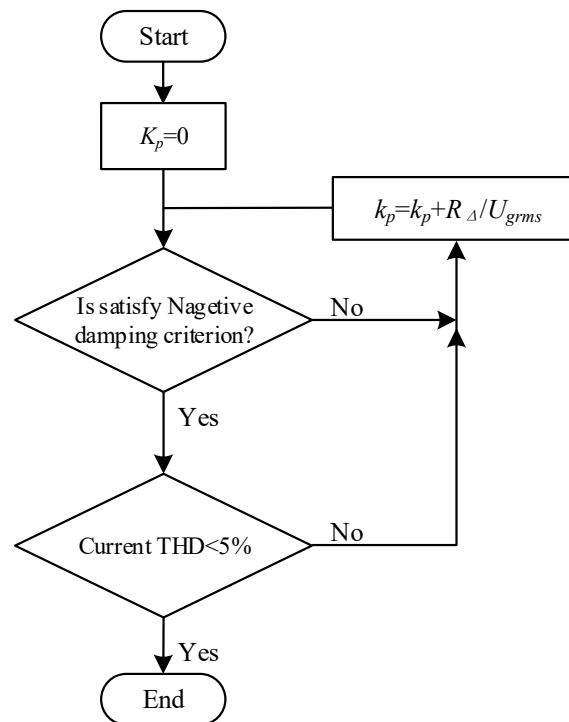
The parameters  $k_p$  and  $k_d$  are related in the following way:

$$\frac{k_p}{k_d} = \frac{R_\Delta}{L_\Delta} \quad (13)$$

The criterion for negative damping stability is also defined in Figure 5, which aids in designing the parameters of this system:

$$\text{Re}[Z_0(s)] > 0 \quad (14)$$

To satisfy the negative damping stability criterion, the value of  $k_p$  is obtained by iteratively calculating  $k_p$ , using the design criterion shown in Equations (13) and (14). The calculated  $k_p$  value is then substituted into the simulation, with a reference to IEEE standard THD of not more than 5%, which is the main reference for the current power quality [32]. Figure 10 presents the specific iterative flowchart.



**Figure 10.** Impedance modeling evaluation flowchart for forward current compensation methods.

The MMC system's improved impedance can be acquired by replacing the obtained  $k_p$  and  $k_d$  parameters in Equations (A20) and (A23).

Figure 11 shows the MMC system impedance before and after using the feed-forward current compensation method proposed in this paper with the improved voltage loop PIR controller proposed in the literature [33]. From Figure 11, it can be seen that although both the method proposed in this paper and the PIR controller can suppress oscillations, the PIR controller still has a negative damping region in the mid-frequency region, while the method proposed in this paper can eliminate the negative damping region.

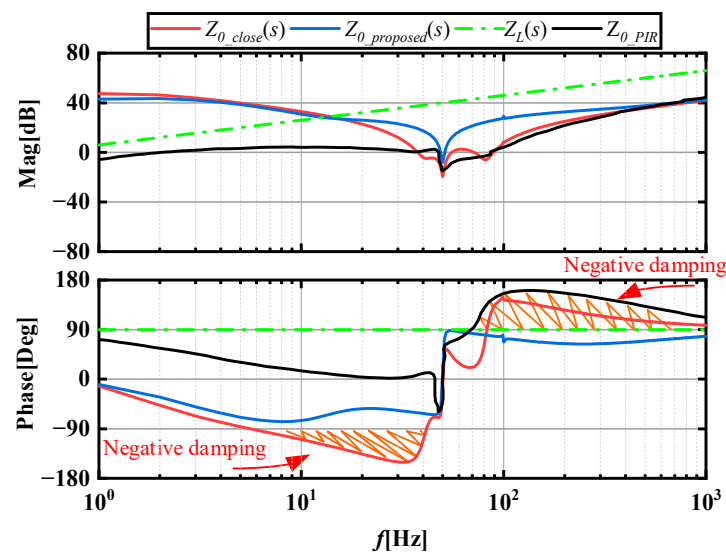


Figure 11. Bode diagram of the impedance the proposed method and [33].

#### 4. Case Study and Simulation Verifications

##### 4.1. MMC Impedance Verification

To verify the accuracy of the theoretical analysis and the performance of the proposed impedance reshaping technique for the feed-forward current compensation of the MMC system, an MMC system was constructed on the MATLAB/Simulink platform for time domain simulation. The MMC system uses an exact model, and the parameter table is listed in Appendix B. To compare the effectiveness of the proposed feed-forward current compensation control, the consistent voltage loop parameters from the literature [33] are selected for verification of the proposed method. Table 1 lists the voltage loop circuit parameters.

Table 1. MMC output voltage regulator parameters.

| Case Study | $K_{Pdm}$          | $K_{Rdm}$            |
|------------|--------------------|----------------------|
| Case I     | $5 \times 10^{-7}$ | $7.5 \times 10^{-4}$ |
| Case II    | $5 \times 10^{-7}$ | $1.5 \times 10^{-4}$ |

Figure 12 shows the impedance Bode plots for voltage control and current feed-forward control. The symbol 'x' denotes the measured impedance at various frequencies, used to verify the theoretical impedance model.

Figure 12 shows that the scanning impedance results agree with the theoretical calculations. Specifically, Figure 12a shows that  $Z_{o\_close}(s)$  intersects the grid impedance magnitude with a phase difference greater than  $180^\circ$ , and that the system is unstable.

##### 4.2. Feed-Forward Current Compensation Control under the Control of Different Voltage Controller Parameters

However,  $Z_{o\_proposed}(s)$  possesses the negative damping feature, which is removed through feed-forward current compensation control to attain stability. Figure 12b shows that the MMC intersects with the magnitude of the grid impedance, and that the phase margin is small. Simultaneously, the MMC system exhibits negative damping properties and may become unstable. The ensuing section verifies the aforementioned instability via simulation.

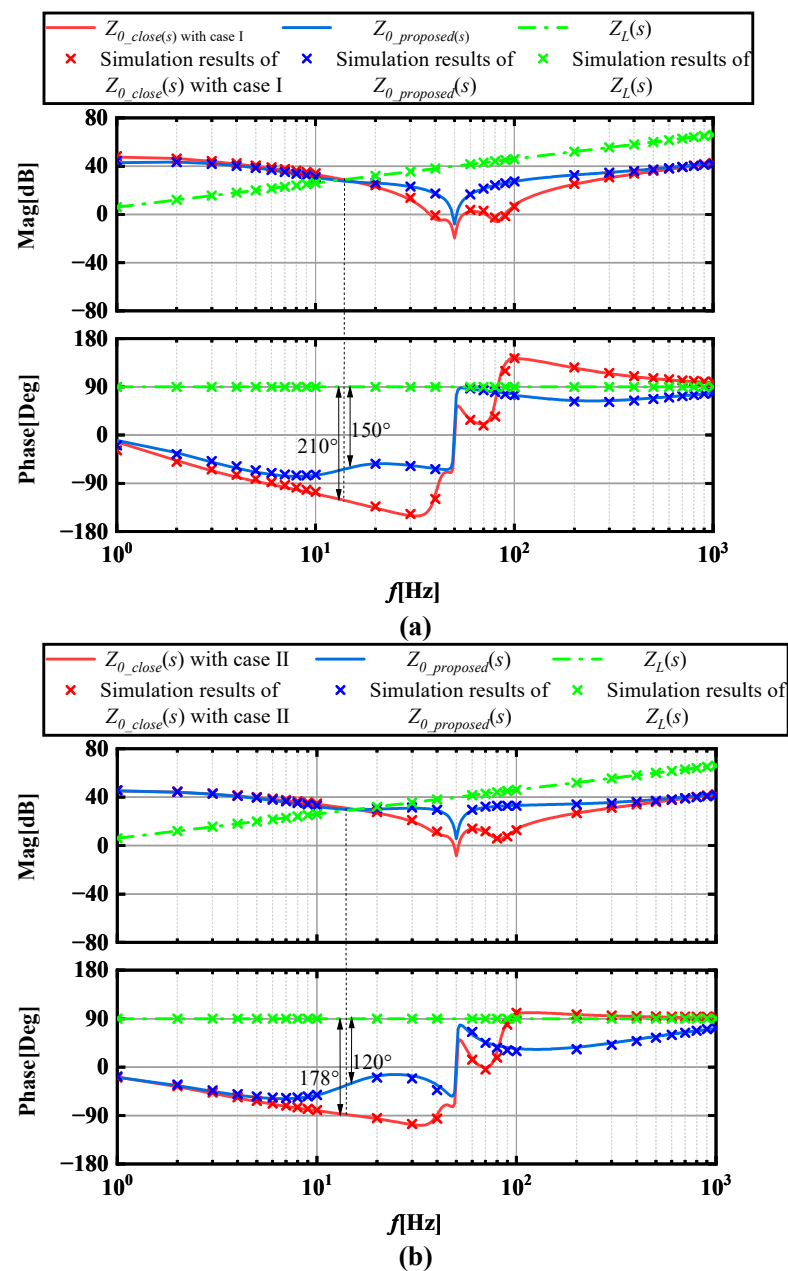
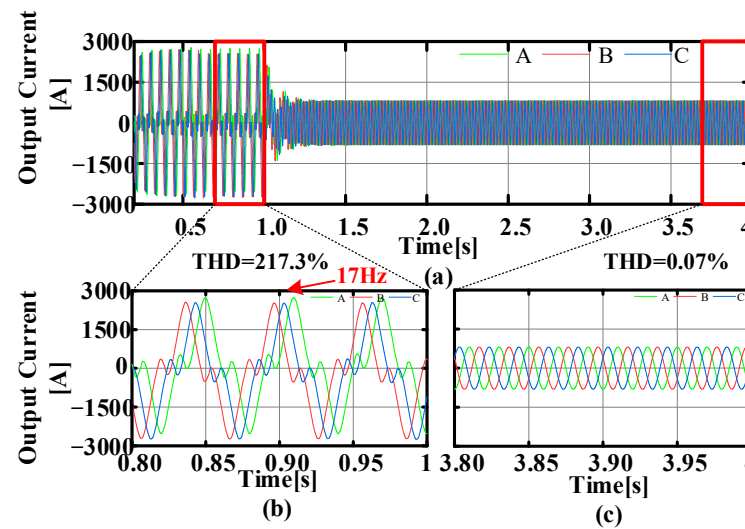


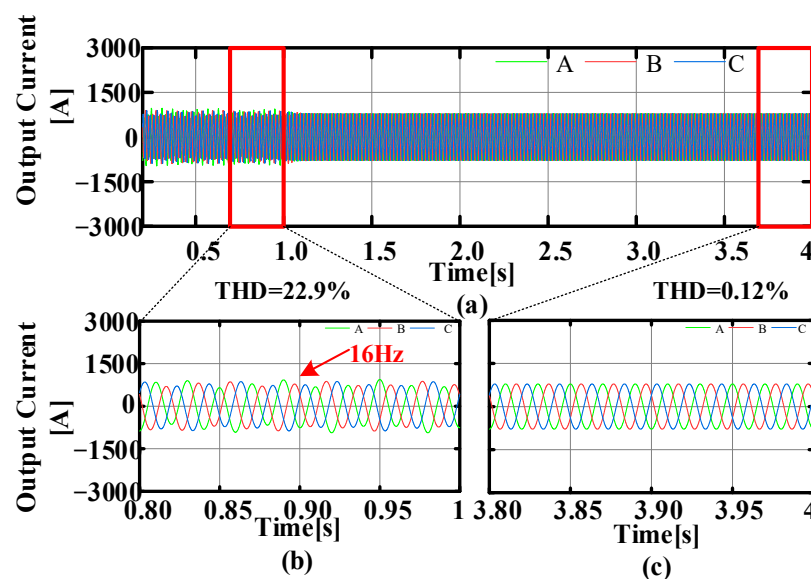
Figure 12. Impedance scan verification: (a) Case I parameters; (b) Case II parameters.

Figure 13a shows the time domain simulation results of the MMC system operating with the parameters given in Case I of Table 1. Figure 13b,c show the output current waveforms of the MMC system before and after using the feed-forward current compensation control. The disappearance of the 17 Hz harmonic and its corresponding mirror frequency, resulting in the 83 Hz harmonic, and effective suppression of the harmonics in the circuit can be observed in Figure 13 after the application of the feed-forward method. The results shown in Figure 12a demonstrate the stability of the MMC system after employing the feed-forward method.



**Figure 13.** Simulation results obtained for Case I: (a) time domain waveform; (b) enlarged oscillatory waveform; (c) enlarged stable waveform.

The simulation results for the MMC system operating with the parameters from Case II, as specified in Table 1, are shown in Figure 14. The system exhibits low harmonic content due to the presence of negative attenuation characteristics. The feed-forward current compensation control method effectively suppresses harmonics in the circuit. This finding is consistent with the results shown in Figure 12b, which indicate that the proposed method stabilizes the MMC system and eliminates negative damping characteristics.



**Figure 14.** Simulation results obtained for Case II: (a) time domain waveform; (b) enlarged oscillatory waveform; (c) enlarged stabilized waveform.

## 5. Experimental Verification

For further verification of the proposed feed-forward current compensation method, a hardware-in-the-loop experimental platform was constructed for the MMC system with a standalone grid, as illustrated in Figure 15. The MMC system uses an average model, and the parameter table is included in Appendix B. The experimental platform comprises the host computer StarSim HIL Advanced 5.0.0.0, StarSim real-time simulator, input/output connection, and an oscilloscope.

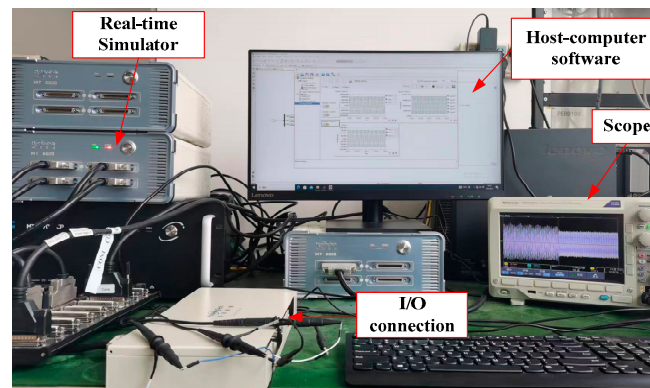


Figure 15. StarSim HIL hardware-in-the-loop experiment platform.

Before 2 s, the circuit with parameters from Case I in Table 1 exhibits the SSO, as demonstrated in Figure 16a. Harmonic currents within the oscillation are mainly at 16 Hz, with the maximum current magnitude reaching 961 A. As depicted in Figure 16, these harmonic currents lead to a 193.5% total harmonic distortion (THD) and degrade the quality of the output current. The use of the feed-forward current compensation method mitigates the SSO phenomenon in the circuit beyond 2 s.

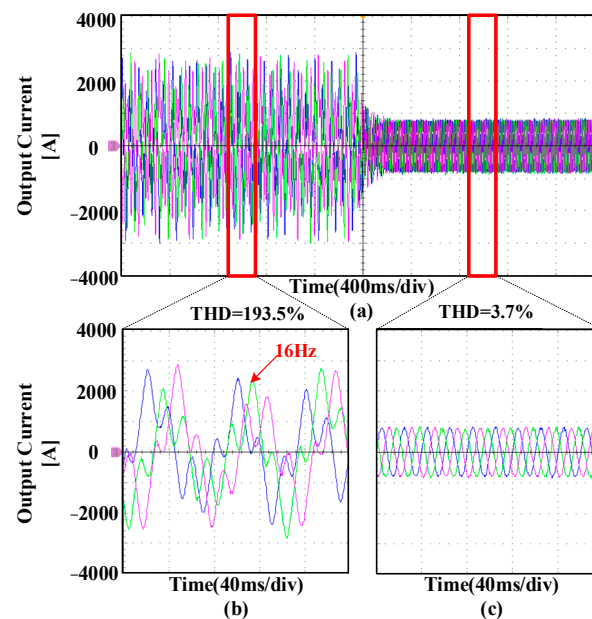
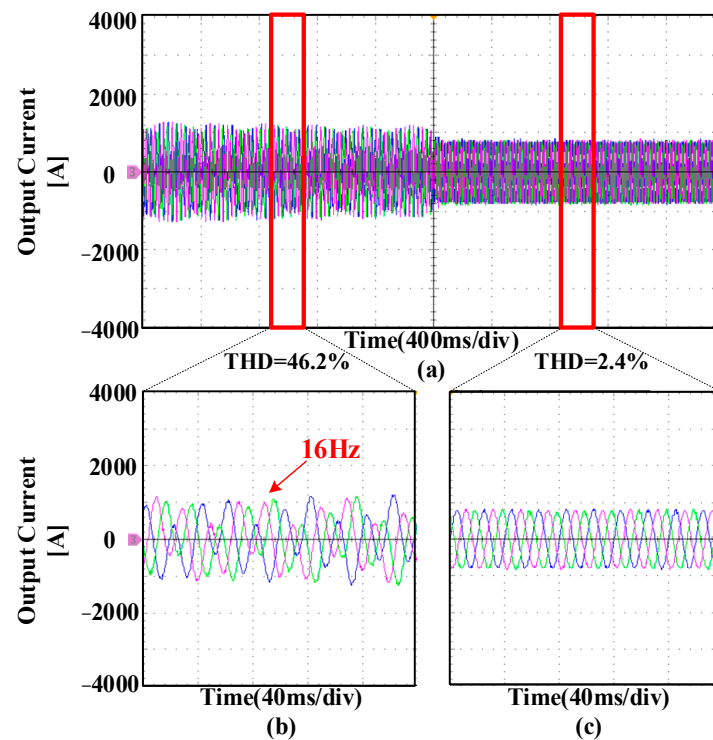


Figure 16. Experimental results obtained for Case I: (a) experimental waveform; (b) enlarged view of oscillating waveform; (c) enlarged view of stabilized waveform.

As seen in Figure 16, the current in the circuit is mainly at the fundamental frequency, with a THD value of 3.7%. The SSO is observed in the circuit with the parameters of Case II in Table 1 before 2 s, as illustrated in Figure 17a. During the oscillation, the 16 Hz harmonic currents prevail, reaching a maximum value of 296 A, causing a deterioration in the output current quality, resulting in a total harmonic distortion of 46.2%, as depicted in Figure 17b. The use of the feed-forward current compensation method eliminates the SSO in the circuit after 2 s. Consequently, the current frequency in the circuit is governed by the fundamental frequency, resulting in a total harmonic distortion of 2.4%, as depicted in Figure 17c.



**Figure 17.** Experimental results obtained for Case II: (a) experimental waveform; (b) enlarged view of oscillating waveform; (c) enlarged view of stabilized waveform.

The differences between the experimental results obtained using the StarSim hardware-in-the-loop platform and those obtained using the MATLAB/Simulink platform can be attributed to two main reasons. Firstly, the MMC models used on the two platforms are different, indicating a certain level of inaccuracy in the average value model. Secondly, the time step used on each platform differs; the StarSim HIL hardware-in-the-loop platform uses a simulation step size of 100  $\mu$ s, while the MATLAB/Simulink platform is set to 10  $\mu$ s.

Table 2 lists the similarities and differences between the methodology proposed in this paper and other references.

**Table 2.** Comparative analysis of stability methods.

| Refs.    | Oscillation Frequency Type | Suppression Method                                                      | Feed-Forward Method  |
|----------|----------------------------|-------------------------------------------------------------------------|----------------------|
| [24]     | High Frequency             | Second-order damping controller, round controller                       | Voltage feed-forward |
| [25]     | High Frequency             | First-order low pass filter, second-order, third-order band pass filter | Voltage feed-forward |
| [26]     | High Frequency             | Amplitude attenuation                                                   | /                    |
| [27]     | Medium and high frequency  | Combination of low-pass and high-pass filters                           | Voltage feed-forward |
| [33]     | Low Frequency              | PIR controller                                                          | /                    |
| Proposed | Low Frequency              | Improved PD controller                                                  | Current feed-forward |

## 6. Conclusions

In this study, a novel stability enhancement method based on impedance reshaping is proposed for standalone MMC control systems. Initially, the stability of MMC controllers using open- and closed-loop control was compared using the HSS method. Our model



identifies the negative damping region within the MMC during voltage control. The mutual coupling between the impedance of the MMC and the power line within this region induces SSO. Subsequently, we propose a damping method using a current feed-forward compensation impedance reshaping method based on the presence of 16 Hz harmonic currents at SSO. This damping method is based on an improved proportional–derivative controller, where the harmonic currents are fed back to the modulation signal to nullify the harmonic currents in the output current. The impedance map results demonstrate that the proposed method effectively eliminates the negative damping region and mitigates the SSO. Finally, this study corroborates the validity of the theoretical analysis through time domain simulation and hardware-in-the-loop experiments, establishing a sound basis for the proposed stability enhancement technique.

**Author Contributions:** Conceptualization, Y.Z., L.H., Y.Y., C.L. (Chun Li), C.L. (Chaoyi Luo), Z.S., X.Y., and H.F.; methodology, Y.Z., L.H., Y.Y., C.L. (Chun Li), C.L. (Chaoyi Luo), Z.S., and H.F.; software, Y.Z., L.H., Y.Y., C.L. (Chun Li), C.L. (Chaoyi Luo), Z.S., and H.F.; validation, Y.Z., L.H., Y.Y., C.L. (Chun Li), C.L. (Chaoyi Luo), Z.S., and H.F.; formal analysis, Y.Z., L.H., Y.Y., C.L. (Chun Li), C.L. (Chaoyi Luo), Z.S., and H.F.; investigation, Y.Z., L.H., Y.Y., C.L. (Chun Li), C.L. (Chaoyi Luo), Z.S., and H.F.; resources, Y.Z.; data curation, Y.Z.; writing—original draft preparation, Y.Z., L.H., Y.Y., C.L. (Chun Li), C.L. (Chaoyi Luo), Z.S., and H.F.; writing—review and editing, Y.Z. and H.F.; visualization, Y.Z. and H.F.; supervision, Y.Z. and X.Y.; project administration, Y.Z.; funding acquisition, Y.Z. All authors have read and agreed to the published version of the manuscript.

**Funding:** This work was supported by the Science and Technology Project of China Southern Power Grid Co., Ltd. (GZKJXM20222414).

**Data Availability Statement:** Data are contained within the article.

**Acknowledgments:** The authors would like to express their gratitude to the Science and Technology Project of China Southern Power Grid Co., Ltd.

**Conflicts of Interest:** Author X.Y., and H.F. has received research grants from Electric Power Research Institute, Guizhou Power Grid Co., Ltd.

## Abbreviations

| Abbreviations | Full Name                           |
|---------------|-------------------------------------|
| MMC           | modular multilevel converter        |
| SSO           | subsynchronous oscillation          |
| HSS           | harmonic state space                |
| HVDC          | high-voltage direct current         |
| SM            | submodule                           |
| DM            | differential mode                   |
| CM            | common mode                         |
| CCSC          | circuit current suppression control |
| THD           | total harmonic distortion           |

## Appendix A. HSS Modeling

To start with, by the principle of small signal analysis, we linearize the set of nonlinear differential equations that describe the basic circuit equations (Equations (3)–(6)) at the operating point:

Equations (Equations (3)–(6)) at the operating point:

$$\frac{d\tilde{i}_o}{dt} = -\frac{R_f}{L_f}\tilde{i}_o - \frac{m_p}{L_f}\tilde{u}_{cp}^\Sigma + \frac{m_n}{L_f}\tilde{u}_{cn}^\Sigma - \frac{2}{L_f}U_o - \frac{U_{cp}^\Sigma}{L_f}\tilde{m}_p + \frac{U_{cn}^\Sigma}{L_f}\tilde{m}_n \quad (\text{A1})$$

$$\frac{d\tilde{i}_{cir}}{dt} = -\frac{R_f}{L_f}\tilde{i}_{cir} - \frac{m_p}{2L_f}\tilde{u}_{cp}^\Sigma - \frac{m_n}{2L_f}\tilde{u}_{cn}^\Sigma - \frac{1}{2L_f}\tilde{U}_{dc} - \frac{U_{cp}^\Sigma}{2L_f}\tilde{m}_p - \frac{U_{cn}^\Sigma}{2L_f}\tilde{m}_n \quad (\text{A2})$$

$$\frac{d\tilde{u}_{cp}^{\Sigma}}{dt} = \frac{1}{C_{arm}} m_p \tilde{i}_{cir} - \frac{1}{2C_{arm}} m_p \tilde{i}_o + \frac{1}{C_{arm}} I_{cir} \tilde{m}_p - \frac{1}{2C_{arm}} I_o \tilde{m}_p \quad (A3)$$

$$\frac{d\tilde{u}_{cn}^{\Sigma}}{dt} = \frac{1}{C_{arm}} m_n \tilde{i}_{cir} + \frac{1}{2C_{arm}} m_n \tilde{i}_o + \frac{1}{C_{arm}} I_{cir} \tilde{m}_n + \frac{1}{2C_{arm}} I_o \tilde{m}_n \quad (A4)$$

Writing Equations (A1)–(A4) into matrix form

$$\dot{\tilde{x}}(t) = A(t)\tilde{x}(t) + B(t)\tilde{u}(t) + C(t)\tilde{m}(t) \quad (A5)$$

$$A(t) = \begin{bmatrix} -\frac{R_f}{L_f} & 0 & -\frac{m_p}{L_f} & \frac{m_n}{L_f} \\ 0 & -\frac{R_f}{L_f} & -\frac{m_p}{2L_f} & -\frac{m_n}{L_f} \\ -\frac{m_p}{2C_{arm}} & \frac{m_p}{C_{arm}} & 0 & 0 \\ \frac{m_n}{2C_{arm}} & \frac{m_n}{C_{arm}} & 0 & 0 \end{bmatrix} \quad (A6)$$

$$B(t) = \begin{bmatrix} -\frac{2}{L_f} & 0 \\ 0 & -\frac{1}{2L_f} \\ 0 & 0 \\ 0 & 0 \end{bmatrix} \quad (A7)$$

$$C(t) = \begin{bmatrix} -\frac{U_{cp}^{\Sigma}}{L_f} & \frac{U_{cn}^{\Sigma}}{L_f} \\ -\frac{U_{cp}^{\Sigma}}{2L_f} & -\frac{U_{cn}^{\Sigma}}{2L_f} \\ -\frac{I_o}{2C_{arm}} & \frac{I_{cir}}{C_{arm}} \\ \frac{I_o}{2C_{arm}} & \frac{I_{cir}}{C_{arm}} \end{bmatrix} \quad (A8)$$

$$\tilde{x}(t) = [\tilde{i}_o(t), \tilde{i}_{cir}(t), \tilde{u}_{cp}^{\Sigma}(t), \tilde{u}_{cn}^{\Sigma}(t)]^T \quad (A9)$$

$$\tilde{u}(t) = [\tilde{u}_o(t), \tilde{u}_{dc}(t)]^T \quad (A10)$$

$$\tilde{m}(t) = [\tilde{m}_p(t), \tilde{m}_n(t)]^T \quad (A11)$$

The time domain model is converted to the harmonic frequency domain using the HSS method:

$$s\tilde{x} = (A_{HSS} - Q)\tilde{x} + B_{HSS}\tilde{u} + C_{HSS}\tilde{m} \quad (A12)$$

$$\tilde{x} = \begin{bmatrix} \tilde{X}_{-h} \\ \vdots \\ \tilde{X}_{-1} \\ \tilde{X}_0 \\ \tilde{X}_1 \\ \vdots \\ \tilde{X}_h \end{bmatrix}, \tilde{X}_h = \begin{bmatrix} \tilde{i}_o(s + jh\omega_0) \\ \tilde{i}_{cir}(s + jh\omega_0) \\ \tilde{u}_{cp}^{\Sigma}(s + jh\omega_0) \\ \tilde{u}_{cn}^{\Sigma}(s + jh\omega_0) \end{bmatrix} \quad (A13)$$

$$u = \begin{bmatrix} \tilde{U}_{-h} \\ \vdots \\ \tilde{U}_{-1} \\ \tilde{U}_0 \\ \tilde{U}_1 \\ \vdots \\ \tilde{U}_h \end{bmatrix}, \tilde{U}_h = \begin{bmatrix} \tilde{u}_o(s + jh\omega_0) \\ \tilde{u}_{dc}(s + jh\omega_0) \end{bmatrix} \quad (A14)$$

$$\tilde{m} = \begin{bmatrix} \tilde{M}_{-h} \\ \vdots \\ \tilde{M}_{-1} \\ \tilde{M}_0 \\ \tilde{M}_1 \\ \vdots \\ \tilde{M}_h \end{bmatrix}, \tilde{M}_h = \begin{bmatrix} \tilde{m}_p(s + jh\omega_0) \\ \tilde{m}_n(s + jh\omega_0) \end{bmatrix} \quad (A15)$$

$$A_{HSS} = \Gamma[A(t)] \quad (A16)$$

$$B_{HSS} = \Gamma[B(t)] \quad (A17)$$

$$C_{HSS} = \Gamma[C(t)] \quad (A18)$$

$$Q = \text{diag}[-jh\omega_0 \quad \cdots \quad -j\omega_0 \quad j0 \quad j\omega_0 \quad \cdots \quad jh\omega_0] \quad (A19)$$

Equation (A12) shows that a matrix containing the relationship between the state variables and the input variables is constructed by the HSS method, and the corresponding impedance values can be obtained by extracting the elements of the matrix corresponding to the relationship.

By extracting the elements of the matrix between the state variables  $\tilde{i}_0$  and the input variables  $\tilde{u}_o$ , the corresponding AC side conductivity matrix elements can be obtained.

$$\tilde{i}_0 = Y_{MMC} \tilde{u}_o$$

$$Y_{MMC} = \begin{bmatrix} \ddots & & & \vdots & & \ddots \\ & Y_0(s - j\omega_0) & Y_{-1}(s) & Y_{-2}(s + j\omega_0) & & \\ \cdots & Y_1(s - j\omega_0) & Y_0(s) & Y_{-1}(s + j\omega_0) & \cdots & \\ & Y_2(s - j\omega_0) & Y_1(s) & Y_0(s + j\omega_0) & & \\ \ddots & & \vdots & & & \ddots \end{bmatrix} \quad (A20)$$

$$\tilde{i}_0 = [\cdots \quad \tilde{i}_0(s - j\omega_0) \quad \tilde{i}_0(s) \quad \tilde{i}_0(s + j\omega_0) \quad \cdots] \quad (A21)$$

$$\tilde{u}_o = [\cdots \quad \tilde{u}_o(s - j\omega_0) \quad \tilde{u}_o(s) \quad \tilde{u}_o(s + j\omega_0) \quad \cdots] \quad (A22)$$

Inverting Equation (A20) gives the AC side impedance matrix.

$$Z_{MMC} = Y_{MMC}^{-1}$$

$$= \begin{bmatrix} \ddots & & & \vdots & & \ddots \\ & Z_0(s - j\omega_0) & Z_{-1}(s) & Z_{-2}(s + j\omega_0) & & \\ \cdots & Z_1(s - j\omega_0) & Z_0(s) & Z_{-1}(s + j\omega_0) & \cdots & \\ & Z_2(s - j\omega_0) & Z_1(s) & Z_0(s + j\omega_0) & & \\ \ddots & & \vdots & & & \ddots \end{bmatrix} \quad (A23)$$

## Appendix B. MMC Parameters

| Symbol               | Description                         | Value |
|----------------------|-------------------------------------|-------|
| $U_{grms}/\text{kV}$ | RMS value of the ac output voltage  | 100   |
| $U_{dc}/\text{kV}$   | Dc side voltage                     | 200   |
| $f_0/\text{Hz}$      | Grid frequency                      | 50    |
| $S_0/\text{kW}$      | Rated output power                  | 100   |
| $L_f/\text{mH}$      | Arm inductance                      | 45    |
| $R_f/\Omega$         | Arm resistance                      | 0.15  |
| $C/\text{mF}$        | Capacitance of the submodule        | 33    |
| $N$                  | Number of the submodule in each arm | 100   |

| Symbol          | Description          | Value                |
|-----------------|----------------------|----------------------|
| $L_0/\text{mH}$ | Load impedance       | 318                  |
| $K_{Pdm}$       | Proportional gains   | $5 \times 10^{-7}$   |
| $K_{Rdm}$       | Resonance gains      | $7.5 \times 10^{-4}$ |
| $\omega_v$      | Resonance bandwidths | $\pi$                |

## References

- Liu, Z.; Lv, X.; Wu, F.; Li, Z. Multi-Mode Active Inertia Support Strategy for MMC-HVDC Systems Considering the Constraint of DC Voltage Fluctuations. *IEEE Trans. Power Deliv.* **2023**, *38*, 2767–2781. [\[CrossRef\]](#)
- Guo, C.; Xu, L.; Yang, S.; Jiang, W. A Supplementary Damping Control for MMC-HVDC System to Mitigate the Low-Frequency Oscillation Under Low Inertia Condition. *IEEE Trans. Power Deliv.* **2023**, *38*, 287–298. [\[CrossRef\]](#)
- Pan, R.; Liu, D.; Liu, S.; Yang, J.; Kou, L.; Tang, G. Stability Comparison Between Grid-Forming and Grid-Following Based Wind Farms Integrated MMC-HVDC. *J. Mod. Power Syst. Clean Energy* **2023**, *11*, 1341–1355. [\[CrossRef\]](#)
- Lin, L.; Zeng, Q.; Zhu, J.; Shi, X.; Hu, J. High-Frequency Oscillation Mechanism Analysis and Suppression Strategy of Grid-Forming Control MMC-HVDC. *IEEE Trans. Power Deliv.* **2023**, *38*, 1588–1600. [\[CrossRef\]](#)
- Jia, K.; Dong, X.; Wen, Z.; Wu, W.; Bi, T. Harmonic Injection Based Fault Ride-through Control of MMC-HVDC Connected Offshore Wind Farms. *IEEE Trans. Sustain. Energy* **2023**, *14*, 1796–1806. [\[CrossRef\]](#)
- Yang, R.; Shi, G.; Zhang, C.; Li, G.; Cai, X. Internal Energy Based Grid-Forming Control for MMC-HVDC Systems with Wind Farm Integration. *IEEE Trans. Ind. Appl.* **2023**, *59*, 503–512. [\[CrossRef\]](#)
- Zhou, H.; Yao, W.; Zhou, M.; Ai, X.; Wen, J.; Cheng, S. Active Energy Control for Enhancing AC Fault Ride-Through Capability of MMC-HVDC Connected with Offshore Wind Farms. *IEEE Trans. Power Syst.* **2023**, *38*, 2705–2718. [\[CrossRef\]](#)
- Tang, G.; He, Z.; Pang, H.; Huang, X.; Zhang, X. Basic Topology and Key Devices of the Five-Terminal DC Grid. *CSEE Power Energy Syst.* **2015**, *1*, 22–35. [\[CrossRef\]](#)
- Paulo, M.S.; Almeida, A.D.O.; Almeida, P.M.D.; Barbosa, P.G. Control of an Offshore Wind Farm Considering Grid-Connected and Stand-Alone Operation of a High-Voltage Direct Current Transmission System Based on Multilevel Modular Converters. *Energies* **2023**, *16*, 5891. [\[CrossRef\]](#)
- Zou, C.; Rao, H.; Xu, S.; Li, Y.; Li, W.; Chen, J.; Zhao, X.; Yang, Y.; Lei, B. Analysis of Resonance Between a VSC-HVDC Converter and the AC Grid. *IEEE Trans. Power Electron.* **2018**, *33*, 10157–10168. [\[CrossRef\]](#)
- Yan, Z.; Xu, J.; Yu, G.; Bai, E.; Chen, W. Detailed Wideband Impedance Modeling and Resonance Analysis of Grid-Connected Modular Multilevel Converter. *Energies* **2023**, *16*, 4782. [\[CrossRef\]](#)
- Yan, Z.; Xu, J. Sequence Impedance Modeling and Analysis of Modular Multilevel Converter Considering DC Port Characteristics. *Energies* **2023**, *16*, 7770. [\[CrossRef\]](#)
- Guo, X.; Yuan, B.; Li, X.; Yin, C. Research on the High Frequency Oscillation of MMC-HVDC Integrated into Renewable Energy System. In Proceedings of the 2021 IEEE Southern Power Electronics Conference (SPEC), Kigali, Rwanda, 6 December 2021; IEEE: Piscataway, NJ, USA, 2021; pp. 1–6.
- Buchhagen, C.; Greve, M.; Menze, A.; Jung, J. Harmonic Stability-Practical Experience of a TSO. In Proceedings of the Wind Integration Workshop, Vienna, Austria, 14–17 November 2016; Volume 2016, pp. 1–6.
- Li, L.; Xu, C.; Enke, Y.U.; Xingwei, Y.U. Zhoushan Multi-Terminal VSC-HVDC Transmission Demonstration Project and Its Evaluation. *South. Power Syst. Technol.* **2019**, *13*, 10. [\[CrossRef\]](#)
- Lyu, J.; Cai, X.; Molinas, M. Optimal Design of Controller Parameters for Improving the Stability of MMC-HVDC for Wind Farm Integration. *IEEE J. Emerg. Sel. Top. Power Electron.* **2018**, *6*, 40–53. [\[CrossRef\]](#)
- Buchhagen, C.; Rauscher, C.; Menze, A.; Jung, D.J. BorWin1—First Experiences with Harmonic Interactions in Converter Dominated Grids. In Proceedings of the International ETG Congress, Bonn, Germany, 17–18 November 2015; Die Energiewende-Blueprints for the New Energy Age; VDE: Offenbach, Germany, 2015; pp. 1–7.
- Jamshidi Far, A.; Jovcic, D. Small-Signal Dynamic DQ Model of Modular Multilevel Converter for System Studies. *IEEE Trans. Power Deliv.* **2016**, *31*, 191–199. [\[CrossRef\]](#)
- Lyu, J.; Zhang, X.; Cai, X.; Molinas, M. Harmonic State-Space Based Small-Signal Impedance Modeling of a Modular Multilevel Converter With Consideration of Internal Harmonic Dynamics. *IEEE Trans. Power Electron.* **2019**, *34*, 2134–2148. [\[CrossRef\]](#)
- Ji, K.; Pang, H.; Yang, J.; Tang, G. DC Side Harmonic Resonance Analysis of MMC-HVDC Considering Wind Farm Integration. *IEEE Trans. Power Deliv.* **2021**, *36*, 254–266. [\[CrossRef\]](#)
- Cespedes, M.; Xing, L.; Sun, J. Constant-Power Load System Stabilization by Passive Damping. *IEEE Trans. Power Electron.* **2011**, *26*, 1832–1836. [\[CrossRef\]](#)
- Xue, Y.; Xu, Z.; Tang, G. Self-Start Control With Grouping Sequentially Precharge for the C-MMC-Based HVDC System. *IEEE Trans. Power Deliv.* **2014**, *29*, 187–198. [\[CrossRef\]](#)
- Zhu, Y.; Pou, J.; Konstantinou, G. Impedance Shaping Effects and Stability Assessment of Circulating Current Control Schemes in Modular Multilevel Converters. *IEEE Trans. Power Deliv.* **2023**, *38*, 666–676. [\[CrossRef\]](#)
- Li, Y.; An, T.; Zhang, D.; Pei, X.; Ji, K.; Tang, G. Analysis and Suppression Control of High Frequency Resonance for MMC-HVDC System. *IEEE Trans. Power Deliv.* **2021**, *36*, 3867–3881. [\[CrossRef\]](#)

25. Li, Y.; Pang, H.; Kong, M.; Lu, J.; Ji, K.; Tang, G. Compensation Control and Parameters Design for High Frequency Resonance Suppression of MMC-HVDC System. *CSEE JPES* **2020**, *7*, 1161–1175. [[CrossRef](#)]
26. Yang, S.; Liu, K.; Qin, L.; Zhu, S.; Xu, B.; Wang, Q. A Broadband Active Damping Method for High-Frequency Resonance Suppression in MMC-HVDC System. *Int. J. Electr. Power Energy Syst.* **2023**, *146*, 108791. [[CrossRef](#)]
27. Dai, F.; Liu, S.; Wang, G.; Zeng, D. Coordinated Suppression Strategies for Passive and Active Resonances of the MMC-HVDC AC-side System on the Medium- and High-frequency Bands. *IET Gener. Transm. Distrib.* **2023**, *17*, 2963–2977. [[CrossRef](#)]
28. Ludois, D.C.; Venkataramanan, G. Simplified Terminal Behavioral Model for a Modular Multilevel Converter. *IEEE Trans. Power Electron.* **2014**, *29*, 1622–1631. [[CrossRef](#)]
29. Xiong, X.; Yang, Y.; Wu, C.; Zhao, C.; Blaabjerg, F. Improving the Stability of Standalone MMCs by Shaping the AC Side Impedance Using Insertion Index Compensation. *IEEE J. Emerg. Sel. Top. Circuits Syst.* **2022**, *12*, 81–89. [[CrossRef](#)]
30. Zou, Z.; Liserre, M. Modeling Phase-Locked Loop-Based Synchronization in Grid-Interfaced Converters. *IEEE Trans. Energy Convers.* **2020**, *35*, 394–404. [[CrossRef](#)]
31. Visioli, A. *Practical PID Control*; Advances in Industrial Control; Springer: London, UK, 2006; ISBN 978-1-84628-585-1.
32. Langella, R.; Testa, A.; Alii, E. IEEE Recommended Practice and Requirements for Harmonic Control in Electric Power Systems. In *IEEE Recommended Practice*; IEEE: Piscataway, NJ, USA, 2014.
33. Wu, H.; Wang, X.; Kocewiak, L.H. Impedance-Based Stability Analysis of Voltage-Controlled MMCs Feeding Linear AC Systems. *IEEE J. Emerg. Sel. Top. Power Electron.* **2020**, *8*, 4060–4074. [[CrossRef](#)]

**Disclaimer/Publisher’s Note:** The statements, opinions and data contained in all publications are solely those of the individual author(s) and contributor(s) and not of MDPI and/or the editor(s). MDPI and/or the editor(s) disclaim responsibility for any injury to people or property resulting from any ideas, methods, instructions or products referred to in the content.