



Article Sliding Mode Predictive Current Control for Single-Phase H-Bridge Converter with Parameter Robustness

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Abstract: As the important technology of renewable energy systems, power electronics technology is directly bound up with the prospect and development of renewable energy technology. As the output end of renewable energy systems, a single-phase H-bridge converter needs to stabilize the output current. When predictive current control (PCC) tracks the reference current, the dynamic response is the fastest, but the control delay and the changes in model parameters will cause the output current steady-state error. The sliding mode predictive current control (SMPCC) algorithm is proposed to control the output current better. The proposed SMPCC scheme uses the combination of traditional PCC and variable structure scheme, and it establishes the mathematical model according to the state equation of the converter. Taking the exponential reaching law as control law, the expression of the variable structure controller is obtained. The MATLAB experimental and simulation results show that SMPCC can not only improve its robustness to the parameter changes but also obtain better steady-state performance while enhancing the rapidity of the current changes. In conclusion, SMPCC has a better control effect in the converter.

Keywords: single-phase H-bridge converter; regulated power supply; steady-state error; sliding mode predictive current control; state observer

1. Introduction

The integration of large-scale renewable energy sources is essential in the efforts towards reaching carbon neutrality, and it will fundamentally reshape modern power systems. Power electronics technologies have seen significant advancements in recent years, and now they have an important role in the development of renewable energy systems. This has contributed to advances in materials, design, modeling, controls, manufacturing, and applications of new power electronics technologies that provide high performance, low cost, and high reliability for converters and renewable energy systems. As the important technology of renewable energy systems, power electronics technology is directly bound up with the prospect and development of renewable energy technology, and it is closely linked to social demand and progress. Renewable energy systems have been widely used with the consecutive development of power electronics technology, and there are applications such as solar photovoltaic systems [1-3], wind generation systems [4,5], and hybrid energy systems [6,7]. In the paper, a renewable energy system is used in high-power occasions, so the output current can achieve 3000 A. As an important part of renewable energy systems, the research on DC/DC converter is mainly divided into two directions: one is to develop a novel topology to improve the conversion efficiency of the converter; the other is to propose new control methods and optimization algorithms to improve the converter performance. The switch voltage stress of a single-phase H-bridge converter is lower, and fewer components are important to improve the power density. Furthermore, the polarity of the output voltage can be changed.

DC/DC converters are strongly nonlinear, so the common algorithm cannot obtain a better effect. The simplest algorithm is proportional-integral (PI) control; many algorithms



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Copyright: © 2023 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). appeared later, such as bilinear theory [8], sliding mode control [9], adaptive control [10], and other algorithms. Sliding mode variable structure control is embodied as discontinuous, and it has the characteristics of changing like a switch with the change of time. This is consistent with the DC/DC converter's characteristic of realizing electric energy conversion by controlling the switches, so there are many applications of variable structure control in the research of many DC/DC converters. Through the established predictive model, the future state variable is predicted by using predictive control. In order to output the control variable, the error between the predicted value and the given value is used [11]. Comparing with using the error between the given value and the measured value, the method is better.

Predictive current control (PCC) has a better prospect with the consecutive development of digital signal processors [12]. As a model-based technology, PCC has many advantages, such as fast dynamic response, high current tracking accuracy, and fixed switching frequency [13–16]. In practical applications, renewable energy systems are timevarying control systems. However, PCC performance is decided by the model accuracy, and the inductor parameter cannot be accurately acquired [17]. The parameter perturbation leads to the mismatch of model parameters, which leads to an unsatisfactory system response and further deteriorates the control performance of the system. Reference [18] improves its sensitivity to the change of inductance parameter by the online look-up table method, but its accuracy is not high. Reference [19] proposed a variable-step adaptive filtering algorithm to achieve predictive control, but its steady-state error is relatively large. Reference [20] improved the current equation to eliminate control delay influence, but its steady-state error still exists.

The sliding-mode algorithms have been applied to the high-performance control of power converters and motor drives for a long time [21]. For example, the first-order sliding-mode controller [22] and the high-order sliding-mode controller [23]. Reference [22] proposes a discrete control algorithm to suppress the influence of various disturbances on the low-speed gimbal servo system, and the constraint between controller parameters and sampling time is established to ensure the stability of the system. Reference [23] demonstrates the robust performance and robust stability of the system with respect to the filter parameters uncertainties, grid impedance, grid frequency, and grid voltage, as well as the unknown load dynamics that include unbalanced loads and nonlinear loads with harmonic currents.

In order to improve the robustness, stability, and dynamic performance, this paper applies sliding mode predictive current control (SMPCC) to renewable energy systems, and it uses carrier-based pulse width modulation (PWM) control to obtain a fixed switching frequency. Compared with traditional PCC, SMPCC is less sensitive to changes in the system parameters. Firstly, the converter's discrete model is obtained by using the mathematical model. By minimizing the error between the reference current and the predicted current, a reasonable sliding mode variable structure controller is designed. The sensitivity of the controller to the inductance parameter is reduced, and it eliminates the steady-state error in traditional PCC. In order to improve the estimation accuracy of output current, A state observer is constructed in the delay compensation. The controller is simulated and experimented with to verify the controller design rationality. Finally, simulation and experiments show that SMPCC can ensure the current dynamic response and it reduces the current steady-state error. This SMPCC reduces the sensitivity to the inductance parameter, and it achieves the expected effect in the control of renewable energy systems. The correctness and validity of SMPCC are proved.

2. Sliding Mode Predictive Current Control

2.1. Establishment of System Model

Figure 1 shows the topology, which is made up of a load R, a capacitor C, a parasitic resistance r, an inductor L, four switches, and an input voltage source V_{in} . PWM1, with a control period of T and a duty cycle of d, is used to control M1 and M4, while PWM2, which is complementary to PWM1, controls M2 and M3. In the power-on process, the

inductance and parasitic resistance will change with the magnitude of the current and the power-on time. Let the inductance value offset be ΔL .



Figure 1. The topology of single-phase H-bridge converter.

It is supposed that the converter works in continuous current mode, so the inductor current i_L is always not equal to zero, the parasitic resistance current $i_r = i_L$. i_o is the output current, $i_o = \frac{V_o}{R}$.

The system state expression can be obtained by circuit analysis as

$$L\frac{di_L}{dt} = uV_{in} - i_L r - i_o R \tag{1}$$

$$RC\frac{di_o}{dt} = i_L - i_o \tag{2}$$

The matrix equation is expressed as

$$\begin{bmatrix} \frac{di_L}{dt} \\ \frac{di_o}{dt} \end{bmatrix} = \begin{bmatrix} -\frac{r}{L} & -\frac{R}{L} \\ \frac{1}{RC} & -\frac{1}{RC} \end{bmatrix} \begin{bmatrix} i_L \\ i_o \end{bmatrix} + \begin{bmatrix} \frac{V_{in}}{L} \\ 0 \end{bmatrix} u$$
(3)

u is the switch function:

$$u = 2d - 1 = \begin{cases} 1, 0 \le t \le dT \\ -1, dT < t \le T \end{cases}$$
(4)

2.2. Design of Sliding Mode Controller

In order to control i_o and i_L at the same time, The sliding mode controller is designed according to the system state equation, and the sliding surface is designed according to the PI control idea, so the system errors are defined as follows:

$$x_1 = i_{L,ref} - i_L \tag{5}$$

$$x_2 = i_{o,ref} - i_o, \tag{6}$$

in the formula, $i_{o,ref}$ is the output reference current.

Then integrate x_2 to attain x_3 :

$$x_3 = \int x_2 dt = \int (i_{o,ref} - i_o) dt \tag{7}$$

Then differentiate x_1 , x_2 and x_3 respectively and sort them out to attain:

$$\frac{dx_1}{dt} = -\frac{di_L}{dt} = -\frac{V_{in}}{L}u + \frac{r}{L}i_L + \frac{R}{L}i_o$$
(8)

$$\frac{dx_2}{dt} = -\frac{di_o}{dt} = -\frac{1}{RC}i_L + \frac{1}{RC}i_o$$
(9)

$$\frac{dx_3}{dt} = x_2 \tag{10}$$

Taking x_1 , x_2 and x_3 as state variables, (8)–(10) are written in the form of state equation:

$$\frac{dx}{dt} = Ax + Bu + D,\tag{11}$$

where *x*, *A*, *B*, *D* can be written

$$\boldsymbol{x} = \begin{bmatrix} x_1 & x_2 & x_3 \end{bmatrix}^T, \boldsymbol{A} = \begin{bmatrix} 0 & 0 & 0 \\ 0 & 0 & 0 \\ 0 & 1 & 0 \end{bmatrix}, \boldsymbol{B} = \begin{bmatrix} -\frac{V_{in}}{L} & 0 & 0 \end{bmatrix}^T,$$

$$\boldsymbol{D} = \begin{bmatrix} \frac{r}{L}i_L + \frac{R}{L}i_o & -\frac{1}{RC}i_L + \frac{1}{RC}i_o & 0 \end{bmatrix}^T$$
(12)

The sliding mode variable *s* of the sliding mode controller is defined as

$$s = \boldsymbol{\lambda}^{\mathrm{T}} \boldsymbol{x} = \lambda_1 x_1 + \lambda_2 x_2 + \lambda_3 x_3 = \lambda_1 x_1 + \lambda_2 x_2 + \lambda_3 \int x_2 dt = \lambda_1 (x_1 + \frac{\lambda_2}{\lambda_1} x_2 + \frac{\lambda_3}{\lambda_1} \int x_2 dt),$$
(13)

where $\lambda = \begin{bmatrix} \lambda_1 & \lambda_2 & \lambda_3 \end{bmatrix}^T$, $\lambda_1 \neq 0$, and λ_1 , λ_2 and λ_3 are positive or negative constants at the same time.

Taking the sliding mode variable derivative *s* and the first derivative of *s* can be obtained.

$$\begin{split} \frac{ds}{dt} &= \boldsymbol{\lambda}^{\mathrm{T}} \frac{dx}{dt} = \boldsymbol{\lambda}^{\mathrm{T}} (\boldsymbol{A} \boldsymbol{x} + \boldsymbol{B} \boldsymbol{u} + \boldsymbol{D}) = \begin{bmatrix} \lambda_1 & \lambda_2 & \lambda_3 \end{bmatrix} \begin{bmatrix} 0 & 0 & 0 \\ 0 & 0 & 0 \\ 0 & 1 & 0 \end{bmatrix} \begin{bmatrix} x_1 \\ x_2 \\ x_3 \end{bmatrix} \\ &+ \begin{bmatrix} \lambda_1 & \lambda_2 & \lambda_3 \end{bmatrix} \begin{bmatrix} -\frac{V_{in}}{L} \\ 0 \\ 0 \end{bmatrix} \boldsymbol{u} + \begin{bmatrix} \lambda_1 & \lambda_2 & \lambda_3 \end{bmatrix} \begin{bmatrix} \frac{r}{L} i_L + \frac{R}{L} i_o \\ -\frac{1}{RC} i_L + \frac{1}{RC} i_o \end{bmatrix}$$
(14)

The controller robustness and stability can be improved by selecting an appropriate reaching law for the sliding mode controller. This design adopts the exponential reaching law as a control law, which has the characteristics of fast response and small chattering [24]. The exponential approach law can be written:

$$\frac{ds}{dt} = -\varepsilon sgn(s) - ms, \varepsilon > 0, m > 0,$$
(15)

where sgn(s) is the sign function.

The sign function in (15) is:

$$sgn(s) = \begin{cases} 1, s > 0 \\ 0, s = 0 \\ -1, s < 0 \end{cases}$$
(16)

According to (14) and (15), the expression can be obtained:

$$\lambda^{\mathrm{T}}Ax + \lambda^{\mathrm{T}}Bu + \lambda^{\mathrm{T}}D = -\varepsilon sgn(s) - ms$$
(17)

Arranging (17) and replacing the switching function u with d, the expression can be obtained:

$$d = -\frac{1}{2} [\lambda^{\mathrm{T}} \boldsymbol{B}]^{-1} [\lambda^{\mathrm{T}} \boldsymbol{A} \boldsymbol{x} + \lambda^{\mathrm{T}} \boldsymbol{D} + \varepsilon sgn(s) + ms] + \frac{1}{2} = [\frac{R}{2V_{in}} + \frac{L\lambda_2}{2RC\lambda_1 V_{in}} - \frac{L\lambda_3}{2V_{in}\lambda_1}] i_o + \frac{\lambda_3 L}{2V_{in}\lambda_1} i_{o,ref} + \frac{1}{2} + \frac{r\lambda_1 RC - L\lambda_2}{2\lambda_1 RCV_{in}} i_L - \frac{\varepsilon L}{2V_{in}\lambda_1} sgn(s) - \frac{mL}{2V_{in}\lambda_1} s$$

$$(18)$$

Chattering may occur in the process of input control, so sgn(s) is replaced by the saturation function sat(s), which can effectively reduce chattering so that the final expression of the controller is obtained as

$$d = -\frac{1}{2} [\boldsymbol{\lambda}^{\mathrm{T}} \boldsymbol{B}]^{-1} [\boldsymbol{\lambda}^{\mathrm{T}} \boldsymbol{A} \boldsymbol{x} + \boldsymbol{\lambda}^{\mathrm{T}} \boldsymbol{D} + \varepsilon sat(s) + ms] + \frac{1}{2} = [\frac{R}{2V_{in}} + \frac{L\lambda_{2}}{2RC\lambda_{1}V_{in}} - \frac{L\lambda_{3}}{2V_{in}\lambda_{1}}] i_{o} + \frac{\lambda_{3}L}{2V_{in}\lambda_{1}} i_{o,ref} + \frac{1}{2} + \frac{r\lambda_{1}RC-L\lambda_{2}}{2\lambda_{1}RCV_{in}} i_{L} - \frac{\varepsilon L}{2V_{in}\lambda_{1}} sat(s) - \frac{mL}{2V_{in}\lambda_{1}} s,$$
(19)

where the expression of sat(s) is

$$sat(s) = \begin{cases} \frac{s}{\delta}, |s| \le \delta\\ sgn(s), |s| > \delta \end{cases}$$
(20)

In the formula, $\delta > 0$, δ is the upper limit of the boundary layer of the switching surface, and the saturation function is used to switch the moving points outside the boundary layer while the moving points inside the boundary layer change linearly. By choosing an appropriate δ value, the error can converge to zero, thereby reducing chattering.

Because MATLAB deals with discrete data, the system needs to be discretized first, and

$$\frac{d\mathbf{x}}{dt} = \frac{\mathbf{x}(k+1) - \mathbf{x}(k)}{\Delta} \tag{21}$$

Therefore, (11) can be changed to discrete form as

$$\mathbf{x}(k+1) = (\mathbf{A}\Delta + I)\mathbf{x}(k) + \mathbf{B}\Delta u + \mathbf{D}\Delta$$
(22)

In the formula, $\mathbf{x}(k) = \begin{bmatrix} x_1(k) & x_2(k) & x_3(k) \end{bmatrix}^T$, \mathbf{I} is the third-order unit matrix and Δ is the sampling period.

In the same way, the discrete exponential reaching law can be obtained (the sign function has been replaced by the saturation function):

$$s(k+1) = (1 - m\Delta)s(k) - \Delta\varepsilon sat[s(k)]$$
⁽²³⁾

From (22) and (23), the discrete sliding mode controller is obtained as

$$d = -\frac{1}{2} [\lambda^{\mathrm{T}} \boldsymbol{B} \Delta]^{-1} (\lambda^{\mathrm{T}} (\boldsymbol{A} \Delta + \boldsymbol{I}) \boldsymbol{x}(k) + \lambda^{\mathrm{T}} \boldsymbol{D} \Delta - (1 - m\Delta) \boldsymbol{s}(k) + \Delta \varepsilon sat[\boldsymbol{s}(k)]) + \frac{1}{2}, \quad (24)$$

where $i_L(k)$ of x(k) is substituted by the predicted value, and the discrete sliding mode predictive controller is obtained by (24).

2.3. System Stability Analysis

The Lyapunov function *V* is used to verify the stability of the system:

$$V = \frac{1}{2}s^2\tag{25}$$

V is derivative to attain:

$$\frac{dV}{dt} = s\frac{ds}{dt} = s[-\varepsilon sgn(s) - ms] = -\varepsilon s * sgn(s) - ms^2 = -\varepsilon|s| - ms^2 \le 0$$
(26)

The equal sign holds if and only if s = 0.

Equation (26) shows that the sliding mode variable *s* is asymptotically stable, and it satisfies the Lyapunov stability condition, which verifies that the system is stable.

2.4. Traditional PCC and Control Delay Compensation

The mathematical model is also

$$L\frac{di_L}{dt} = V_{ab} - i_L r - V_o, \qquad (27)$$

where V_{ab} is the voltage between point *a* and point *b*.

A cost function is used in traditional PCC. The function which defines the error of the inductor current can be written:

$$J = \left| i_{L,ref} - i_L(k+1) \right| \tag{28}$$

Equation (28) is the Euclidean distance between the estimated current and the reference current.

From (27), the inductor current predicted value which is at the (k + 1)th time

$$i_L(k+1) = (1 - \frac{Tr}{L})i_L(k) + T[V_{ab}(k) - V_o(k)]/L$$
⁽²⁹⁾

The inductor voltage in the next moment can be obtained.

$$V_L(k+1) = L[i_{L,ref} - i_L(k+1)]/L$$
(30)

 V_{ab} can be expressed to

$$V_{ab} = \begin{cases} V_{in}, 0 \le t \le dT \\ -V_{in}, dT < t \le T \end{cases}$$
(31)

The average value of V_{ab} in one control cycle is

$$V_{ab,ave} = \frac{V_{in}dT - V_{in}(T - dT)}{T} = V_L + i_L r + V_o$$
(32)

The above equation is solved to attain

$$d = \frac{V_L + i_L r + V_{in} + V_o}{2V_{in}}$$
(33)

In order to eliminate a one-step delay of digital execution, $i_L(k+1)$ is used in (29) instead of $i_L(k)$.

It is difficult to acquire the actual value of *L* in the system, so the control effect is affected. The specific form of the state observer of the error is:

$$\hat{i}_L(k+1) = (1 - \frac{Tr}{L})\hat{i}_L(k) + T[V_{ab}(k) - V_o(k)]/L + K_C[i_L(k) - \hat{i}_L(k)]$$
(34)

In the equation, $\hat{i}_L(k)$ is the estimated value of $i_L(k)$ which is at the (k-1)th time, and K_C is the gain of the state observer. In (34), the observer of $\hat{i}_L(k+1)$ consists of Equation (29) and a correction part, which is based on the error between $\hat{i}_L(k)$ and $i_L(k)$.

Define the state error $i_L(k) = i_L(k) - \hat{i}_L(k)$, (34) is subtracted from (29) to obtain the dynamics error.

$$\widetilde{i}_L(k+1) = \widetilde{i}_L(k) - K_C \widetilde{i}_L(k) = (1 - \frac{Tr}{L} - K_C)\widetilde{i}_L(k)$$
(35)

And we will know

$$\tilde{i}_{L}(k+1) = (1 - \frac{Tr}{L} - K_{C})^{k+1} \tilde{i}_{L}(0),$$
(36)

By adjusting K_{C} , the predicted current can gradually approach the actual current.

The inductor current ripple varies with the inductor value. The inductor current ripple equation is shown below.

$$\Delta i_L = \frac{V_L \Delta t}{L} = \frac{Et}{L} = \frac{V_{on} dT}{L},\tag{37}$$

where Et is volt seconds, and V_{on} is the voltage when the inductor is conducting forward.

When the inductance parameter is inaccurate, the inductance parameter is $(L + \Delta L)$, and the inductance current ripple can be obtained

$$\Delta i_{L+\Delta L} = \frac{V_L \Delta t}{L + \Delta L} = \frac{Et}{L + \Delta L} = \frac{V_{on} dT}{L + \Delta L}$$
(38)

Therefore, the difference between the inductance current ripple when the inductance parameter is inaccurate and the ideal inductance current ripple

$$\Delta i_{\Delta L} = \Delta i_{L+\Delta L} - \Delta i_L = \frac{V_{on}dT}{L} - \frac{V_{on}dT}{L+\Delta L} = \frac{-V_{on}dT\Delta L}{L(L+\Delta L)}$$
(39)

3. Results

3.1. Simulation

In order to verify the effectiveness of SMPCC, a model is built for simulation. The control diagram is shown in Figure 2. V_{saw} is the carrier signal of M1 and M4, V_e is modulated signal of M1 and M4, T_{on} is the time when M4 is turned on in a control cycle, T_{off} is the time when M4 is turned off in a control cycle.



Figure 2. The converter control algorithm. (a) The converter control diagram; (b) Pulse width modulation (PWM) algorithm; (c) The detailed block diagram of the proposed sliding mode predictive current control (SMPCC).

The parameters are: the capacitor C = 2 mF, the switching frequency $f = \frac{1}{T} = 10 \text{ kHz}$, the load resistance $R = 0.137 \Omega$ and parasitic resistance $r = 0.02 \Omega$, the inductor L = 0.1 mH, the input voltage $V_{in} = 630 \text{ V}$. Take $\lambda = \begin{bmatrix} 1 & 1 & 5 \end{bmatrix}^T$, $\delta = 200$, m = 10000, $\varepsilon = 1$, and the sampling period is 100 µs. Traditional PCC and SMPCC are simulated.

The gain of the state observer $K_C = 0.95$ in the simulation. In Figure 3, the steady-state current analysis shows the output reference current $i_{o,ref} = i_{L,ref} = 2300$ A and output control current of traditional PCC and SMPCC. The average output control current of traditional PCC is 2290 A, while that of SMPCC is 2299 A. The output control current of traditional PCC has a certain tracking error, while SMPCC can eliminate the steady-state error well, and it can accurately track the reference current. Through the comparative analysis of the simulation results, under ideal conditions, it can be found that after using SMPCC, the output control current steady-state error is reduced, so the steady-state performance is improved.



Figure 3. The simulation analysis of steady-state current.

The output reference current increases at 0.02 s from 2300 A to 3000 A when the input voltage is unchanged; Figure 4 shows the simulation. From Figure 4a, it can be found that when the output reference current increases suddenly, the system response will lag by one cycle, which is the inherent control delay. The output control current is quickly adjusted to 3000 A and into a steady state. The total adjustment time (rise time plus adjustment time) of SMPCC is shorter than that of traditional PCC, indicating that the controller of SMPCC has a faster response speed. From Figure 4b, it can be found that the output voltage of traditional PCC is adjusted quickly to 409 V, while the output voltage of SMPCC can be adjusted faster to the theoretical value, which is 411 V. The duty cycle change diagram of M1 and M4 in SMPCC is shown in Figure 4c. In order to keep the output of the controller stable, it can be found that a larger duty cycle is given at 0.02 s. The duty cycle will not change significantly after the output is stable, and with the system fluctuation, it will change in real time. The theoretical value of the duty cycle is smaller than the actual value because there is a certain voltage at the parasitic resistance. The difference between the output voltage obtained from the actual value of the duty cycle minus the voltage at the parasitic resistance is basically consistent with the actual output voltage.

When the input voltage is unchanged, Figure 5 shows the simulation waveform by tracking sinusoidal current $i_{o,ref} = i_{L,ref} = 2300sin(100\pi t)$ A as an example. It can be found from Figure 5 that the output control current smoothly reverses. The output reference current is sinusoidal when the input voltage is unchanged; Figure 5 shows the simulation. It can be found from Figure 5a that when the output reference current is sinusoidal, the response of the system will lag by one cycle, which is the inherent control delay. Soon, the output control current can change sinusoidally when the output reference current changes, while the controller of SMPCC has a faster response speed. From Figure 5b, it can be found that the output voltage of traditional PCC can also change sinusoidally soon when the output reference current changes, while the controller of SMPCC has a faster response speed as well. Figure 5c shows the duty cycle change diagram of M1 and M4 in SMPCC. It can be found from Figure 5c that when the output reference current is sinusoidal, the controller will give the duty cycle of sinusoidal change at this time due to the sinusoidal



change of output voltage, and with the system fluctuation, the duty cycle will change in real time.

Figure 4. The simulation when output reference current abruptly increases. (**a**) The output current changes; (**b**) The output voltage changes; (**c**) The duty cycle changes of M1 and M4 in SMPCC.



Figure 5. The simulation when the output reference current is sinusoidal. (**a**) The output current changes; (**b**) The output voltage changes; (**c**) The duty cycle changes of M1 and M4 in SMPCC.

Fast Fourier transform (FFT) analysis is conducted for the output current of the two methods. Figures 6a and 6b show total harmonic distortion (THD) analysis of the output

current waveform under traditional PCC and SMPCC, respectively. The fundamental wave content of the output current in Figure 6a is 2280 A, and the THD of the output current is 0.27%, while that in Figure 6b is 2294 A, and the THD of the output current is 0.27% as well. Comparing Figures 6a and 6b, it can be found that the output current fundamental wave content of SMPCC is closer to the output reference current value than that of traditional PCC, and the THD of SMPCC is the same as that of traditional PCC. Through the comparative analysis of the results, it can be found that under ideal conditions, the output current steady-state error is reduced, and the system's steady-state performance is improved after using SMPCC.





Figure 7 shows the amplitude response waveforms of the two control methods, and the output reference currents of both methods are suddenly applied from $i_{o,ref} = 2300sin(100\pi t)$ A to $i_{o,ref} = 3000sin(100\pi t)$ A when t = 0.025 s. Comparing traditional PCC and SMPCC, it can be found that after the sudden change of output reference current, the system response will lag by one cycle, which is the inherent control delay. It can be found that the output control current tracking time of traditional PCC is longer than that of SMPCC after the inherent switching cycle delay of the system. In conclusion, the current response of SMPCC is faster than that of traditional PCC, which is consistent with the dynamic performance analysis.



Figure 7. Simulation of a sudden increase in the output reference current amplitude. (**a**) Control current; (**b**) Current error.

The frequency response waveforms of the two control methods are shown in Figure 8, and the output reference currents of both methods are suddenly applied from

 $i_{o,ref} = 2300sin(100\pi t)$ A to $i_{o,ref} = 2300sin(120\pi t)$ A when t = 0.02 s. Comparing traditional PCC and SMPCC, it can be found that after the sudden change of output reference current, the system response will lag by one cycle, which is the inherent control delay. It can be found that the output control current tracking time of traditional PCC is longer than that of SMPCC after the inherent switching cycle delay of the system. In conclusion, the current response of SMPCC is faster than that of traditional PCC, which is consistent with the dynamic performance analysis.



Figure 8. Simulation of a sudden increase in the output reference current frequency. (**a**) Control current; (**b**) Current error.

To check the robustness of traditional PCC, Figure 9 shows the case where its true inductance value $(L + \Delta L)$ is different from the control value L (0.1 mH). Compared with the performance under ideal conditions, it can be observed from Figure 9a that the ripple of the inductor current becomes larger in the steady state when $\Delta L = -0.2$ L; in Figure 9b, the ripple of the inductor current becomes smaller when the inductor current is steady when $\Delta L = 0.2$ L. The simulation results are consistent with the theoretical analysis.



Figure 9. When the real inductance value is different from the control value. (a) Comparison of $\Delta L = -0.2$ L and $\Delta L = 0$; (b) Comparison of $\Delta L = 0.2$ L and $\Delta L = 0$.

To check the robustness of SMPCC, Figure 10 shows the case where its real inductance value $(L + \Delta L)$ is different from the control value L (0.1 mH). It can be seen that when $\Delta L = -0.2$ L, the inductor current ripple value of traditional PCC is 262 A, and the current ripple error becomes 53 A. Comparing the performance under ideal conditions, it can be found that the inductor current ripple value becomes larger when $\Delta L = -0.2$ L, and the change is obvious. Similarly, when $\Delta L = -0.2$ L, the inductor current ripple error becomes 2 A. It is also larger than the inductor current ripple value of SMPCC is 213 A, and the current ripple error becomes 2 A. It is also larger than the inductor current ripple value in the case of $\Delta L = 0$, but the change is smaller. Similarly, when $\Delta L = 0.2$ L, it can be found that the current ripple error of traditional PCC and SMPCC and the inductor current ripple value are larger than the ideal performance, but the impact on SMPCC is smaller. This shows that the inaccuracy of the inductor parameter has an impact

on the steady-state performance of traditional PCC and SMPCC, but its impact on SMPCC is much smaller than that of traditional PCC. When the inductor parameter is at -20% to 20% deviation, the steady-state performance of SMPCC can still achieve satisfactory results. This is because traditional PCC only relies on the system model of the converter, and it has a greater dependence on the inductance in the control delay compensation and current prediction, while in the control delay compensation, SMPCC adds a state observer to correct its prediction of the state at the (k + 1)th time, and it uses the combination of predictive control and variable structure scheme to greatly improve the accuracy of its model prediction. So far, the results show that in the same simulation environment, SMPCC performance is greater than that of traditional PCC.





Figure 10. When the real parasitic resistance value is different from the control value. (a) Comparison of $\Delta L = -0.2$ L and $\Delta L = 0$; (b) Comparison of $\Delta L = 0.2$ L and $\Delta L = 0$.

3.2. Experimental

To further verify the effectiveness of SMPCC, many experiments are carried out in the prototype, which is shown in Figure 11. The main control CPU is TMS320F28335 from TI. During the experiments, the sampling frequency is 10 kHz. The experimental parameters are: the capacitance $C = 2.5 \mu$ F, the load resistance $R = 13.7 \Omega$, the parasitic resistance $r = 0 \Omega$, the inductance L = 5 mH, the input voltage $V_{in} = 63$ V, other control parameters are the same as that in the simulation.



Figure 11. The single-phase H-bridge experimental prototype.

The results under traditional PCC are presented here for comparison. The steady-state waveforms under these two methods are shown in Figure 12, and the output reference

current is $i_{o,ref} = 2.3$ A. Figure 12a,b are the output currents of traditional PCC and SMPCC, respectively. The average output current of traditional PCC is 2.28 A, and the average output current of SMPCC is 2.30 A, which is equal to the output reference current. The results show that both traditional PCC and SMPCC can track the output reference current well. SMPCC can greatly reduce the steady-state current error because SMPCC has many advantages, such as anti-interference and insensitivity to parameter changes. Simulation results and experimental tests show that the output current amplitude of proposed SMPCC is very close to the expected control value, and the performance is satisfactory.



Figure 12. Steady-state response of single-phase H-bridge converter, V_o (10 V/div), I_o (1 A/div). (a) traditional PCC; (b) SMPCC.

In addition to the steady-state performance comparison, Figure 13 presents the dynamic response of traditional PCC and SMPCC to sudden changes in the output reference current. In Figure 13, the output reference current is suddenly added from $i_{o,ref} = 2.3$ A to $i_{o,ref} = 3$ A, Figure 13a is the output reference current response waveform under traditional PCC, and Figure 13b is the output reference current response waveform under SMPCC. Comparing the output current responses of the two control methods, it can be found that after the output reference current changes abruptly, the steady-state error of SMPCC is smaller than that of traditional PCC, and the corresponding speed is faster, which is consistent with the situation in the simulation.



Figure 13. Dynamic response when the output reference current changes suddenly, V_o (10 V/div), I_o (1 A/div). (a) traditional PCC; (b) SMPCC.

Figure 14 shows the dynamic response of traditional PCC and SMPCC when the output reference current is sinusoidal and the output reference current is $2.3sin(100\pi t)$ A. Figure 14a shows the output voltage and output control current response waveform under traditional PCC, and Figure 14b shows the output voltage and output control current response waveform under SMPCC. According to the output current response of the two control methods, the output control current of traditional PCC and SMPCC smoothly reverses when the output reference current is sinusoidal, which is consistent with the situation in the simulation.



Figure 14. Dynamic response when the output reference current is sinusoidal, V_o (10 V/div), I_o (1 A/div). (a) traditional PCC; (b) SMPCC.

Figure 15 shows the dynamic response of SMPCC when the amplitude of output reference current changes suddenly. After the amplitude of output reference current is suddenly added from $i_{o,ref} = 2.3sin(100\pi t)$ A to $i_{o,ref} = 3sin(100\pi t)$ A, SMPCC can quickly track the change of output reference current, which is consistent with the situation in the simulation.



Figure 15. Dynamic response when the amplitude of output reference current changes suddenly, I_o (1 A/div).

Figure 16 shows the dynamic response of SMPCC when the frequency of output reference current changes suddenly. After the frequency of output reference current is suddenly added from $i_{o,ref} = 2.3sin(100\pi t)$ A to $i_{o,ref} = 2.3sin(120\pi t)$ A, SMPCC can quickly track the change of output reference current, which is consistent with the situation in the simulation.



Figure 16. Dynamic response when the frequency of output reference current changes suddenly, I_o (1 A/div).

Through experimental and simulation analysis, the effectiveness of traditional PCC and proposed SMPCC is demonstrated. The proposed SMPCC dynamic performance is greater than that of traditional PCC. SMPCC also eliminates the steady-state error, and it is insensitive to system parameter changes. SMPCC has superior performance in practical applications.

4. Conclusions

This paper uses SMPCC to stabilize the output current quickly to the output reference current without an overshoot.

In order to reduce the control delay influence, a state observer is proposed in this paper. In view of the problem of steady-state error, a mathematical model is obtained for a renewable energy system, and the combination of traditional PCC and variable structure scheme eliminates the steady-state error in traditional PCC, and it reduces its sensitivity to inductance parameters.

Finally, the experimental and simulation results show that SMPCC has a good effect on renewable energy systems. The theoretical analysis in this paper is consistent with the experimental conclusions, which proves the effectiveness of SMPCC.

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